

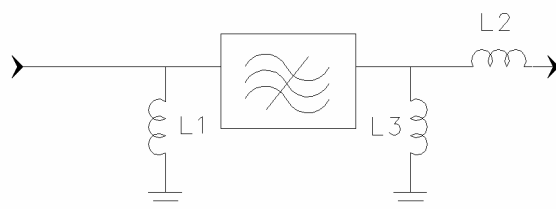
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	159.92	160	160.08
Insertion Loss	dB	-	22.5	24.5
1 dB Bandwidth	MHz	2.5	2.58	-
3 dB Bandwidth	MHz	-	2.73	-
40 dB Bandwidth	MHz	-	3.27	3.3
Amplitude Ripple($f_0 \pm 1.15\text{MHz}$)	dB	-	0.6	1
Group delay Variation($f_0 \pm 1.15\text{MHz}$)	nsec	-	230	300
Phase Linearity($f_0 \pm 1.15\text{MHz}$)	degree	-	3.5	-
Ultimate Rejection	dB	40	47	-
Absolute Delay	usec	-	4.55	4.6
Substrate Material		Quartz		
Ambient Temperature	°C	25		
Package Size		DIP3512 (35.0x12.8x4.7mm ³)		

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration



L1=15nH L2=47nH L3=27nH

Source/Load Impedance=50 ohm

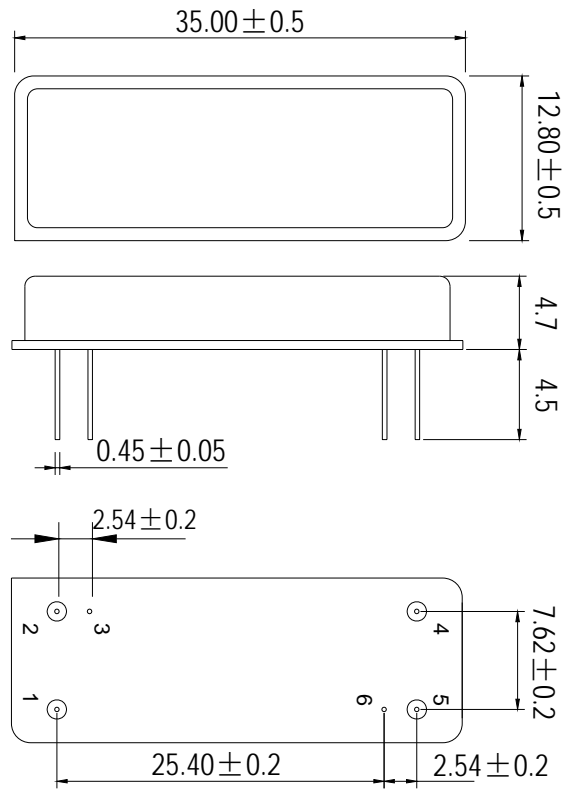
Notes - Component values may change depending on board layout.



SIPAT Co., Ltd.
(CETC No. 26 Research Institute)
Nanping Huayuan Road No. 14
Chongqing, China, 400060

Part Number	SP LBS16018		
Rev. Date	2004-12-6		
Rev.	1.0	Page 1/4	

Package Dimension

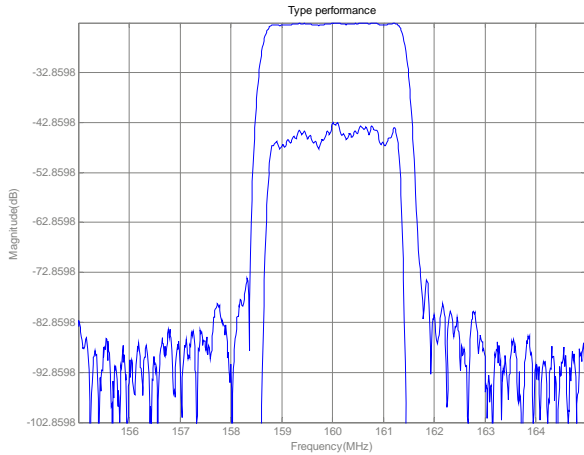


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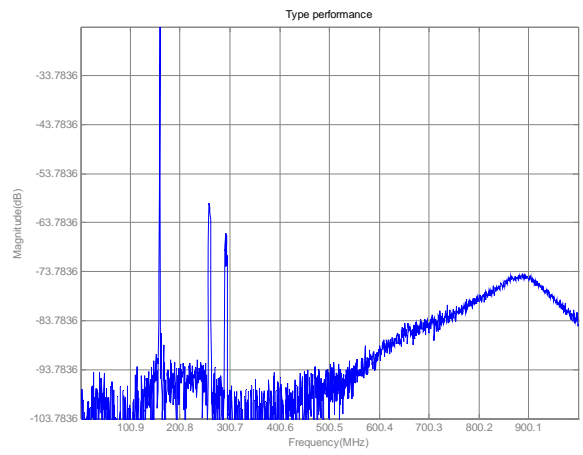
Part Number	SP LBS16018	
Rev. Date	2004-12-6	
Rev.	1.0	Page 2/4

Typical Performance

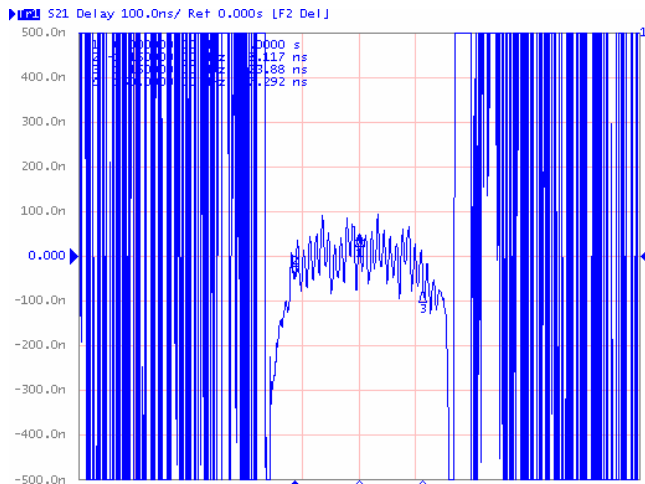
Frequency Respond



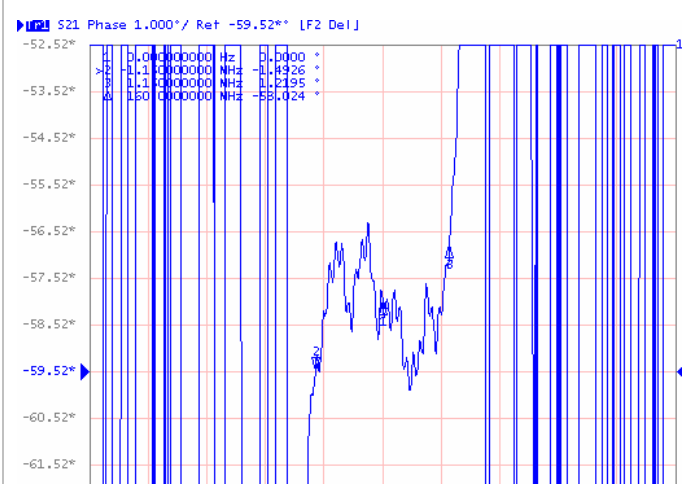
Wideband Respond



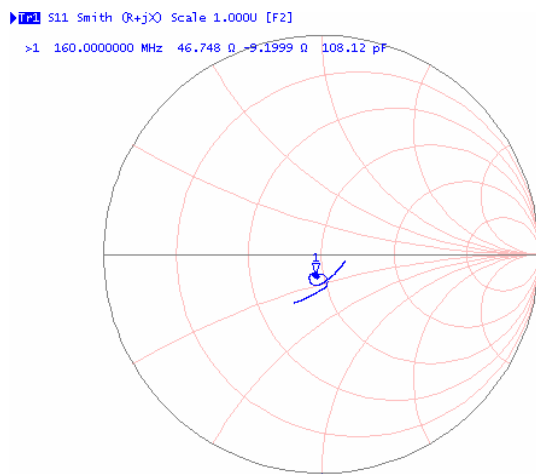
Group delay ($f_0 \pm 1.15\text{MHz}$)



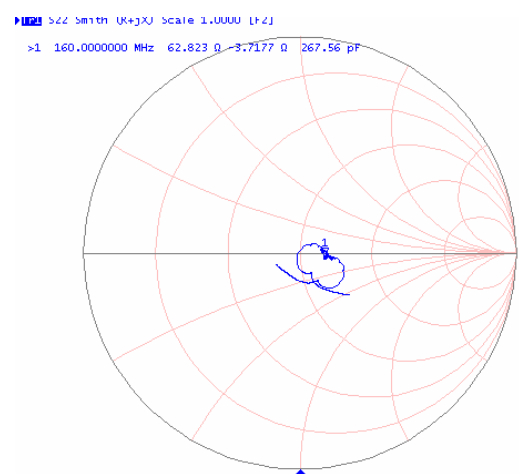
Phase Linearity ($f_0 \pm 1.15\text{MHz}$)



Smith Chart S11



Smith Chart S22



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Page 3/4

Marking

LBS16018

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		Rev. Date	2004-12-6	
		Rev.	1.0	Page 4/4