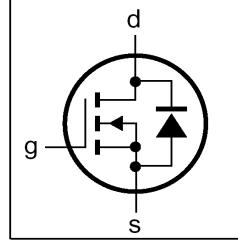


- Ease of Parallelizing
- Fast Switching
- Simple Drive Requirements



BV_{DSS}	200V
R_{DS(ON)}	2.0Ω
I_D	0.65A

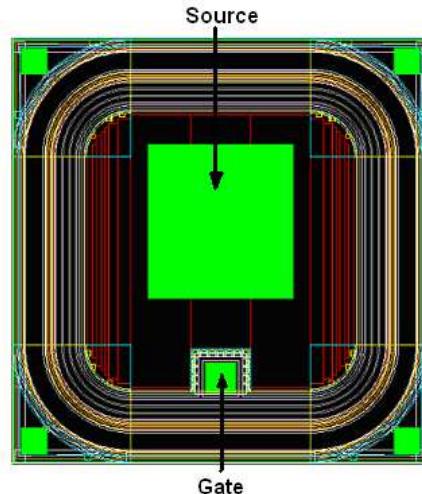
Description

This advanced high voltage MOSFET is produced using Belling's proprietary DMOS technology. Designed for high efficiency logic level circuit.

- **Die size with scribe line** 1570μm X 1570μm
Scribe line 80um
- **Die Thickness** 300± 20um
- **Metallization**

Top	Al
Bottom	Ti / Ni / Ag
- **Bonding Pad Size**

Gate	140μm X 102μm
Source	540μm X 540μm
- **Passivation**



Electrical Characteristics (T_C=25C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	200	-	-	V
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =10V, I _D =0.65A	-	-	2.0	Ω
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =400uA	0.5	-	1.8	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =200V, V _{GS} =0V	-	-	0.1	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} = 20V	-	-	10	nA
V _{SD}	Forward On Voltage	V _{GS} =0V, I _S =0.65A	-	-	1.2	V