

FC SERIES DISTRIBUTOR

DATA SHEET

PXM

This distributor is to be installed for each group of FC/FCX series transmitter and receiving instruments, compact controllers and so on combined therewith.

While receiving power from a system power supply, the distributor functions to provide DC power to a transmitter and convert current signal from the transmitter into voltage signal of 1 to 5V DC with a precision resistor for distribution to individual receiving instruments.

FEATURES

1. The terminal structure allows easy connection of an external cable, and the transmitters of up to 4 loops are connectable to one distributor.
2. A current limiting circuit is provided for each loop, and an arrester can be built in at option.
3. An output backup function can be built in for prevention of short circuit faults.



SPECIFICATIONS

Power supply voltage:

25.8V DC(supplied from power supply PXJ)

Input signal:

4 to 20mA DC (input resistance 250Ω)
4 to 20mA DC for reception
(without transmitter power supply, 250Ω built in)

Capacity:

4 loops maximum

Transmitter power supply voltage:

24V DC

Output signal:

- 1 to 5V DC(output resistance 250Ω) without output buffer amplifier function
- 1 to 5V DC(output resistance 250Ω) with output buffer amplifier function - - 1 point per loop
- 1 to 5V DC(allowable load resistance 10kΩ or more) - - 2 points per loop

Note: Output buffer not provided for 0₊ terminal with short circuit bar.

Accuracy:

±0.1%. (versus full span with input signal as reference)

Ambient temperature:

0 to 45°C

Ambient humidity:

90% RH or less

Mass{weight}:

Approx. 1kg

Intrinsically safe explosionproofing:

Usable as a distributor for zener barrier (PWZB1).

External dimensions (H x W x D):

200 x 80 x 91.5 mm

Finish color:

Case; black (synthetic resin molding)
Cover; semi-transparent (synthetic resin molding)

Option:

Arrester can be built in at option.

CODE SYMBOLS

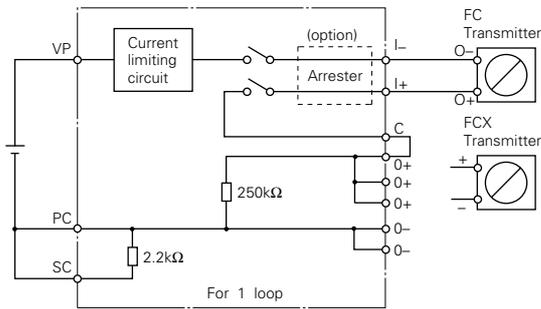
1 2 3 4 5 6 7 8 9								
P	X	M						2
Description								
Application		Input signal		(*1) Output buffer	Arrester			
A								
B								
C								
D								
G								
H								
J								
K								
L								
P								
1st loop (*3)		4 to 20mA for general application 4 to 20mA for reception only		None None	None None			
2nd loop (*3)		4 to 20mA for general application 4 to 20mA for reception only		None None	None None			
3rd loop (*3)		4 to 20mA for general application 4 to 20mA for reception only		None None	None None			
4th loop (*3)		4 to 20mA for general application 4 to 20mA for reception only		None None	None None			
0								
1								
2								
3								

- Notes: * (1) Combined specification of with and without output buffer cannot be supplied.
 (2) Specify the 9th digit so that it matches the 4th, 5th, 6th and 7th digits.
 (3) Specify G or P in the 4th, 5th, 6th and 7th digits for combination with a zener barrier (PWZB1).
 (4) An arrester cannot be provided when the distributor is for zener barrier connection.

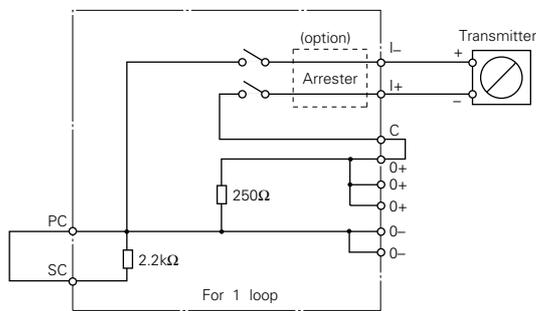
EXAMPLE OF SYSTEM CONFIGURATION

1. Without output buffer

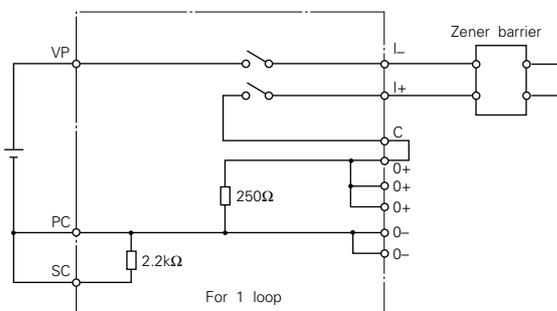
(a) 4 to 20mA input, for general application



(b) 4 to 20mA input, for reception only

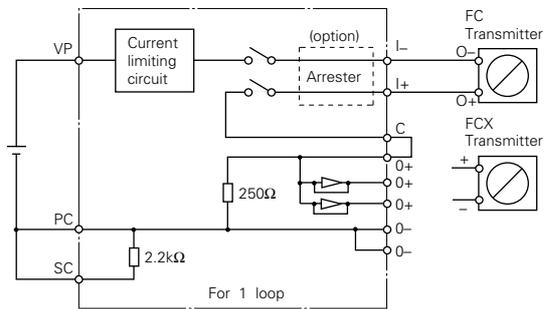


(c) For zener barrier connection

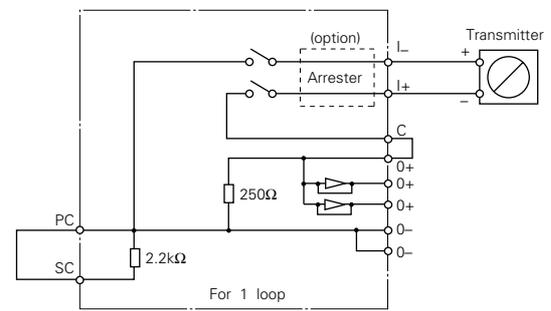


2. With output buffer

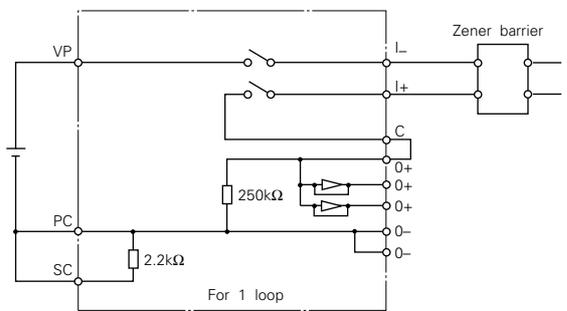
(a) 4 to 20mA input, for general application



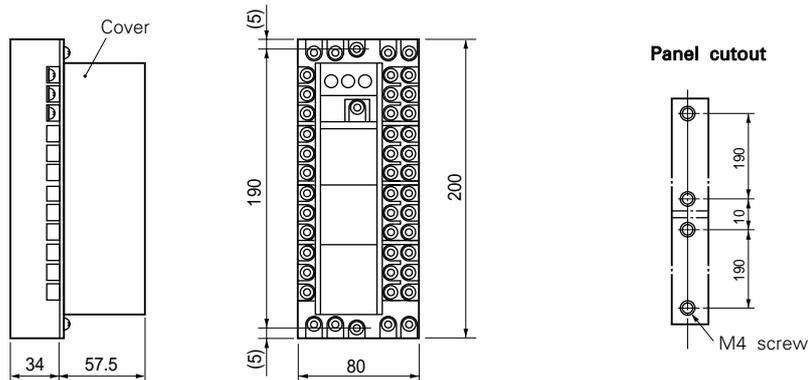
(b) 4 to 20mA input, for reception only



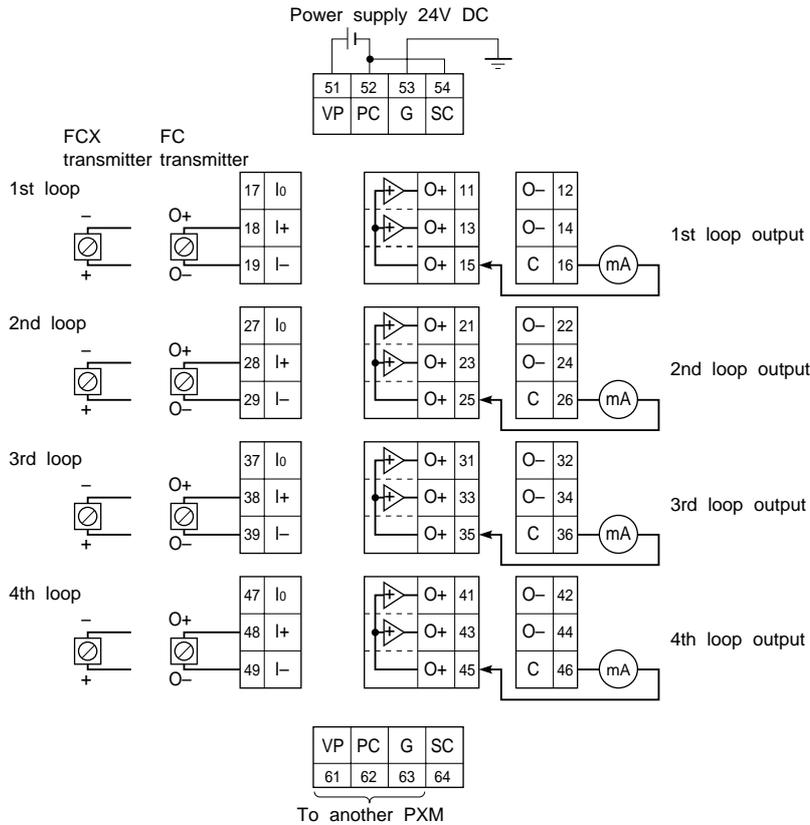
(c) For zener barrier connection



OUTLINE DIAGRAM (Unit:mm)



CONNECTION DIAGRAM



Note: 11 and 13 are connected to 15 output terminal with buffer amplifier, 21 and 23 to 25, 31 and 33 to 35, and 41 and 43 to 45 respectively.

RELATED DEVICES

System power supply unit PXJ
Power supply unit PXL

ORDERING INFORMATION

1. Product name
2. Type

⚠ Caution on Safety

*Before using this product, be sure to read its instruction manual in advance.

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