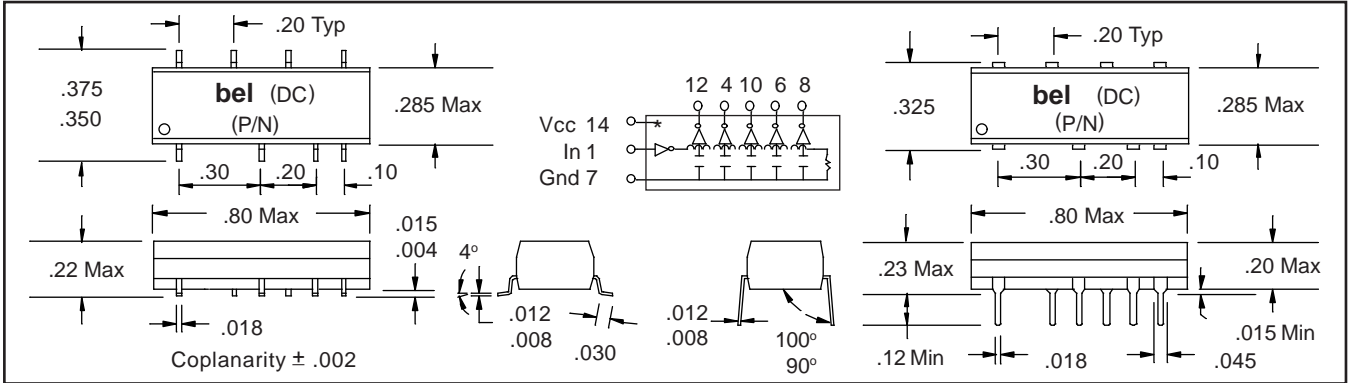


5 TAP LEADING EDGE CONTROL LOW VOLTAGE DELAY MODULES

Cat 30-R2



Part Numbers

SMD	Thru-Hole	Total Delay	Delay per Tap	Rise Time
S477-0025-02	A477-0025-02	25 ns	5 ns	3 ns
S477-0030-02	A477-0030-02	30 ns	6 ns	3 ns
S477-0040-02	A477-0040-02	40 ns	8 ns	3 ns
S477-0050-02	A477-0050-02	50 ns	10 ns	3 ns
S477-0060-02	A477-0060-02	60 ns	12 ns	3 ns
S477-0070-02	A477-0070-02	70 ns	14 ns	3 ns
S477-0080-02	A477-0080-02	80 ns	16 ns	3 ns
S477-0090-02	A477-0090-02	90 ns	18 ns	3 ns
S477-0100-02	A477-0100-02	100 ns	20 ns	3 ns
S477-0125-02	A477-0125-02	125 ns	25 ns	3 ns
S477-0150-02	A477-0150-02	150 ns	30 ns	3 ns
S477-0200-02	A477-0200-02	200 ns	40 ns	3 ns
S477-0250-02	A477-0250-02	250 ns	50 ns	3 ns

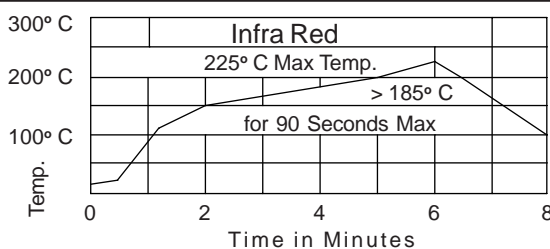
Tolerances

Input to Taps ± 2 ns or 5% , Whichever is Greater
 Tap to Tap ± 2 ns or 7%, Whichever is Greater
 Delays measured @ 50% levels on Leading Edge only with no loads on Taps
 Rise and Fall Times measured from 10% to 90% levels

Drive Capabilities

Nh	Logic 1 Fanout	-	10 LSTTL Loads Max.
Nl	Logic 0 Fanout	-	10 LSTTL Loads Max.

Recommended Temperature Profile



Test Conditions @ 25°C

Ein	Pulse Voltage	3.0 Volts
Trin	Rise Time	3.0 ns (10%-90%)
PW	Pulse Width	1.2 x Total Delay
PP	Pulse Period	4 x Pulse Width
Iccl	Supply Current	25 ma Typical
Vcc	Supply Voltage	3.3 Volts

Electrical Characteristics

	Min.	Max.	Units
Vcc	3.0	3.6	V
Vih	2.4		V
Vil		0.8	V
Iik		-20	ma
Ioh		20	ma
Iol		-20	ma
Voh	2.9		V
Vol		0.1	V
Vik		-0.5	V
Iih		1	ua
Iil		-1	ua
Icch		10	ma
Iccl		30	ma
Ta	0°	70°	C
PW	40		%
d		50	%
Tc	Temp. Coeff. of Total Delay (TD) 100 + (25000/TD) PPM/°C		

Notes

Transfer molded for better reliability
 Compatible with TTL & CMOS circuits
 Terminals: Electro-Tin plate phosphor bronze
 Performance warranty is limited to specified parameters listed
 SMD - Tape & Reel available:
 32mm Wide x 16mm Pitch, 500 pieces per 13" reel

Other Delays and Tolerances Available Consult Sales

Specifications subject to change without notice.

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