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# **2SK1151(L)(S), 2SK1152(L)(S)**

Silicon N-Channel MOS FET

**HITACHI**

November 1996

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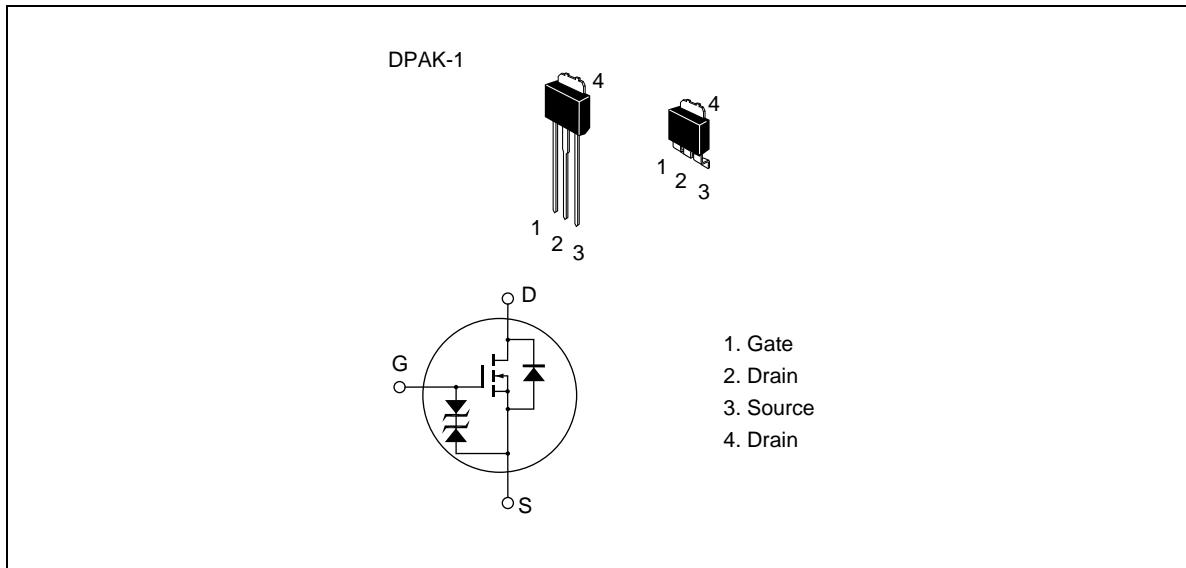
## **Application**

High speed power switching

## **Features**

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator and DC-DC converter

## **Outline**



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### **Absolute Maximum Ratings (Ta = 25°C)**

Item		Symbol	Ratings	Unit
Drain to source voltage	2SK1151	V <sub>DSS</sub>	450	V
	2SK1152		500	
Gate to source voltage		V <sub>GSS</sub>	±30	V
Drain current		I <sub>D</sub>	1.5	A
Drain peak current		I <sub>D(pulse)</sub> <sup>*1</sup>	6	A
Body to drain diode reverse drain current		I <sub>DR</sub>	1.5	A
Channel dissipation		Pch <sup>*2</sup>	20	W
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature		T <sub>stg</sub>	-55 to +150	°C

Note 1. PW ≤ 10 µs, duty cycle ≤ 1%

2. Value at T<sub>c</sub> = 25°C

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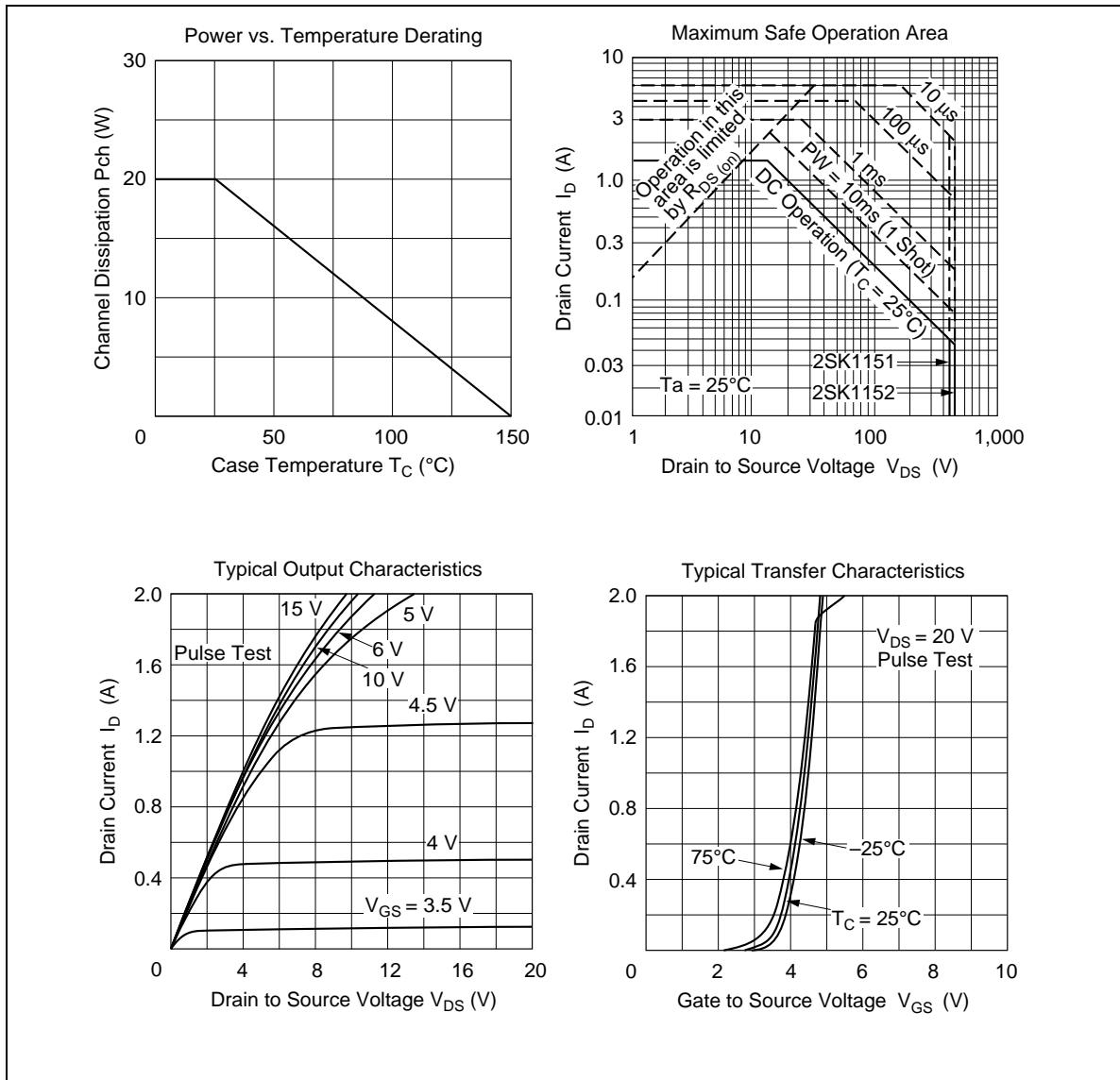
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### Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	2SK1151 V <sub>(BR)DSS</sub> 2SK1152	450 500	—	—	V	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0
Gate to source breakdown voltage	V <sub>(BR)GSS</sub>	±30	—	—	V	I <sub>G</sub> = ±100 μA, V <sub>DS</sub> = 0
Gate to source leak current	I <sub>GSS</sub>	—	—	±10	μA	V <sub>GS</sub> = ±25 V, V <sub>DS</sub> = 0
Zero gate voltage drain current	2SK1151 I <sub>DSS</sub> 2SK1152	—	—	100	μA	V <sub>DS</sub> = 360 V, V <sub>GS</sub> = 0 V <sub>DS</sub> = 400 V, V <sub>GS</sub> = 0
Gate to source cutoff voltage	V <sub>GS(off)</sub>	2.0	—	3.0	V	I <sub>D</sub> = 1 mA, V <sub>DS</sub> = 10 V
Static Drain to source on stateresistance	2SK1151 R <sub>DS(on)</sub> 2SK1152	—	3.5 4.0	5.5 6.0	Ω	I <sub>D</sub> = 1 A, V <sub>GS</sub> = 10 V * <sup>1</sup>
Forward transfer admittance	y <sub>fs</sub>	0.6	1.1	—	S	I <sub>D</sub> = 1 A, V <sub>DS</sub> = 20 V * <sup>1</sup>
Input capacitance	C <sub>iss</sub>	—	160	—	pF	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0,
Output capacitance	C <sub>oss</sub>	—	45	—	pF	f = 1 MHz
Reverse transfer capacitance	C <sub>rss</sub>	—	5	—	pF	
Turn-on delay time	t <sub>d(on)</sub>	—	5	—	ns	I <sub>D</sub> = 1 A, V <sub>GS</sub> = 10 V,
Rise time	t <sub>r</sub>	—	10	—	ns	R <sub>L</sub> = 30 Ω
Turn-off delay time	t <sub>d(off)</sub>	—	20	—	ns	
Fall time	t <sub>f</sub>	—	10	—	ns	
Body to drain diode forward voltage	V <sub>DF</sub>	—	1.0	—	V	I <sub>F</sub> = 1.5 A, V <sub>GS</sub> = 0
Body to drain diode reverse recovery time	t <sub>rr</sub>	—	220	—	ns	I <sub>F</sub> = 1.5 A, V <sub>GS</sub> = 0, di <sub>F</sub> /dt = 100 A/μs

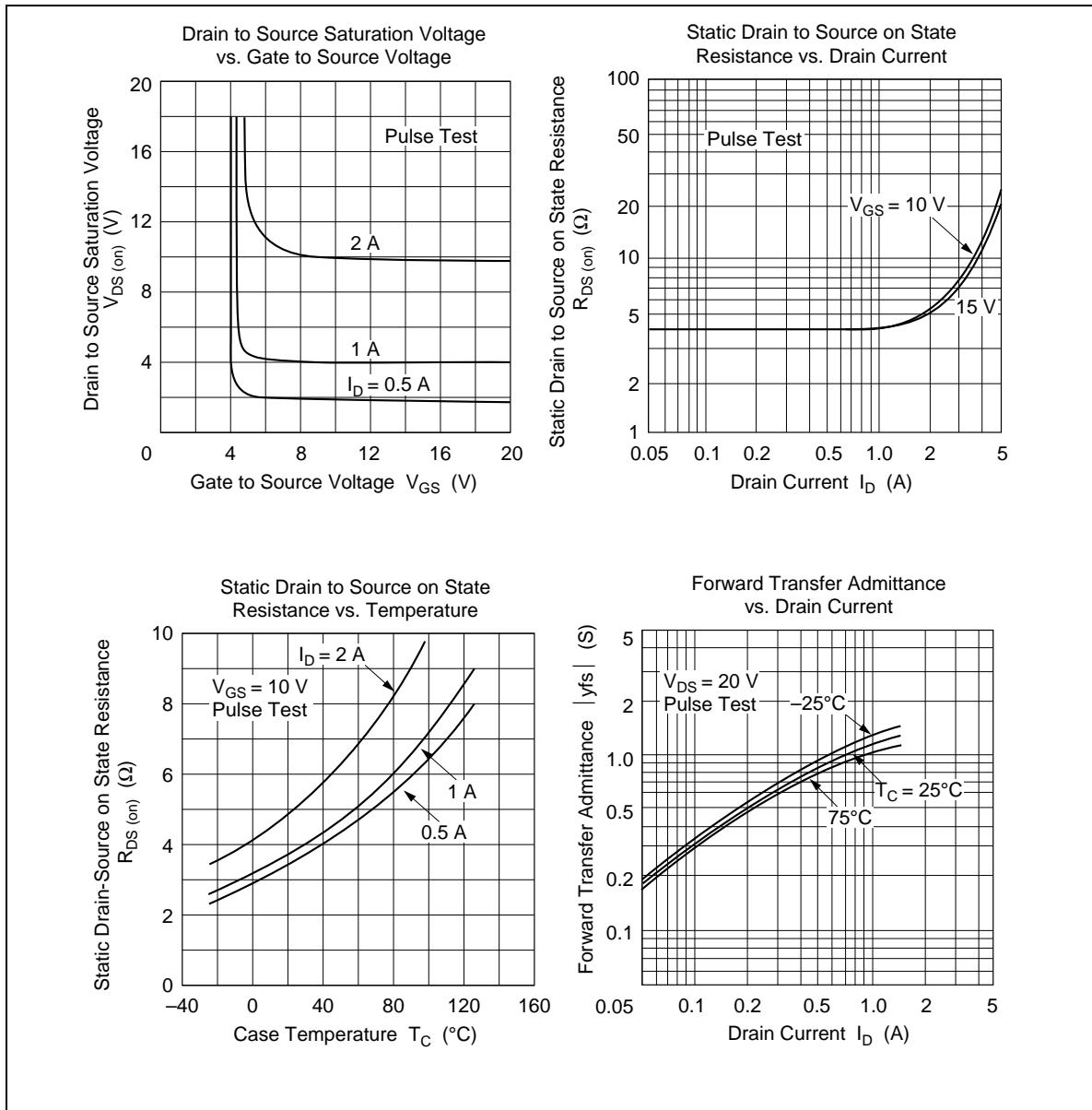
Note 1. Pulse test

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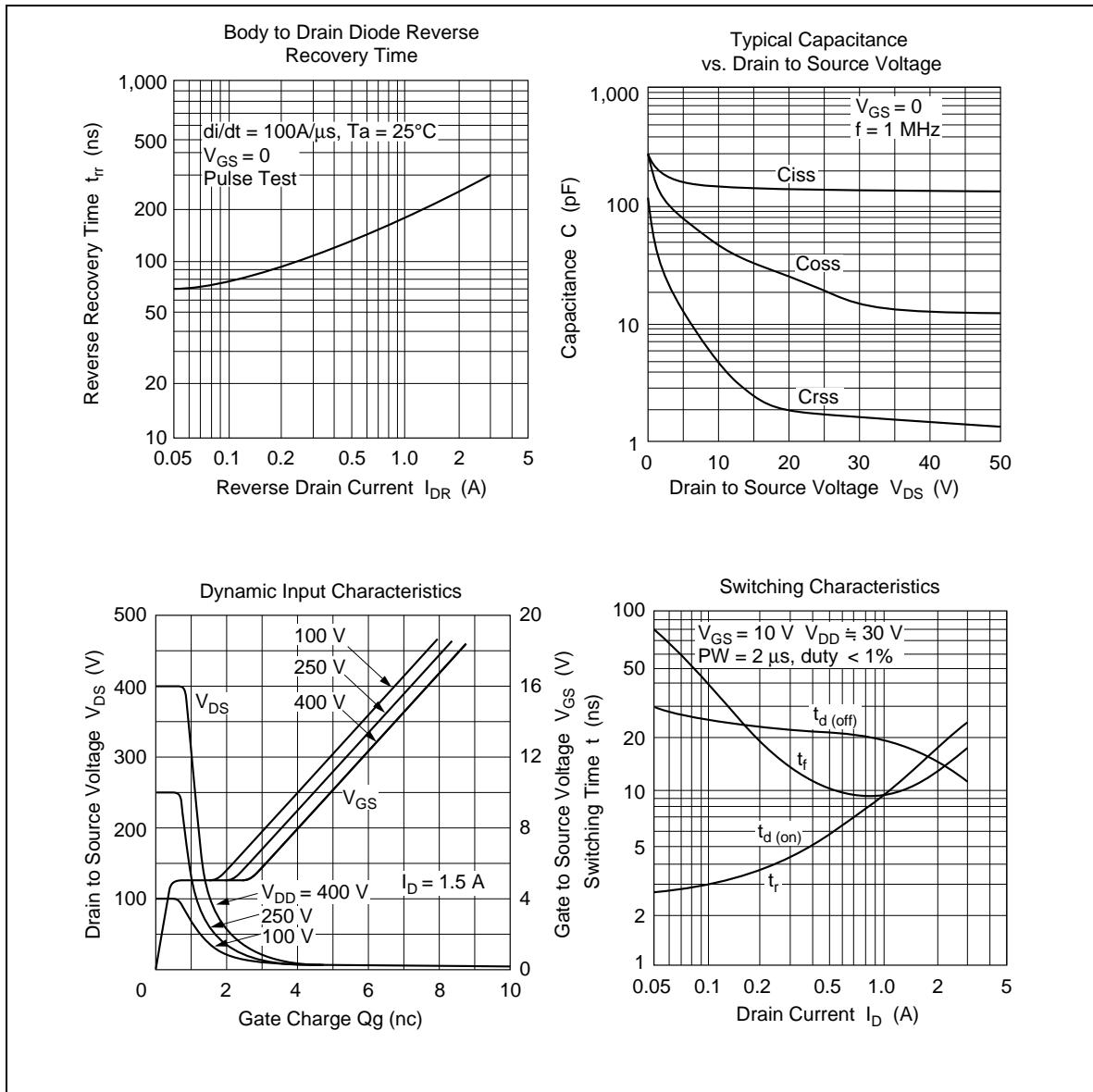
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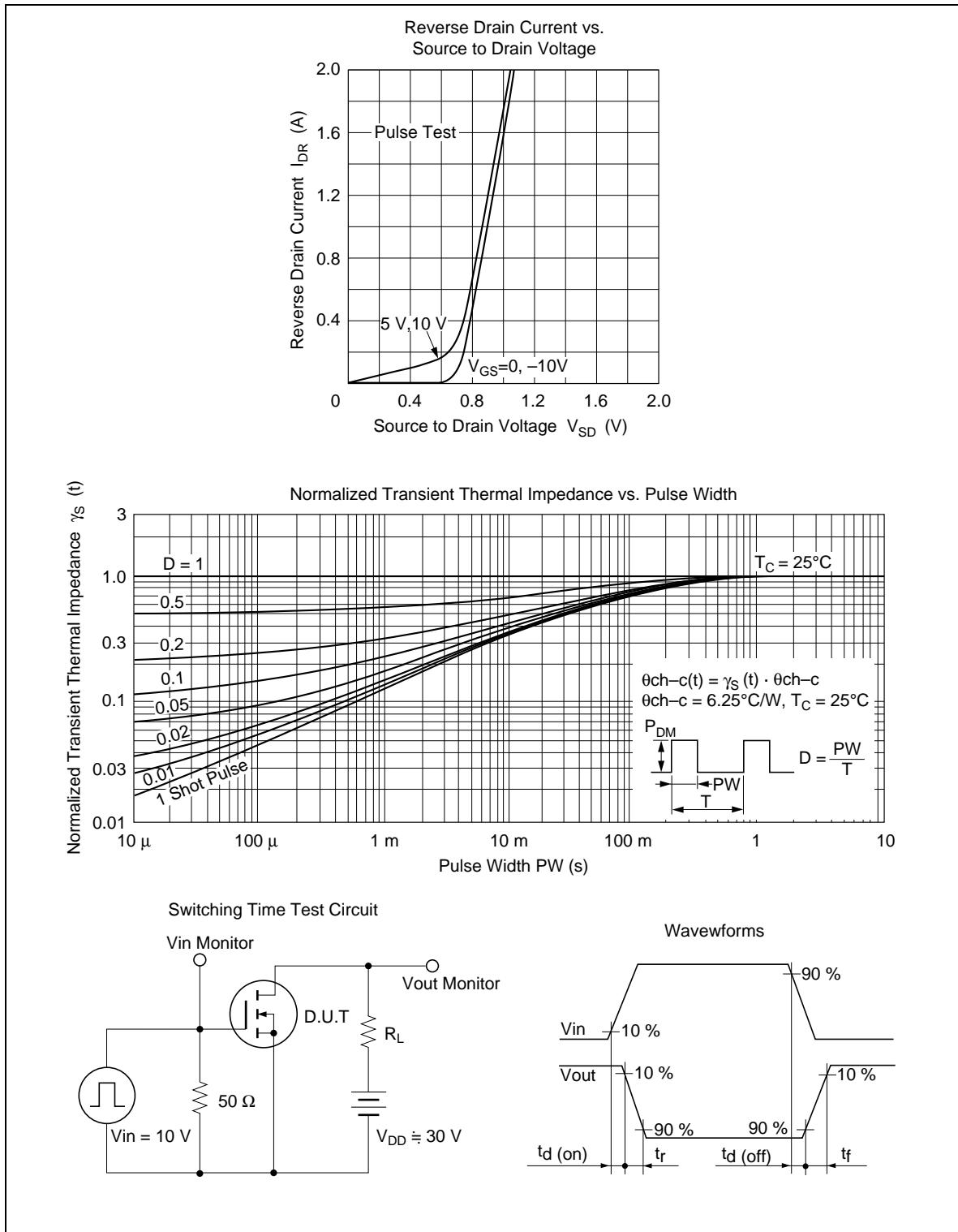
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