ABSOLUTE MAXIMUM RATINGS

Characteristic	Symbol	Value	Unit
Supply Voltage	V_{CC}	±22	V
Differential input voltage	$V_{I(DIFF)}$	±18	V
Power Dissipation	P_D	400	mW
Input Voltage	V_I	±15	V
Operating Temperature	T_{OPR}	0~+70	°C
Storage Temperature	T_{STG}	-65~+150	°C

ELECTRICAL CHARACTERISTICS(Ta=25°C ,Vcc=15V,Vee=-15V)

Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Supply Current	Icc			3.5	5.6	mA
Input offset voltage	V_{IO}	Rs<10kΩ		2	6	mV
Input offset current	I_{IO}			5	200	nA
Input bias current	I_{BIAS}			30	500	nA
Large signal voltage gain	Gv	Vo(p-p)=10V,R_L<2kΩ	20	200		V/mV
Commom Mode Input Voltage Range	$V_{I(R)}$		±12	±13		V
Commom Mode Rejection Ratio	CMRR	Rs<10kΩ	70	90		dB
Supply Voltage Rejection Ratio	PSRR	Rs<10kΩ	76	90		dB
Output Voltage swing	Vo(p-p)	R_L>10kΩ		±12	±14	V
Power Comsumption	Pc			70	170	mV
Slew Rate	SR	Vi=10V,R_L>2kΩ,C_L<100pF	1.2			V/μs
Rise Time	T_{RIS}	Vi=20mV,R_L>2kΩ, C_L<100pF		0.3		μs
Overshoot	OS	Vi=20mV,R_L>2kΩ,C_L<100pF		15		%

TYPICAL PERFORMANCE CHARACTERISTICS

Fig.1 Positive output voltage swing vs Load resistance

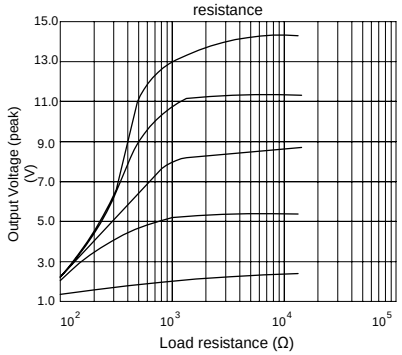


Fig.2 Positive output voltage swing vs Load resistance

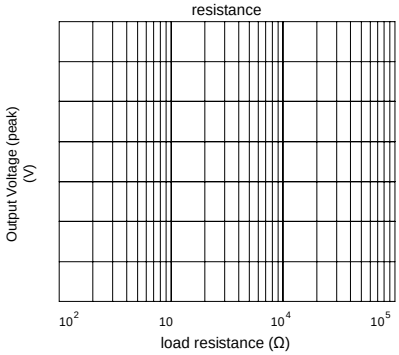
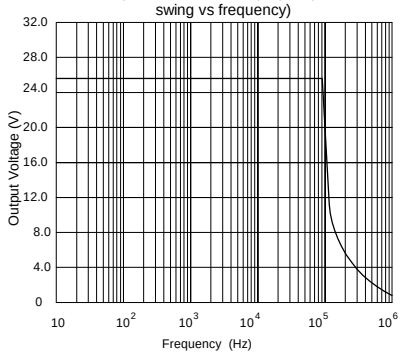


Fig.3 Power bandwith(large signal swing vs frequency)



TYPICAL PERFORMANCE CHARACTERISTICS

Fig.1 Positive output voltage swing vs Load resistance

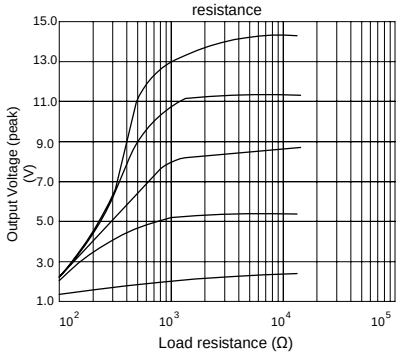


Fig.2 Positive output voltage swing vs Load

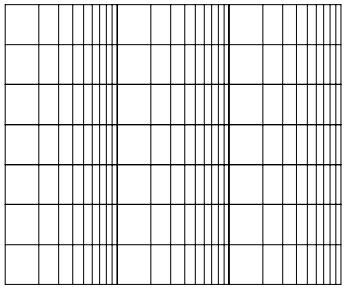


Fig.3 Power bandwith(large signal swing vs frequency)

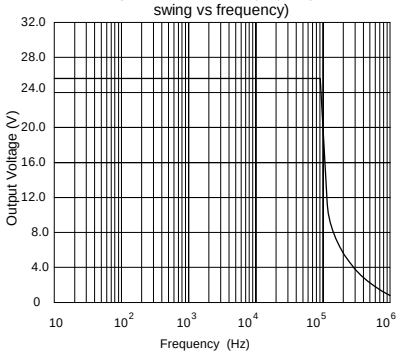


Fig. 4 Burst Noise vs Rs

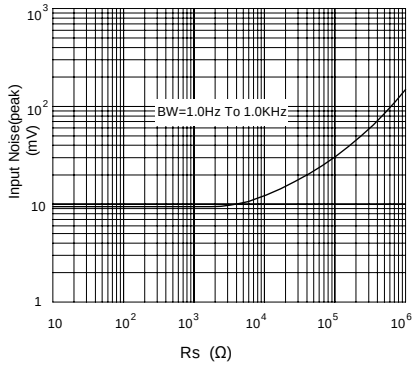


Fig. 5 RMS Noise vs Rs

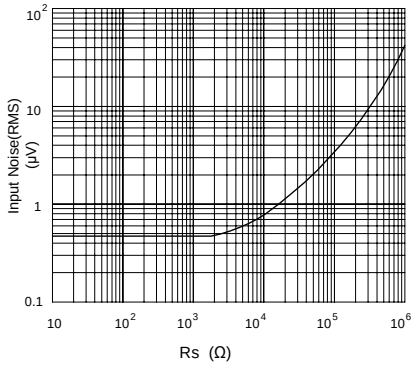


Fig. 6 Output Noise vs Rs

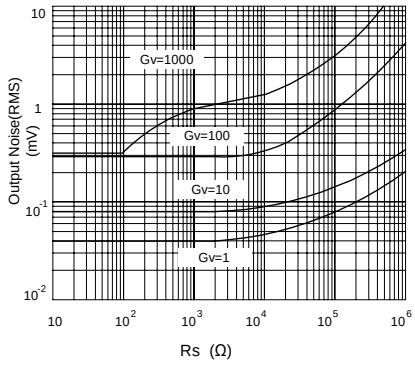


Fig. 7 Spectral Noise Density

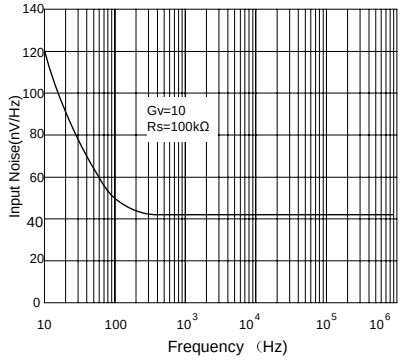


Fig. 8 Open loop frequency response

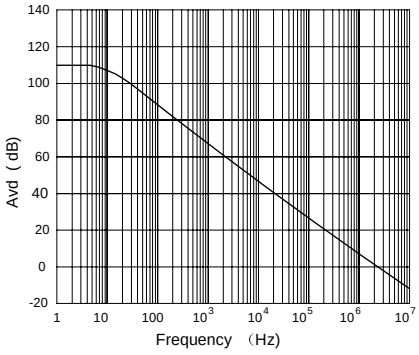
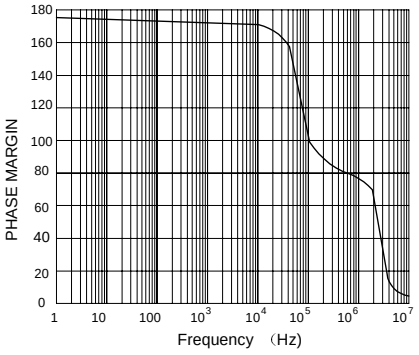
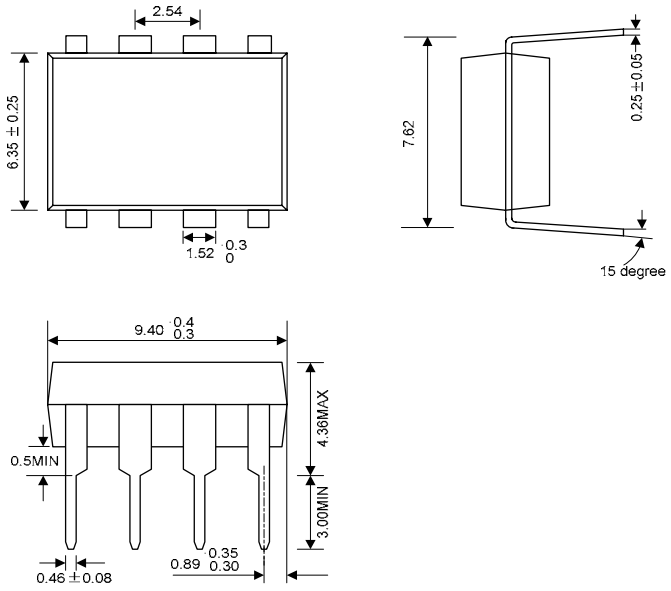


Fig. 9 PHASE MARGIN vs FREQUENCY

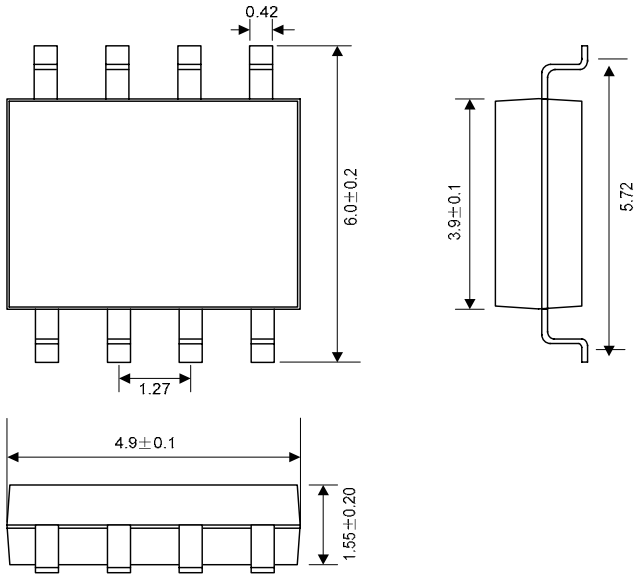


PACKAGE DIMENSIONS

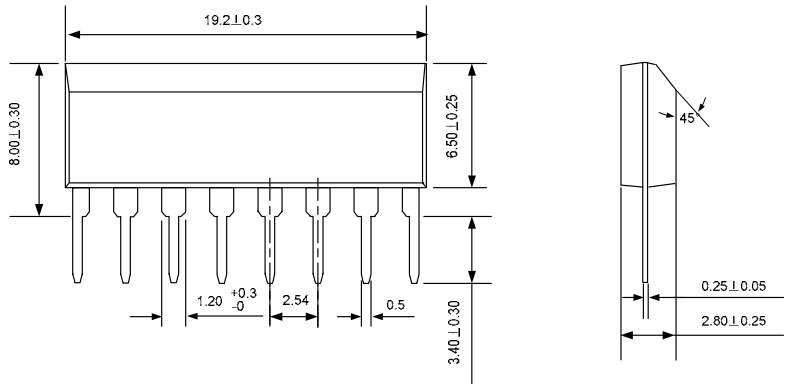
DIP-8-300-2.54



SOP-8-225-1.27



SIP-8-2.54 UNIT: mm



Attach

Revision History

Data	REV	Description	Page
	1.0	Original	
2003.10.23	1.1	Add “OREDRING INFORMATION”	1
2005.3.17	1.2	Change “UTC4558 To UTC4558/E”	1
		Revise “Package Dimensions”	5
2009.11.10	1.3	Add”SIP-8-2.54” 1,6	