

Philips Components

Document No.	853-0685
ECN No.	99799
Date of Issue	June 14, 1990
Status	Product Specification
ECL Products	

10189 Inverter

Hex Inverter with Enable

FEATURES

- Typical propagation delay: 2.0ns
- Typical supply current ($-I_{EE}$): 30mA

DESCRIPTION

The 10189 includes six inverters offering individual inputs and outputs and a common enable input, driving all outputs Low. Each input is connected to V_{EE} via a pull-down resistor resulting in high input impedance and eliminating the need for tying unused inputs Low.

Due to open emitter outputs, the 10189 features OR capability with high fan-out for driving 50Ω lines.

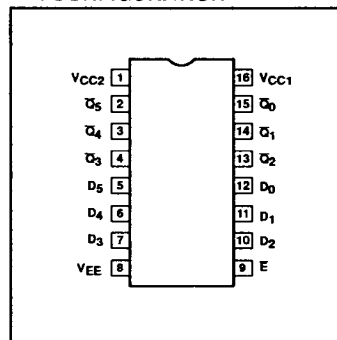
ORDERING INFORMATION

DESCRIPTION	ORDER CODE
16-Pin Plastic DIP	10189N
16-Pin Ceramic DIP	10189F

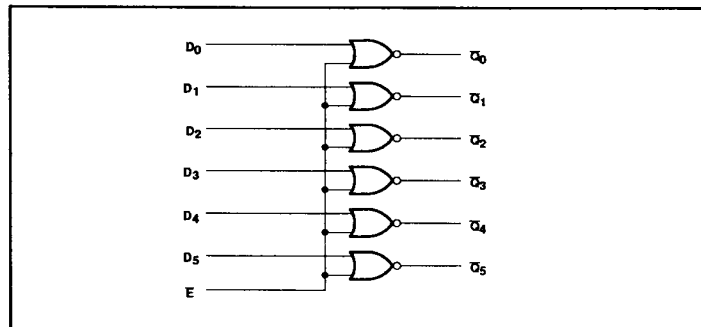
PIN DESCRIPTION

PINS	DESCRIPTION
$D_0 - D_5$	Data Inputs
$\bar{E}$	Common Enable Input
$\bar{Q}_0 - \bar{Q}_5$	Data Outputs

PIN CONFIGURATION



LOGIC DIAGRAM



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ABSOLUTE MAXIMUM RATINGS

SYMBOL	PARAMETER		LIMITS	UNIT
V _{EE}	Supply voltage		−8.0	V
V _{IN}	Input voltage (V _{IN} should never be more negative than V _{EE})		0 to V _{EE}	V
I _O	Output source current (continuous)		−50	mA
T _S	Storage temperature range		−55 to +150	°C
T _J	Maximum junction temperature	Ceramic Package	+165	°C
		Plastic Package	+150	°C

NOTE:
Operation beyond the limits set forth in this table may impair the useful life of the device. Unless otherwise noted, these limits are specified over the operating ambient temperature range.

DC OPERATING CONDITIONS

SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			UNIT
			MIN.	NOM.	MAX.	
V _{CC1} , V _{CC2}	Circuit ground		0	0	0	V
V _{EE}	Supply voltage (negative)			−5.2		V
V _{IH}	High level input voltage	T _A = −30°C			−890	mV
		T _A = +25°C			−810	mV
		T _A = +85°C			−700	mV
V _{IHT}	High level input threshold voltage	T _A = −30°C	−1205			mV
		T _A = +25°C	−1105			mV
		T _A = +85°C	−1035			mV
V _{ILT}	Low level input threshold voltage	T _A = −30°C			−1500	mV
		T _A = +25°C			−1475	mV
		T _A = +85°C			−1440	mV
V _{IL}	Low level input voltage	T _A = −30°C	−1890			mV
		T _A = +25°C	−1850			mV
		T _A = +85°C	−1825			mV
T _A	Operating ambient temperature range		−30	+25	+85	°C

NOTE:
When operating at other than the specified V_{EE} voltage (−5.2V), the DC and AC Electrical Characteristics will vary slightly from specified values.

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DC ELECTRICAL CHARACTERISTICS $V_{CC1} = V_{CC2} = \text{ground}$, $V_{EE} = -5.2V \pm 0.010V$, $T_A = -30^\circ\text{C}$ to $+85^\circ\text{C}$ output loading 50Ω to $-2.0V \pm 0.010V$ unless otherwise specified^{1,3}

SYMBOL		PARAMETER		TEST CONDITIONS ²		LIMITS			UNIT
						MIN.	TYP.	MAX.	
V _{OH}	High level output voltage	T _A = −30°C	Apply V _{ILMIN} to all inputs.	−1060		−890	mV		
		T _A = +25°C		−960		−810	mV		
		T _A = +85°C		−890		−700	mV		
V _{OHT}	High level output threshold voltage	T _A = −30°C	Apply V _{ILT} to each D _n input, one at a time, with V _{ILMIN} applied to E input and V _{IHMAX} applied to all other inputs.	−1080			mV		
		T _A = +25°C		−980			mV		
		T _A = +85°C		−910			mV		
V _{OLT}	Low level output threshold voltage	T _A = −30°C	Apply V _{IHT} to each D _n input, one at a time, with V _{ILMIN} applied to E input and V _{ILMIN} applied to all other inputs.			−1655	mV		
		T _A = +25°C				−1630	mV		
		T _A = +85°C				−1595	mV		
V _{OL}	Low level output voltage	T _A = −30°C	Apply V _{IHMAX} to all D _n inputs with V _{ILMIN} applied to E input.	−1890		−1675	mV		
		T _A = +25°C		−1850		−1650	mV		
		T _A = +85°C		−1825		−1615	mV		
I _{IH}	High level input current	Other inputs	T _A = −30°C	Apply V _{IHMAX} to each input under test, one at a time, with V _{ILMIN} applied to all other inputs.			425	μA	
			T _A = +25°C				265	μA	
			T _A = +85°C				265	μA	
		E input	T _A = −30°C	Apply V _{IHMAX} to E input with V _{ILMIN} applied to all other inputs.			890	μA	
			T _A = +25°C				555	μA	
			T _A = +85°C				555	μA	
I _{IL}	Low level input current	T _A = −30°C	Apply V _{ILMIN} to each input under test, one at a time, with V _{IHMAX} applied to all other inputs.	0.5			μA		
		T _A = +25°C		0.5			μA		
		T _A = +85°C		0.3			μA		
−I _{EE}	V _{EE} supply current	T _A = −30°C				44	mA		
		T _A = +25°C			30	40	mA		
		T _A = +85°C				44	mA		
$\frac{\Delta V_{OH}}{\Delta V_{EE}}$	High level output voltage compensation	T _A = +25°C				0.016		V/V	
$\frac{\Delta V_{OL}}{\Delta V_{EE}}$	Low level output voltage compensation					0.250		V/V	
$\frac{\Delta V_{BB}}{\Delta V_{EE}}$	Reference bias voltage compensation					0.148		V/V	

NOTES:

- The specified limits represent the worst case values for the parameter. Since these worst case values normally occur at the supply voltage and temperature extremes, additional noise immunity can be achieved by decreasing the allowable operating condition ranges.
- Conditions for testing shown in the tables are not necessarily worst case. For worst case testing guidelines, refer to DC Testing, Chapter 1, Section 3.
- The specified limits shown in the DC Electrical Characteristics table can be met only after thermal equilibrium has been established. Thermal equilibrium is established by applying power for at least 2 minutes, while maintaining transverse airflow of 2.5 meters/sec (500 linear feet/min) over the device, mounted either in a test socket or on a printed circuit board. Test voltage values are given in the DC Operating Conditions table.

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AC ELECTRICAL CHARACTERISTICS $V_{CC1} = V_{CC2} = \text{ground}$, $V_{EE} = -5.2V \pm 0.010V$

SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS								UNIT
			T _A = -30°C		T _A = +25°C			T _A = +85°C			
			MIN.	MAX.	MIN.	TYP.	MAX.	MIN.	MAX.		
t _{PLH} t _{PHL}	Propagation delay D _n to Q _n	Waveform 1	1.00 1.00	3.30 3.30	1.00 1.00	2.00 2.00	2.90 2.90	1.00 1.00	3.30 3.30	ns ns	
t _{PLH} t _{PHL}	Propagation delay E to Q _n	Waveform 2	1.10 1.10	3.90 3.90	1.10 1.10	2.50 2.50	3.50 3.50	1.10 1.10	3.90 3.90	ns ns	
t _{TLH} t _{THL}	Transition time 20% to 80%, 80% to 20%	Waveform 1, 2	1.10 1.10	3.60 3.60	1.10 1.10	2.00 2.00	3.30 3.30	1.10 1.10	3.70 3.70	ns ns	

NOTE:

For AC test setup information, see AC Testing, Chapter 2, Section 3.

AC WAVEFORMS

