

10V Drive Nch MOSFET

RCJ450N20

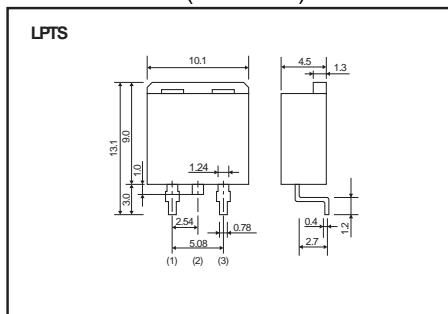
● Structure

Silicon N-channel MOSFET

● Features

- 1) Low on-resistance.
- 2) High-speed switching.
- 3) Wide range of SOA.
- 4) Drive circuits can be simple.
- 5) Parallel use is easy.

● Dimensions (Unit : mm)



● Application

Switching

● Packaging specifications

Type	Package	Taping
	Code	TL
	Basic ordering unit (pieces)	1000
RCJ450N20	O	

● Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	Limits	Unit
Drain-source voltage	V _{DSS}	200	V
Gate-source voltage	V _{GSS}	±30	V
Drain current	Continuous	I _D *3	A
	Pulsed	I _{DP} *1	A
Source current (Body Diode)	Continuous	I _S *3	A
	Pulsed	I _{SP} *1	A
Avalanche current	I _{AS} *2	22.5	A
Avalanche energy	E _{AS} *2	160	mJ
Power dissipation	P _D *4	40	W
Channel temperature	T _{ch}	150	°C
Range of storage temperature	T _{stg}	-55 to +150	°C

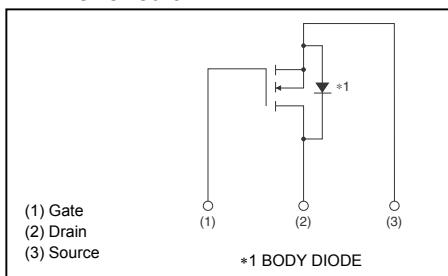
*1 Pw≤10μs, Duty cycle≤1%

*2 L=500μH, V_{DD}=50V, R_G=25Ω, T_{ch}=25°C

*3 Limited only by maximum temperature allowed.

*4 T_C=25°C

● Inner circuit



● Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to Case	R _{th} (ch-c)	3.12	°C / W

* T_C=25°C

* Limited only by maximum temperature allowed.

● Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Gate-source leakage	I _{GSS}	-	-	±100	nA	V _{GS} =±30V, V _{DS} =0V
Drain-source breakdown voltage	V _{(BR)DSS}	200	-	-	V	I _D =1mA, V _{GS} =0V
Zero gate voltage drain current	I _{DSS}	-	-	1	μA	V _{DS} =200V, V _{GS} =0V
Gate threshold voltage	V _{GS (th)}	3.0	-	5.0	V	V _{DS} =10V, I _D =1mA
Static drain-source on-state resistance	R _{DS (on)*}	-	42	55	mΩ	I _D =22.5A, V _{GS} =10V
Forward transfer admittance	Y _{fs} *	17.0	-	-	S	V _{DS} =10V, I _D =22.5A
Input capacitance	C _{iss}	-	4200	-	pF	V _{DS} =25V
Output capacitance	C _{oss}	-	270	-	pF	V _{GS} =0V
Reverse transfer capacitance	C _{rss}	-	160	-	pF	f=1MHz
Turn-on delay time	t _{d(on)} *	-	52	-	ns	V _{DD} ≈ 100V, I _D =22.5A
Rise time	t _r *	-	210	-	ns	V _{GS} =10V
Turn-off delay time	t _{d(off)} *	-	90	-	ns	R _L =4.4Ω
Fall time	t _f *	-	70	-	ns	R _G =10Ω
Total gate charge	Q _g *	-	80	-	nC	V _{DD} ≈ 100V, I _D =45A
Gate-source charge	Q _{gs} *	-	28	-	nC	V _{GS} =10V
Gate-drain charge	Q _{gd} *	-	28	-	nC	

*Pulsed

● Body diode characteristics (Source-Drain)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward Voltage	V _{SD} *	-	-	1.5	V	I _s =45A, V _{GS} =0V

*Pulsed

●Electrical characteristic curves ($T_a=25^\circ\text{C}$)

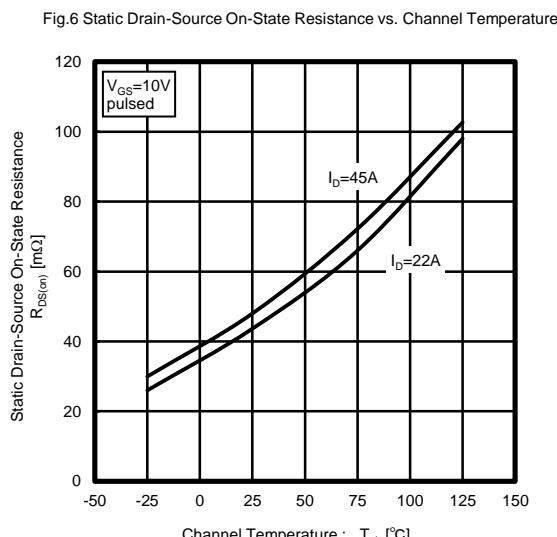
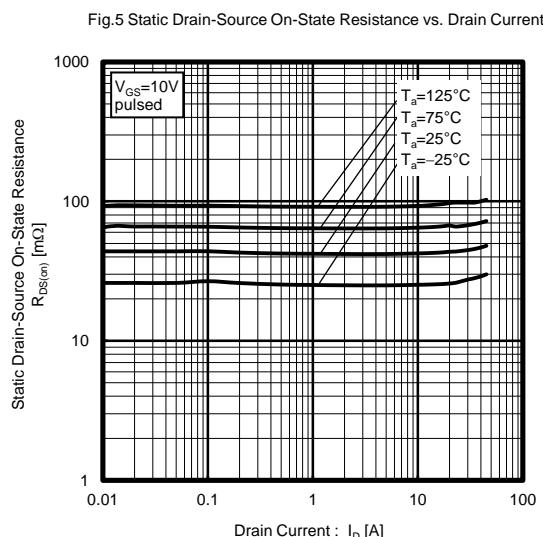
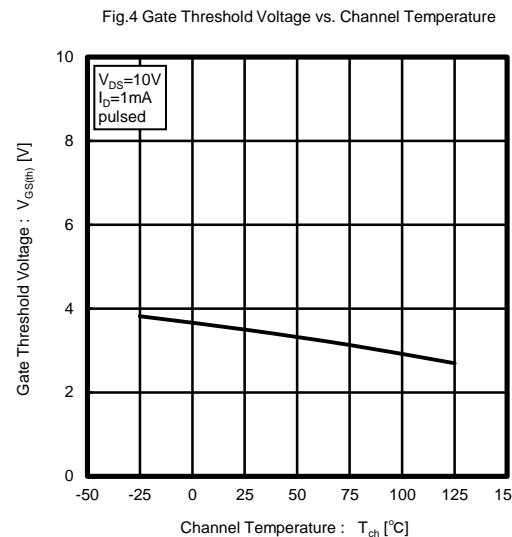
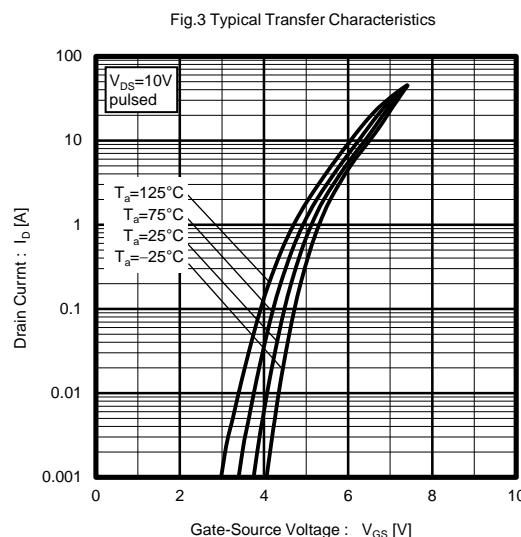
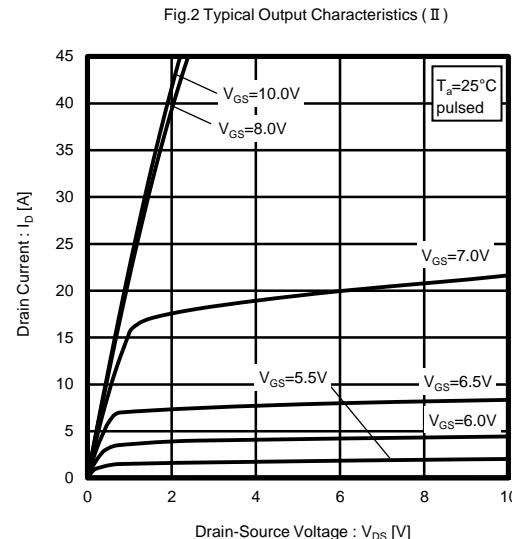
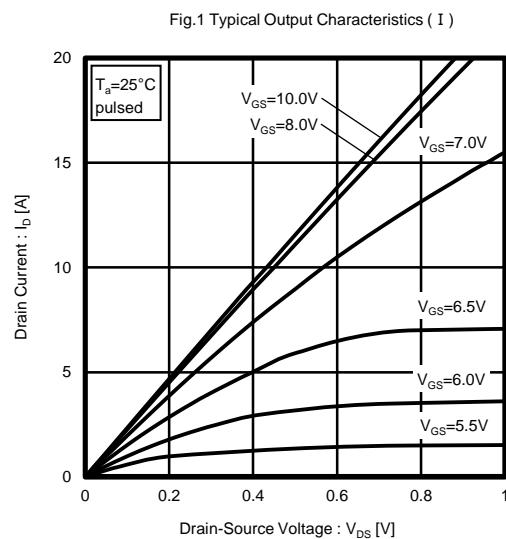


Fig.7 Forward Transfer Admittance vs. Drain Current

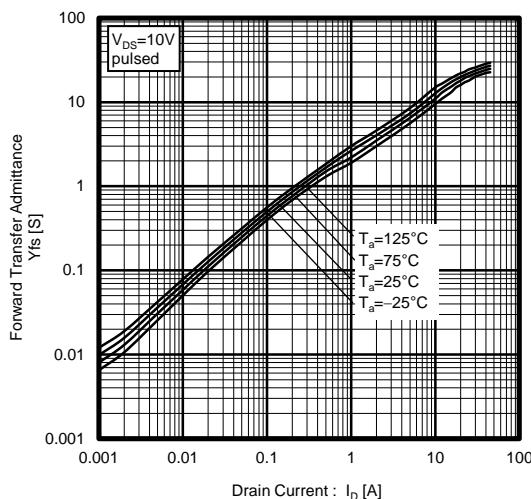


Fig.8 Source Current vs. Source-Drain Voltage

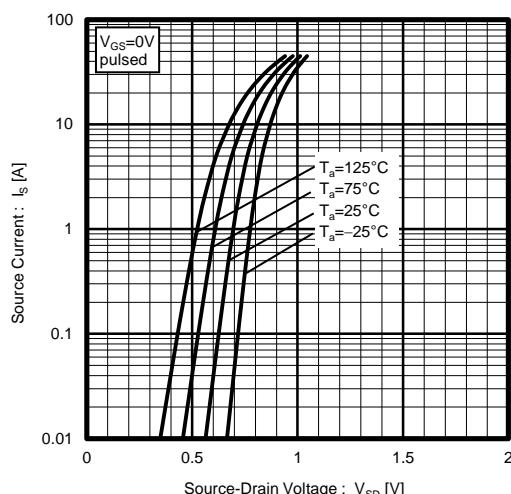


Fig.9 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

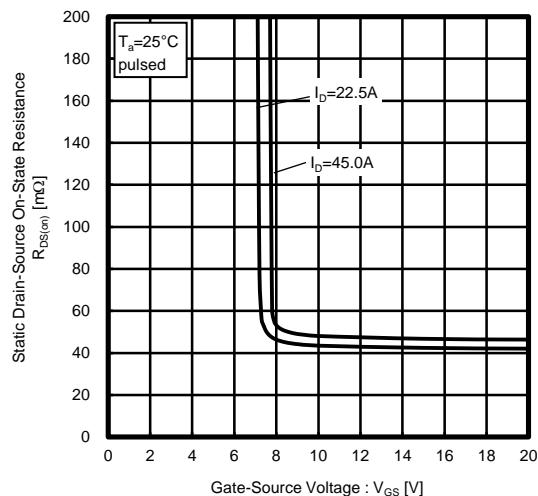


Fig.10 Switching Characteristics

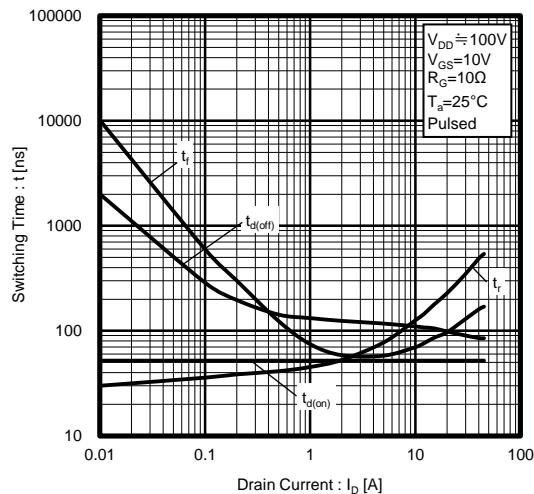


Fig.11 Dynamic Input Characteristics

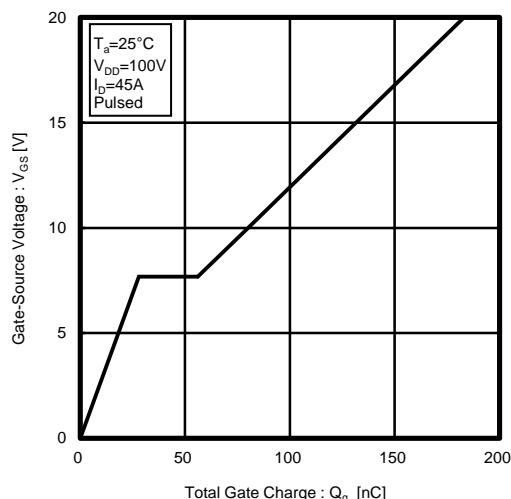


Fig.12 Typical Capacitance vs. Drain-Source Voltage

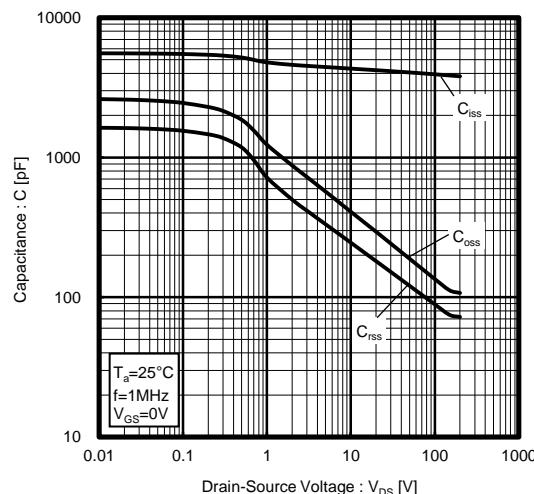


Fig.13 Reverse Recovery Time vs. Source Current

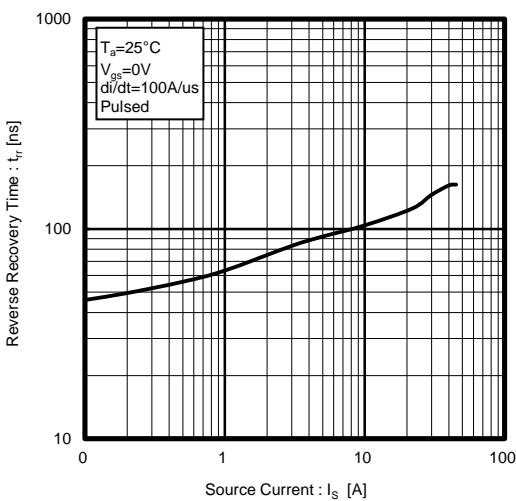


Fig.14 Maximum Safe Operating Area

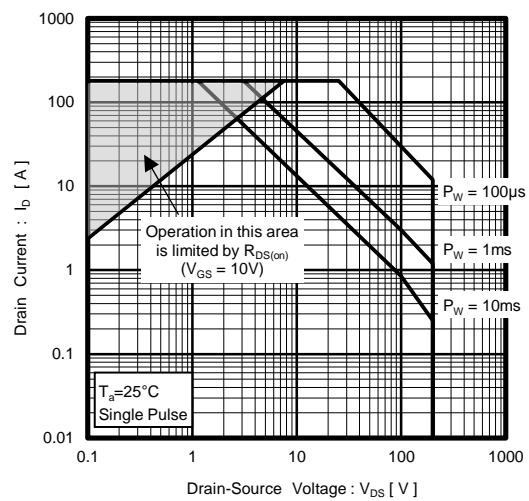
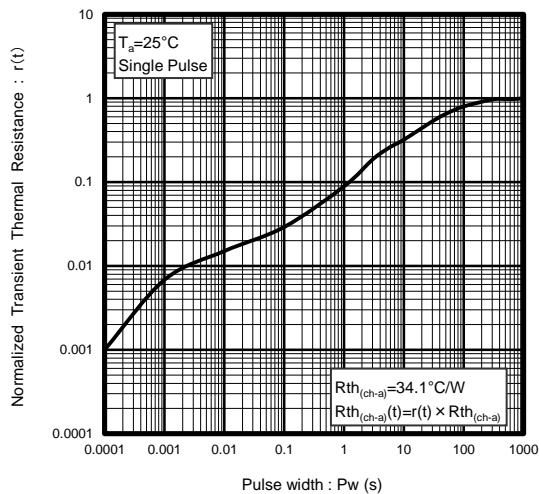


Fig.15 Normalized Transient Thermal Resistance v.s. Pulse Width



● Measurement circuits

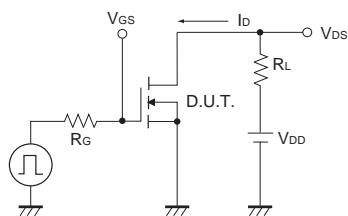


Fig.1-1 Switching Time Measurement Circuit

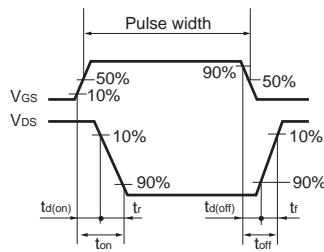


Fig.1-2 Switching Waveforms

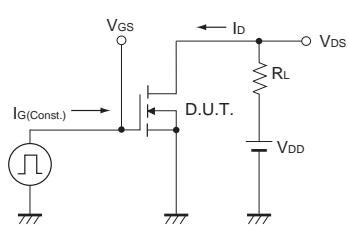


Fig.2-1 Gate Charge Measurement Circuit

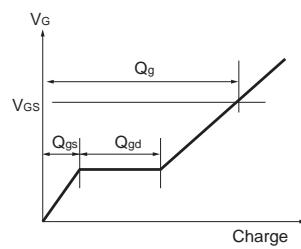


Fig.2-2 Gate Charge Waveform

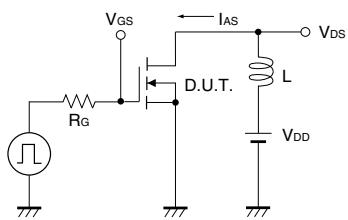


Fig.3-1 Avalanche Measurement Circuit

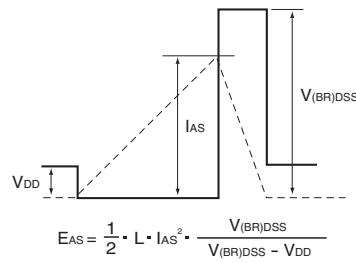


Fig.3-2 Avalanche Waveform

Notes

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