VN808CM-E

Octal channel high side driver

Features

Type	$R_{DS(on)}$	I_{out}	V_{CC}
VN808CM-E	160 mΩ	0.7 A	45 V

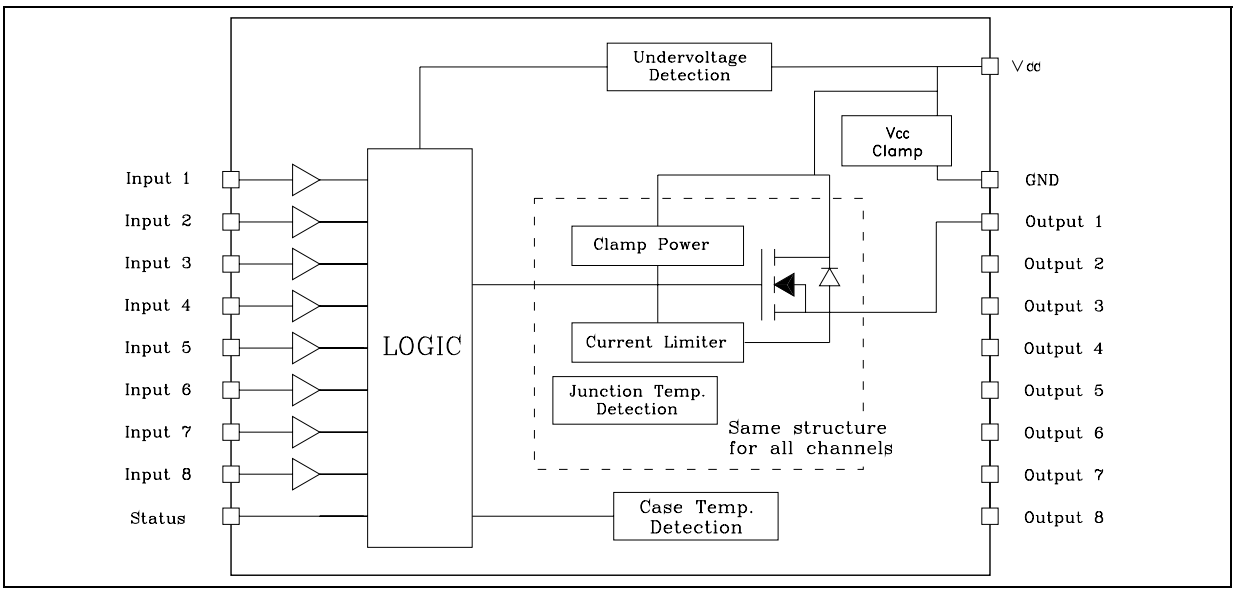
- CMOS compatible input
- Junction overtemperature protection
- Case overtemperature protection for thermal independence of the channels
- Current limitation
- Shorted load protection
- Undervoltage shutdown
- Protection against loss of ground
- Very low standby current
- Compliance to 61000-4-4 IEC test up to 4 kV

PowerSO-36

Description

The VN808CM-E is a monolithic device designed in STMicroelectronics VIPower M0-3 technology, intended for driving any kind of load with one side connected to ground. It can be driven by using a 3.3 V logic supply.

Active current limitation combined with thermal shutdown and automatic restart, protect the device against overload. In overload condition, channel turns OFF and back ON automatically so as to maintain junction temperature between T_{TSD} and T_R. If this condition makes case temperature reach T_{CSD}, overloaded channel is turned OFF and will restart only when case temperature has decreased down to T_{CR}(see waveform 3 *Figure 6 on page 10*). Non overloaded channels continue to operate normally. Device automatically turns OFF in case of ground pin disconnection. This device is especially suitable for industrial applications conform to IEC 61131



Contents

1 Maximum ratings

Table 1. Absolute maximum rating

Symbol	Parameter	Value	Unit
V_{CC}	DC supply voltage	45	V
$-I_{GND}$	DC ground pin reverse current TRAN ground pin reverse current (pulse duration < 1ms)	-250 -6	mA A
I_{OUT}	DC output current	Internally limited	A
$-I_{OUT}$	Reverse DC output current	-2	A
I_{IN}	DC Input current	± 10	mA
V_{ESD}	Electrostatic discharge (R = 1.5 kΩ; C = 100 pF)	2000	V
P_{TOT}	Power dissipation at T_c = 25 °C	96	W
L_{MAX}	Max inductive load (V_{CC} = 24 V, R_{LOAD} = 48 Ω, T_A = 100 °C)	2	H
T_J	Junction operating temperature	Internally limited	°C
T_C	Case operating temperature	Internally limited	°C
T_{STG}	Storage temperature	-40 to 150	°C

Table 2. Thermal data

Symbol	Parameter		Value	Unit
R_{thJC}	Thermal resistance junction-case	Max	1.3	°C/W
R_{thJA}	Thermal resistance junction-ambient (1)	Max	50	°C/W

1. When mounted on FR4 printed circuit board with 0.5 cm^2 of copper area (at least 35 μm think) connected to all TAB pins.

2 Electrical characteristics

(10.5 V < V_{CC} < 32 V; -40 °C < T_J < 125 °C; unless otherwise specified)

Table 3. Power section

Symbol	Parameter	Test conditions	Min	Typ	Max	Unit
V_{CC}	Operating supply voltage		10.5		45	V
V_{USD}	Undervoltage shutdown		7		10.5	V
R_{ON}	On state resistance	I_{OUT} = 0.5 A; T_J = 25 °C I_{OUT} = 0.5 A;			160 280	mΩ mΩ
I_S	Supply current	OFF state; V_{CC} = 24 V; T_{CASE} = 25 °C ON state (all channels ON); V_{CC} = 24 V, T_{CASE} = 100 °C			150 12	µA mA
I_{LGND}	Output current at turn-off	V_{CC} = V_{STAT} = V_{IN} = V_{GND} = 24 V V_{OUT} = 0 V			1	mA
$I_{L(off)}$	OFF state output current	V_{IN} = V_{OUT} = 0 V;	0		5	µA
$V_{OUT(off)}$	OFF state output voltage	V_{IN} = 0 V, I_{OUT} = 0 A			3	V
$t_{d(Vccon)}$	Power-on delay time from V_{CC} rising edge	*Figure 7 on page 12*		1		ms

Table 4. Switching (V_{CC} = 24 V)

Symbol	Parameter	Test conditions	Min	Typ	Max	Unit
t_{ON}	Turn-on time	R_L = 48 Ω from 80% V_{OUT} *Figure 4.*	-	50	100	µs
t_{OFF}	Turn-off time	R_L = 48 Ω to 10% V_{OUT} *Figure 4.*	-	75	150	µs
$dV_{OUT}/dt_{(on)}$	Turn-on voltage slope	R_L = 48 Ω from V_{OUT} = 2.4 V to V_{OUT} = 19.2 V *Figure 4.*	-	0.7		V/µs
$dV_{OUT}/dt_{(off)}$	Turn-off voltage slope	R_L = 48 Ω from V_{OUT} = 21.6 V to V_{OUT} = 2.4 V *Figure 4.*	-	1.5		V/µs

Table 5. Input pin

Symbol	Parameter	Test conditions	Min	Typ	Max	Unit
V_{INL}	Input low-level				1.25	V
I_{INL}	Low-level input current	V_{IN} = 1.25 V	1			µA
V_{INH}	Input high-level		2.25			V
I_{INH}	High-level input current	V_{IN} = 2.25 V			10	µA
$V_{I(HYST)}$	Input hysteresis voltage		0.25			V
V_{ICL}	Input clamp voltage	I_{IN} = 1 mA I_{IN} = -1 mA	6.0	6.8 -0.7	8.0	V V

Table 6. Protections

Symbol	Parameter	Test conditions	Min	Typ	Max	Unit
T_{CSD}	Case shutdown temperature		125	130	135	°C
T_{CR}	Case reset temperature		110			°C
T_{CHYST}	Case thermal hysteresis		7	15		°C
T_{TSD}	Junction shutdown temperature		150	175	200	°C
T_R	Junction reset temperature		135			°C
T_{HYST}	Junction thermal hysteresis		7	15		°C
I_{lim}	DC short-circuit current	V_{CC} = 24 V; R_{LOAD} = 10 mΩ	0.7		1.7	A
V_{demag}	Turn-off output clamp voltage	I_{OUT} = 0.5 A; L = 6 mH	V_{CC}-57	V_{CC}-52	V_{CC}-47	V

Table 7. Status pin

Symbol	Parameter	Test conditions	Min	Typ	Max	Unit
I_{HSTAT}	High-level output current	V_{CC} = 18...32 V; R_{STAT} = 1 kΩ (Fault condition)	2	3	4	mA
I_{LSTAT}	Leakage current	Normal operation; V_{CC} = 32 V			0.1	µA
V_{CLSTAT}	Clamp voltage	I_{STAT} = 1 mA I_{STAT} = -1 mA	6.0	6.8 -0.7	8.0	V V

3 Pin connections

Figure 2. Connection diagram (top view)

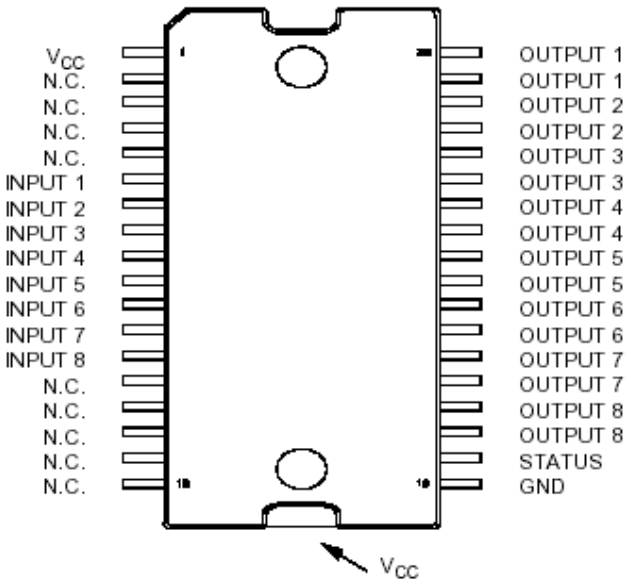


Table 8. Pin functions

Pin N°	Symbol	Function
TAB	V_{CC}	Positive power supply voltage
1	V_{CC}	Positive power supply voltage
2,3,4,5	NC	Not connected
6	Input 1	Input of channel 1
7	Input 2	Input of channel 2
8	Input 3	Input of channel 3
9	Input 4	Input of channel 4
10	Input 5	Input of channel 5
11	Input 6	Input of channel 6
12	Input 7	Input of channel 7
13	Input 8	Input of channel 8
14,15,16,17,18	NC	Not connected
19	GND	Logic ground
20	STATUS	Common open source diagnostic for over-temperature
21,22	Output 8	High-side output of channel 8
23,24	Output 7	High-side output of channel 7
25,26	Output 6	High-side output of channel 6

Table 8. Pin functions (continued)

Pin N°	Symbol	Function
27.28	Output 5	High-side output of channel 5
29,30	Output 4	High-side output of channel 4
31,32	Output 3	High-side output of channel 3
33,34	Output 2	High-side output of channel 2
35,36	Output 1	High-side output of channel 1

4 Current, voltage conventions and truth table

Figure 3. Current and voltage conventions

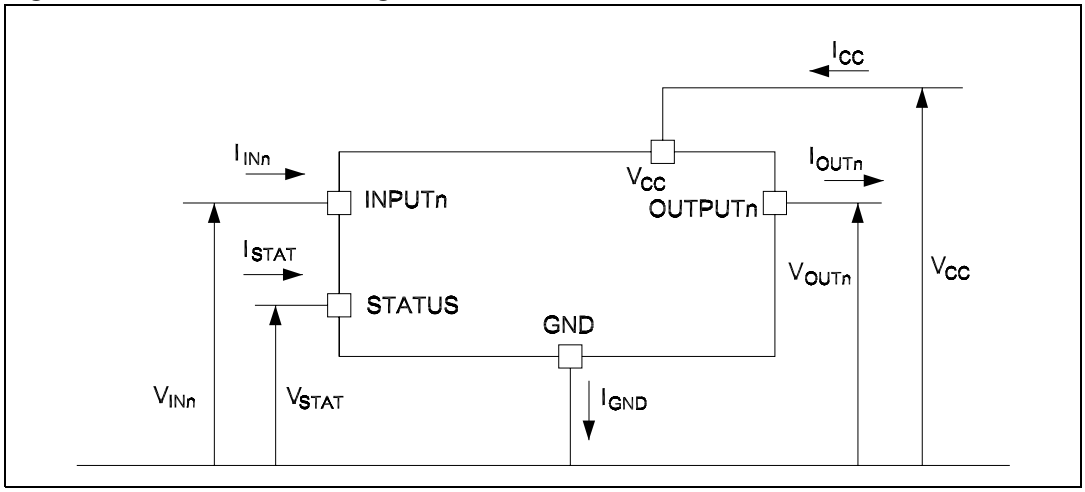


Table 9. Truth table

Conditions	INPUTn	OUTPUTn	STATUS
Normal operation	L H	L H	L L
Current limitation	L H	L X	L L
Overtemperature (see waveforms 3, 4 *Figure 6*) -> $T_J > T_{TSD}$	L H	L L	L H
Undervoltage	L H	L L	X X

5 Switching time waveforms

Figure 4. Turn-ON and turn-OFF

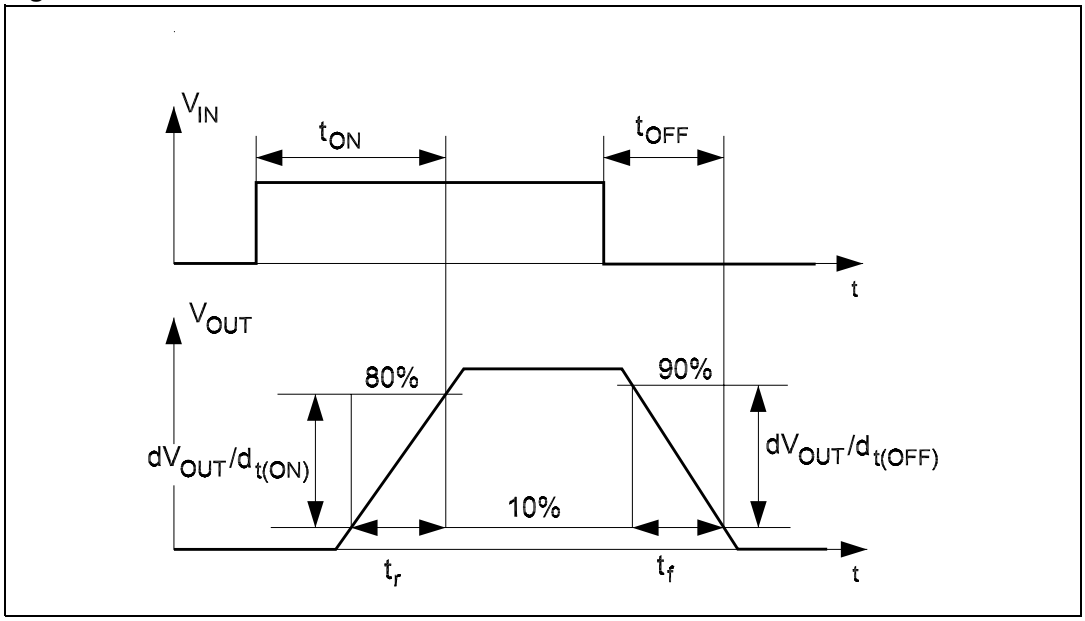


Figure 5. V_{CC} turn-ON

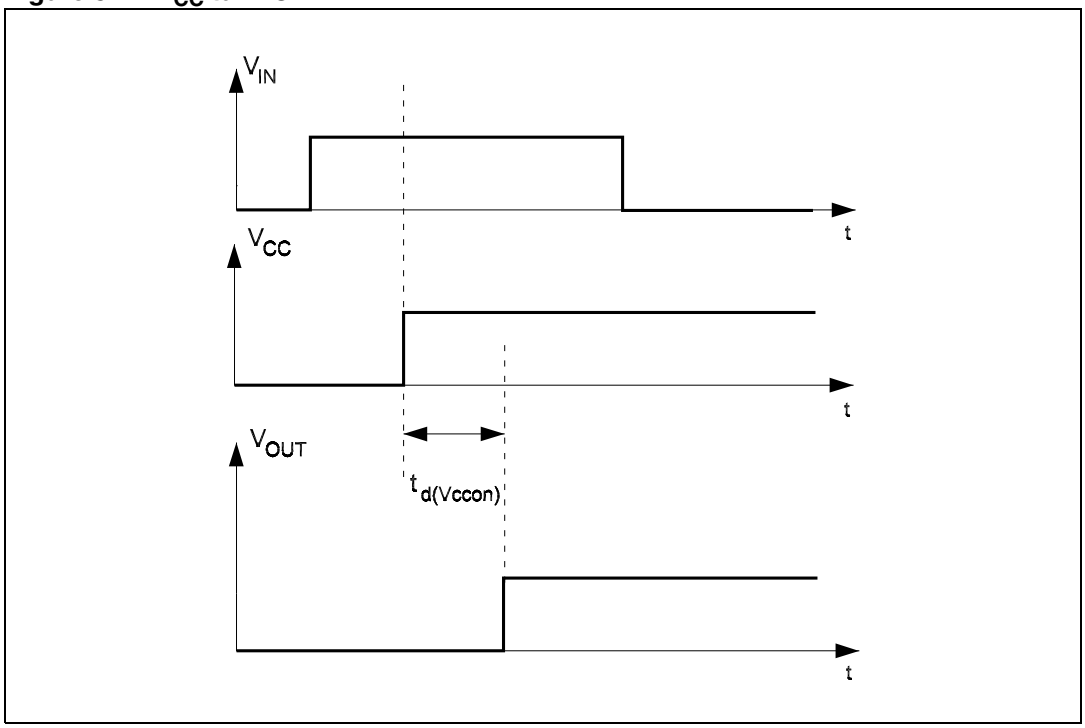


Figure 6. Waveforms

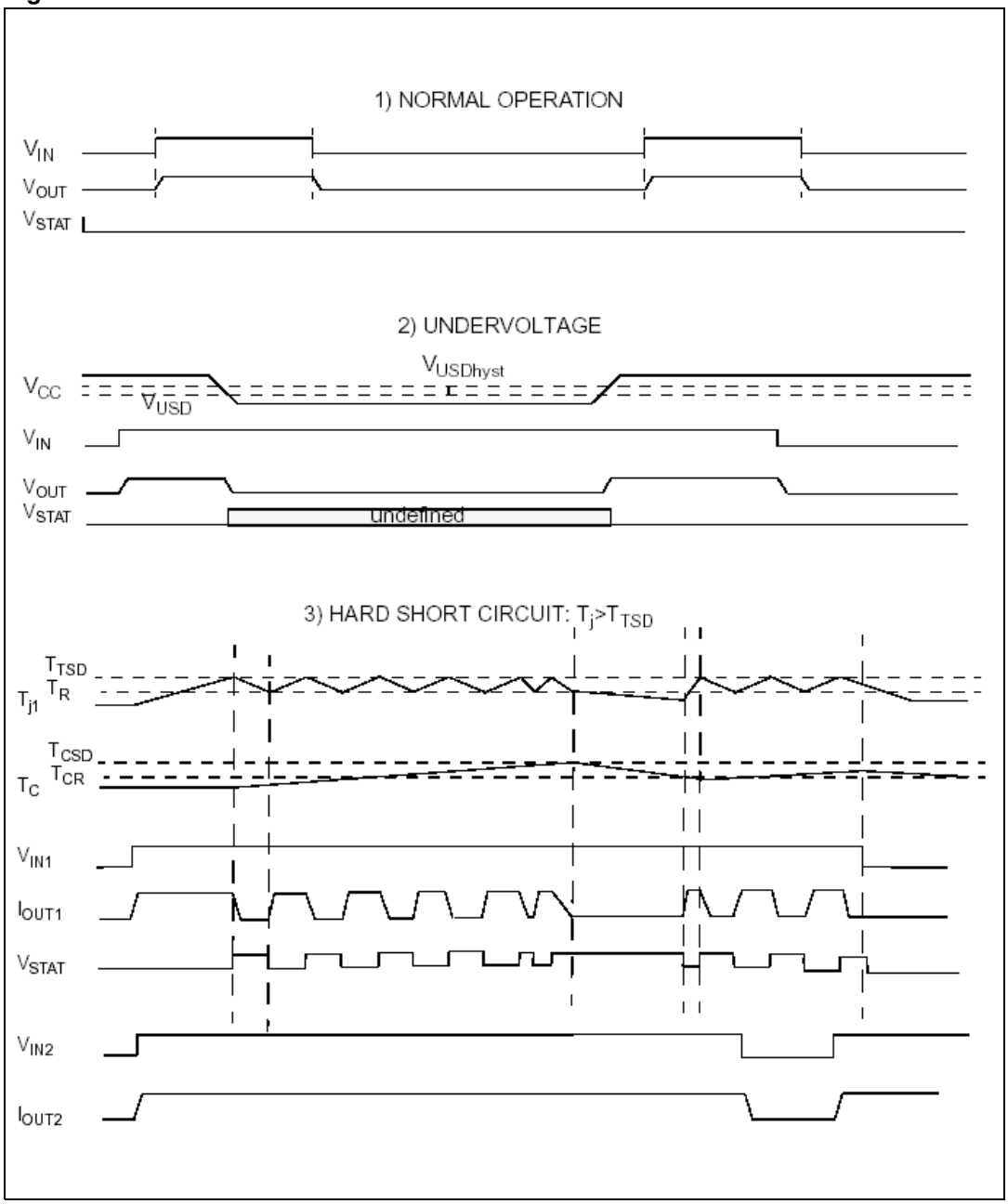
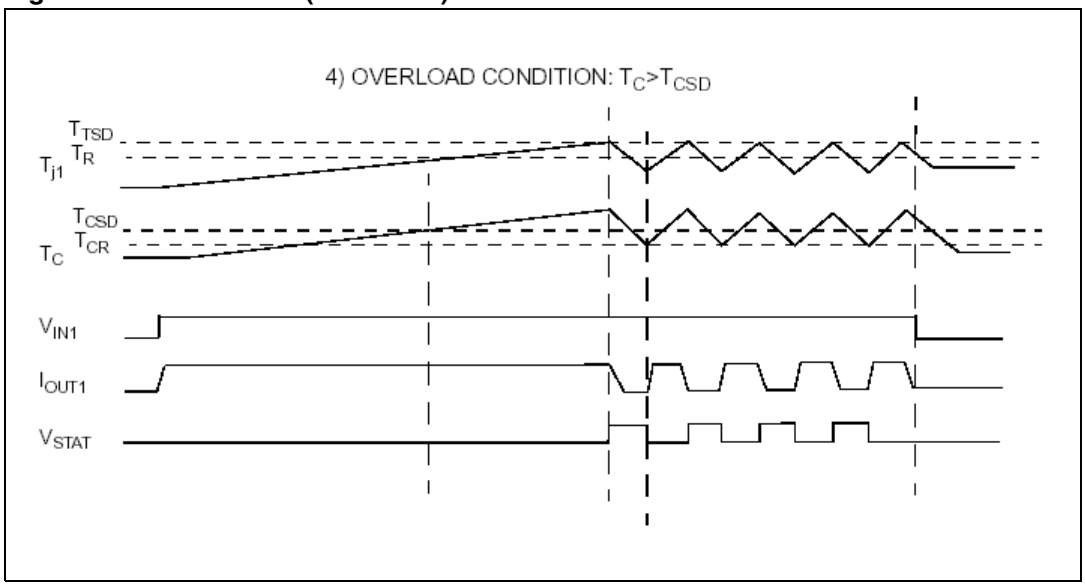


Figure 6. Waveforms (continued)



6 Reverse polarity protection

This schematic can be used with any type of load.

The following is an indication on how to dimension the R_{GND} resistor.

$R_{GND} = (-V_{CC}) / (-I_{GND})$

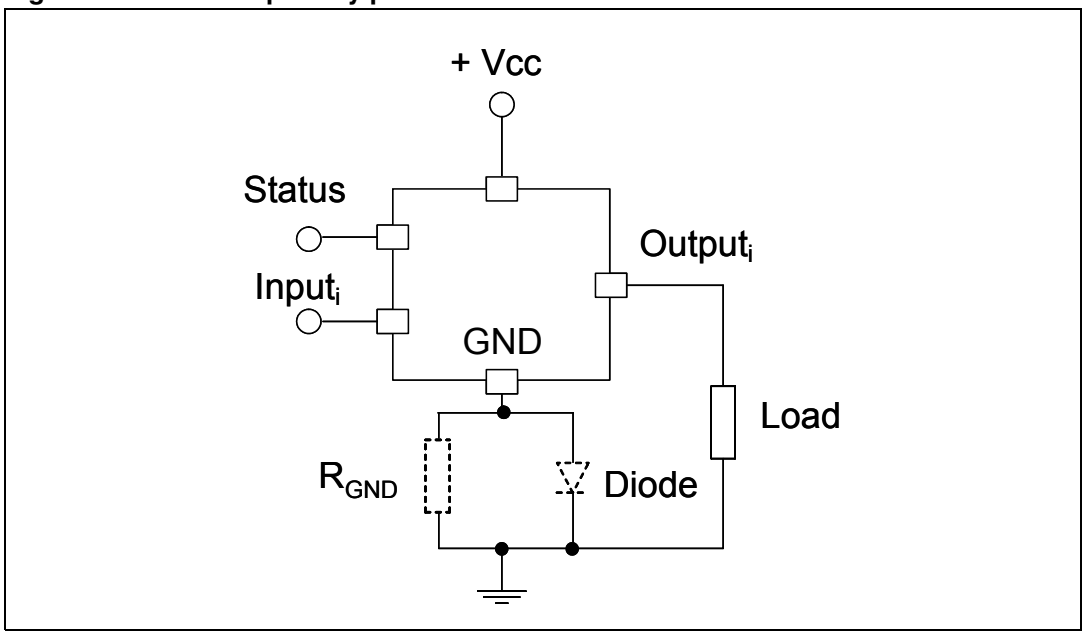
where $-I_{GND}$ is the DC reverse ground pin current and can be found in the absolute maximum rating section of the device datasheet.

Power dissipation in R_{GND} (when $V_{CC}< 0$: during reverse polarity situations) is:

$PD = (-V_{CC})^2/R_{GND}$

Note: In normal condition (no reverse polarity) due to the diode there will be a voltage drop between GND of the device and GND of the system.

Figure 7. Reverse polarity protection



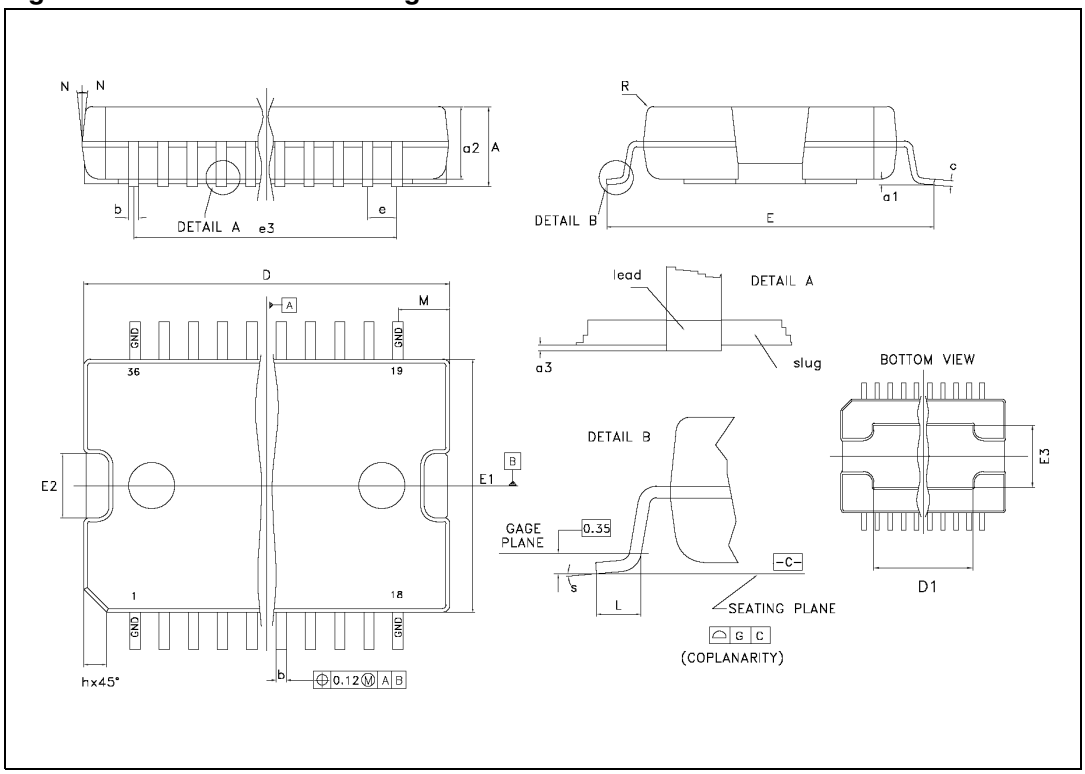
7 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: www.st.com. ECOPACK is an ST trademark.

Table 10. PowerSO-36 mechanical data

Dim.	mm			inch		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A			3.60			0.1417
a1	0.10		0.30	0.003		0.0118
a2			3.30			0.1299
a3	0		0.10	0		0.0039
b	0.22		0.38	0.008		0.0150
c	0.23		0.32	0.009		0.0126
D (1)	15.80		16.00	0.622		0.6299
D1	9.40		9.80	0.370		0.3858
E	13.90		14.50	0.547		0.5709
E1 (1)	10.90		11.10	0.429		0.4370
E2			2.90			0.1142
E3	5.8		6.2	0.228		0.2441
e		0.65			0.025	
e3		11.05			0.435	
G	0		0.10	0.000		0.0039
H	15.50		15.90	0.610		0.6260
h			1.10			0.0433
L	0.80		1.10	0.031		0.0433
N			10°			10°
S	0°		8°	0°		8°

Figure 8. PowerSO-36 drawings



7.1 Footprint recommended data

Figure 9. Footprint recommended data

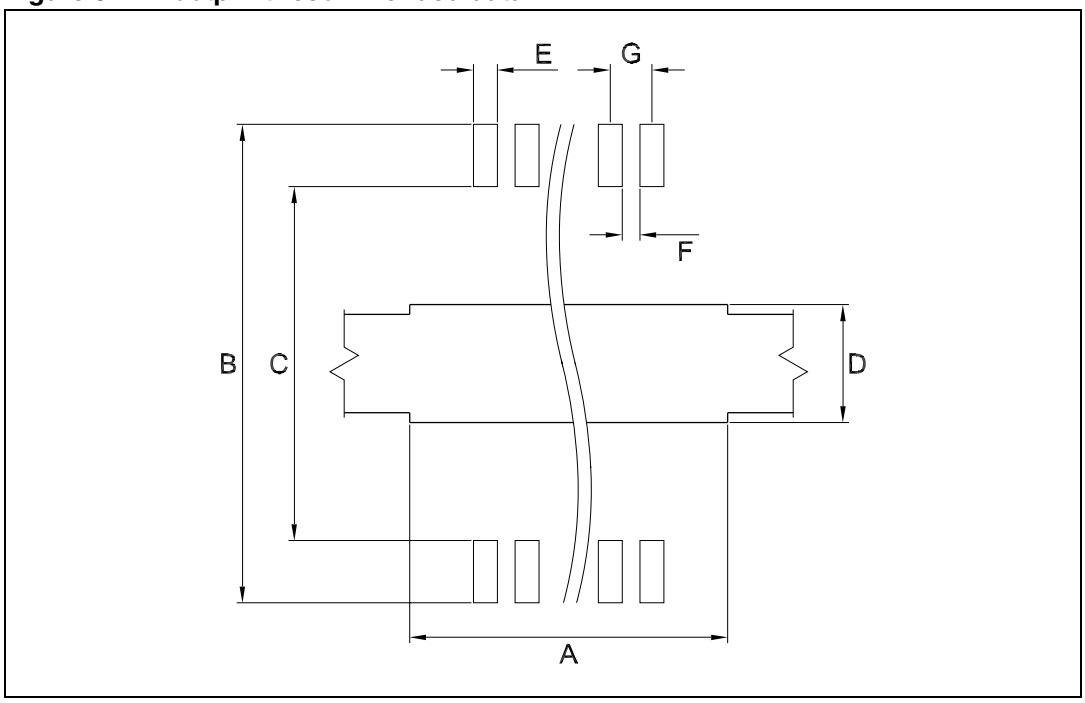


Table 11. Footprint data

Dim.	mm.	inch
A	9.5	0.374
B	14.7-15.0	0.579-0.591
C	12.5-12.7	0.492-0.500
D	6.3	0.248
E	0.46	0.018
F	0.27	0.011
G	0.65	0.026

7.2 Tube shipment information

Figure 10. Tube shipment information

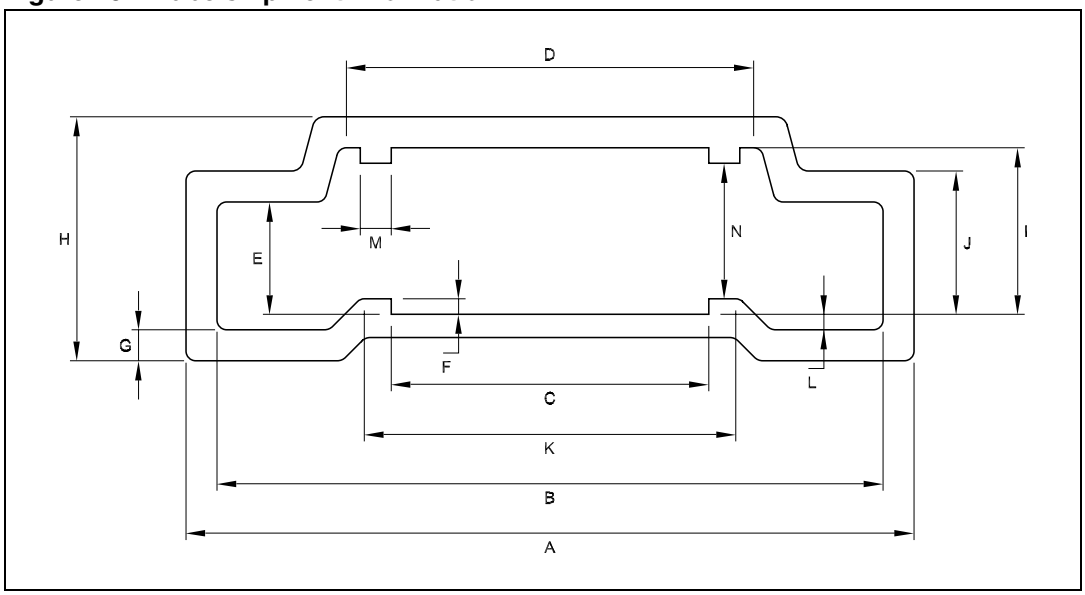


Table 12. Tube mechanical data

Dim.	mm.	inch
A	18.80	0.740
B	17.2 ±0.2	0.677 ±0.008
C	8.20 ±0.2	0.323 ±0.008
D	10.90 ±0.2	0.429 ±0.008
E	2.90 ±0.2	0.114 ±0.008
F	0.40	0.016
G	0.80	0.031
H	6.30	0.248
I	4.30 ±0.2	0.165 ±0.008
J	3.7 ±0.2	0.146 ±0.008
K	9.4	0.370
L	0.40	0.016
M	0.80	0.031
N	3.50 ±0.2	0.138 ±0.008

Base quantity 31 pcs.

Bulk quantity 310 pcs.

7.3 Tape and reel shipment information

Figure 11. Tape specifications

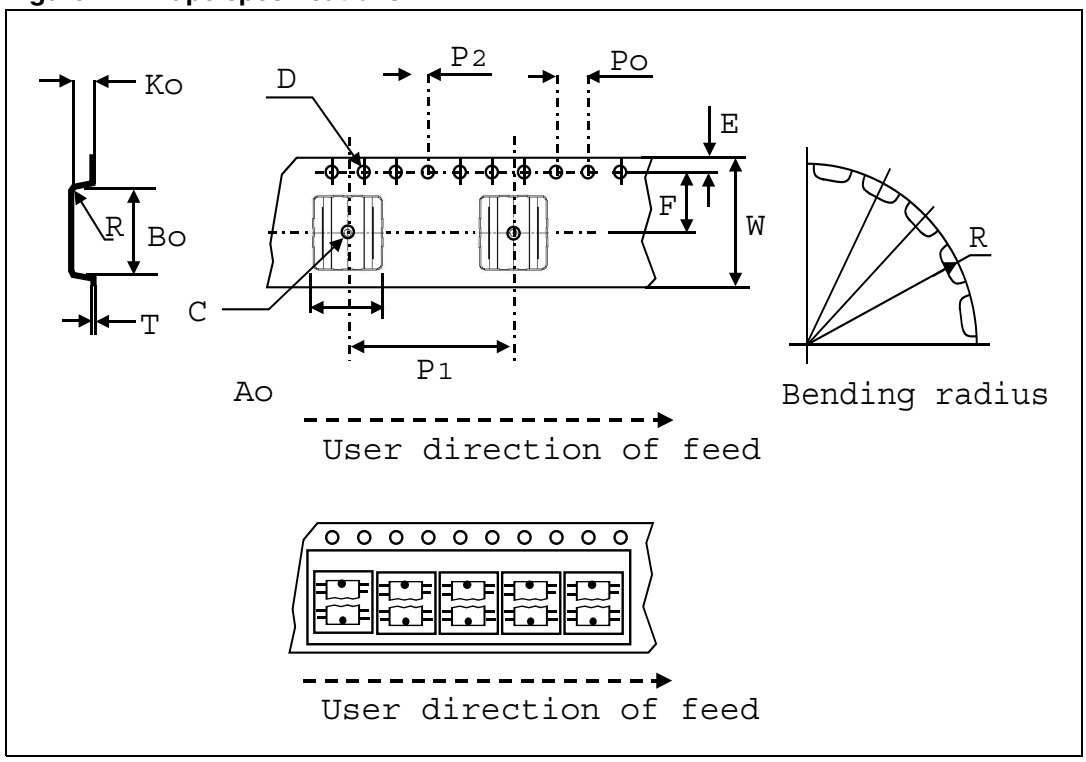


Table 13. Tape mechanical data

Dim.	mm.	inch
D	1.50 +0.1/0	0.059 +0.004/0
E	1.75 ±0.1	0.069 ±0.004
Po	4.00 ±0.1	0.157 ±0.004
T max.	0.40	0.016
D1 min.	1.50	0.059
F	11.5 ±0.05	0.453 ±0.002
K max.	6.50	0.256
P2	2.00 ±0.1	0.079 ±0.004
R	50	1.968
W	24.00 ±0.30	0.945 ±0.012
P1	24.00	0.945
Ao, Bo, Ko	0.05 min to 1.0 max.	0.002 min to 0.039 max.

Base quantity 600 pcs.

Bulk quantity 600 pcs.

Figure 12. Reel specifications

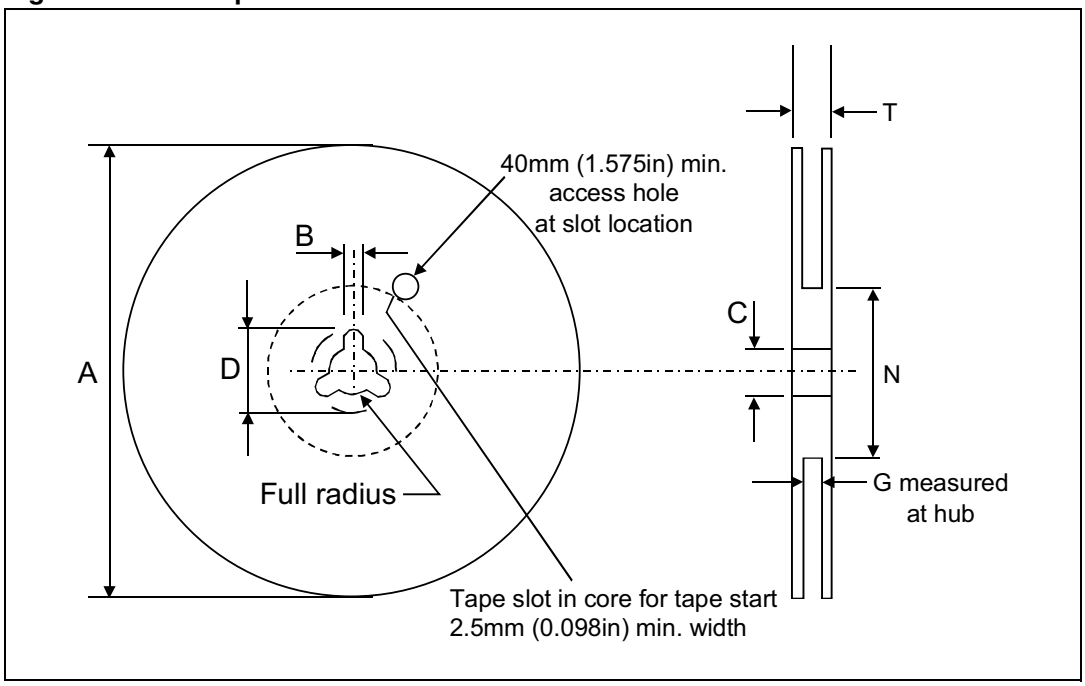


Table 14. Reel mechanical data

Dim.	mm.	inch
Tape size	24.0 ±0.30	0.945 ±0.012
A max.	330.0	12.992
B min.	1.5	0.059
C	13.0 ±0.20	0.512 ±0.008
D min.	20.2	0.795
N min.	60	2.362
G	24.4 +2/-0	0.960 +0.079/-0
T max.	30.4	1.197

8 Order codes

Table 15. Order codes

Order codes	Package	Packaging
VN808CM-E	PowerSO-36	Tube
VN808CMTR-E	PowerSO-36	Tape and reel

9 Revision history

Table 16. Document revision history

Date	Revision	Changes
29-Jun-2005	1	Initial release
12-Sep-2005	2	New template
28-Jun-2006	3	Application schematic updated
09-Jul-2008	4	Added *Section 6: Reverse polarity protection*
04-Aug-2008	5	Added *Figure 8: PowerSO-36 drawings*
26-Aug-2009	6	Updated *Section 6: Reverse polarity protection*
15-Sep-2009	7	Typing mistake in cover page: *Section : Features* and *Table 5: Input pin*
24-Feb-2010	8	Updated *Section 7: Package mechanical data*

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