Vishay High Power Products

# ADD-A-PAK Generation VII Power Modules Schottky Rectifier, 200 A

#### FEATURES

- 175 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation
- Low thermal resistance
- UL pending
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level

#### BENEFITS

- Excellent thermal performances obtained by the usage of exposed direct bonded copper substrate
- · High surge capability
- Easy mounting on heatsink

#### **ELECTRICAL DESCRIPTION**

The VSKDS401.. Schottky rectifier doubler has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature.

Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I <sub>F(AV)</sub>	Rectangular waveform	200	А		
V <sub>RRM</sub>		45	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	29 000	А		
V <sub>F</sub>	100 Apk, T <sub>J</sub> = 125 °C	0.52	V		
TJ	Range	- 55 to 175	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VSKDS401/045	UNITS		
Maximum DC reverse voltage	V <sub>R</sub>	45	V		
Maximum working peak reverse voltage	V <sub>RWM</sub>	45	v		



The ADD-A-PAK generation VII, new generation of

ADD-A-PAK module, combines the excellent thermal

performances obtained by the usage of exposed direct

bonded copper substrate, with advanced compact simple

package solution and simplified internal structure with

200 A

**PRODUCT SUMMARY** 

**MECHANICAL DESCRIPTION** 

minimized number of interfaces.

IF(AV)





## VSKDS401/045

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ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I <sub>F(AV)</sub>	50 % duty cycle at $T_C$ = 120 °C, rectangular waveform		200	
Maximum peak one cycle		5 $\mu s$ sine or 3 $\mu s$ rect. pulse	Following any rated load condition and with	29 000	А
non-repetitive surge current	IFSM	10 ms sine or 6 ms rect. pulse	rated $V_{RRM}$ applied	3450	
Non-repetitive avalanche energy	E <sub>AS</sub>	$T_J = 25 \text{ °C}, I_{AS} = 24 \text{ A}, L = 1 \text{ mH}$		270	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		40	А

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
		200 A T 25 %C	T <sub>1</sub> = 25 °C	0.72	
Maximum forward voltage drop	V	400 A	1j=25 0	0.98	V
Maximum forward voltage drop	V <sub>FM</sub>	200 A	T _ 105 °C	0.69	
		400 A	T <sub>J</sub> = 125 °C	0.96	
		T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	20	mA
Maximum reverse leakage current	I <sub>RM</sub>	T <sub>J</sub> = 125 °C		180	
Maximum junction capacitance	CT	$V_{\rm R}$ = 5 $V_{\rm DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		10 300	pF
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		5.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs
Maximum RMS insulation voltage	V <sub>INS</sub>	50 Hz		3000 (1 min) 3600 (1 s)	V

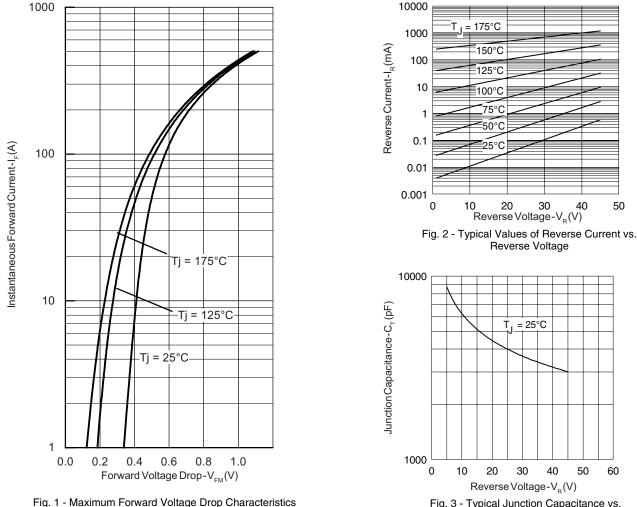
THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 175	°C
Maximum thermal resistance, junction to case per leg	B <sub>thic</sub> DC operation 0.2		0.26	- °C/W	
Typical thermal resistance, case to heatsink per module		R <sub>thCS</sub> 0.1			
Approvimente weight				75	g
Approximate weight				2.7	oz.
Mounting torque + 10.9/	to heatsink		A mounting compound is recommended and the torque should be rechecked after a period of 3 h to allow for the	4	Nm
Mounting torque ± 10 %	busbar		spread of the compound.	3	
Case style			JEDEC	TO-240AA co	mpatible

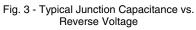


### VSKDS401/045

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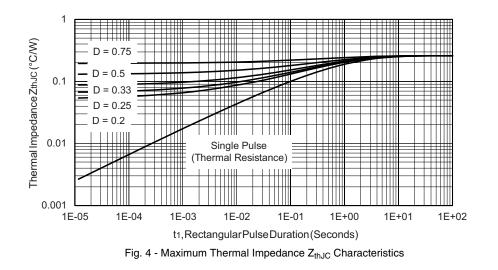
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= 25°C Т.

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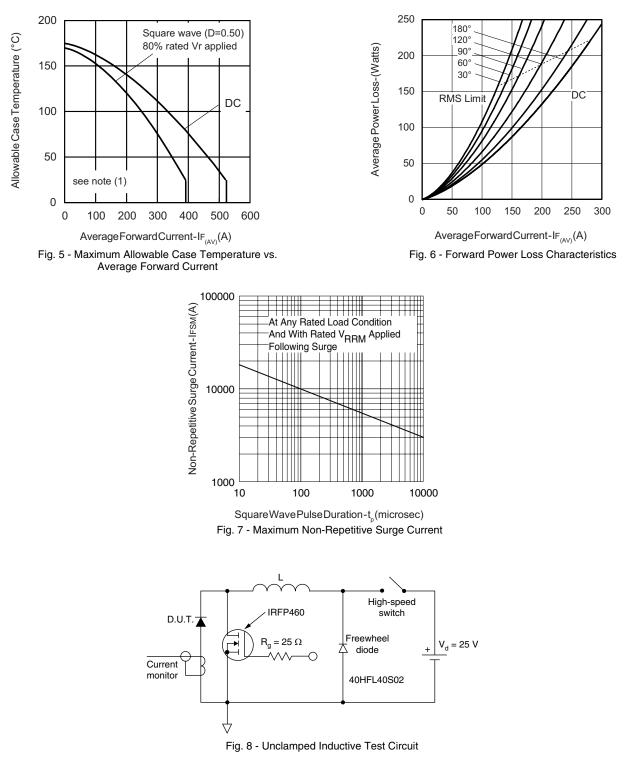
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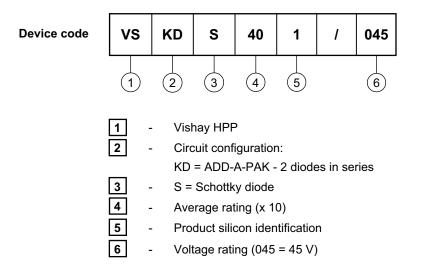
#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC};$   $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D); I_R at V_{R1} = 80 \% rated V_R$ 

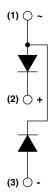


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#### ORDERING INFORMATION TABLE



#### **CIRCUIT CONFIGURATION**



LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95369			

**Vishay Semiconductors** 



### **ADD-A-PAK Generation VII - Diode**

#### **DIMENSIONS** in millimeters (inches)





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