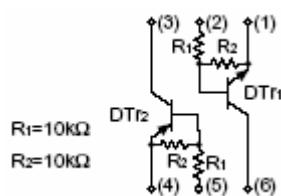


DIGITAL TRANSISTOR (NPN+PNP)

FEATURES

- DTA114E and DTC114E transistors are built-in a package.
- Transistor elements are independent, eliminating interference.
- Mounting cost and area can be cut in half.

External circuit



MARKING: D3

Absolute maximum ratings(Ta=25°C)

Parameter	Symbol	Limits			Unit		
Supply voltage	V _{CC}	50			V		
Input voltage	V _{IN}	-10~40			V		
Output current	I _O	50	mA	100	mA		
	I _{C(MAX)}	100					
Power dissipation	P _d	150(TOTAL)			mW		
Junction temperature	T _j	150					
Storage temperature	T _{stg}	-55~150					

Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ	Max.	Unit	Conditions
Input voltage	V _{I(off)}			0.5	V	V _{CC} =5V, I _O =100μA
	V _{I(on)}	3				V _O =0.3V, I _O =10mA
Output voltage	V _{O(on)}			0.3	V	I _O /I _i =10mA/0.5mA
Input current	I _i			0.88	mA	V _i =5V
Output current	I _{O(off)}			0.5	μA	V _{CC} =50V, V _i =0
DC current gain	G _I	30				V _O =5V, I _O =5mA
Input resistance	R ₁	7	10	13	KΩ	
Resistance ratio	R ₂ /R ₁	0.8	1	1.2		
Transition frequency	f _T		250		MHz	V _{CE} =10V, I _E =-5mA, f=100MHz