Dual N-channel MOSFET

ELM34810AA-N

■General description

ELM34810AA-N uses advanced trench technology to provide excellent Rds(on), low gate charge and low gate resistance.

■Features

- Vds=30V
- Id=7A
- Rds(on) < 21mΩ (Vgs=10V)
- Rds(on) < 35mΩ (Vgs=4.5V)

■Maximum absolute ratings

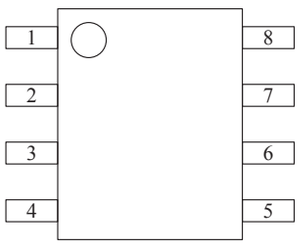
Parameter		Symbol	Limit	Unit	Note
Drain-source voltage		Vds	30	V	
Gate-source voltage		Vgs	±20	V	
Continuous drain current	Ta=25°C	Id	7	A	
	Ta=70°C		6		
Pulsed drain current		Idm	40	A	3
Power dissipation	Ta=25°C	Pd	2.0	W	
	Ta=70°C		1.3		
Junction and storage temperature range		Tj, Tstg	-55 to 150	°C	

■Thermal characteristics

Parameter		Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-ambient	Steady-state	Rθja		62.5	°C/W	

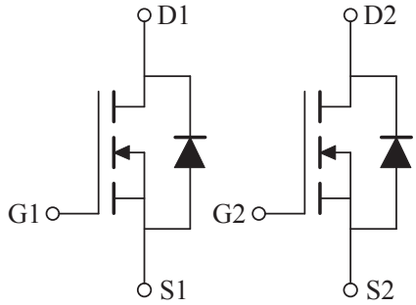
■Pin configuration

SOP-8(TOP VIEW)



Pin No.	Pin name
1	SOURCE1
2	GATE1
3	SOURCE2
4	GATE2
5	DRAIN2
6	DRAIN2
7	DRAIN1
8	DRAIN1

■Circuit



■Electrical characteristics

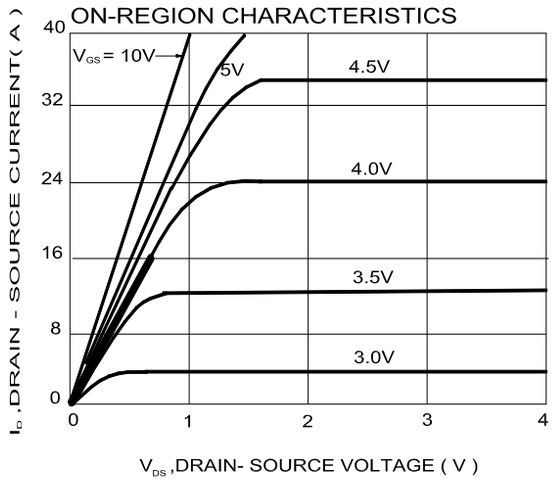
Ta=25°C

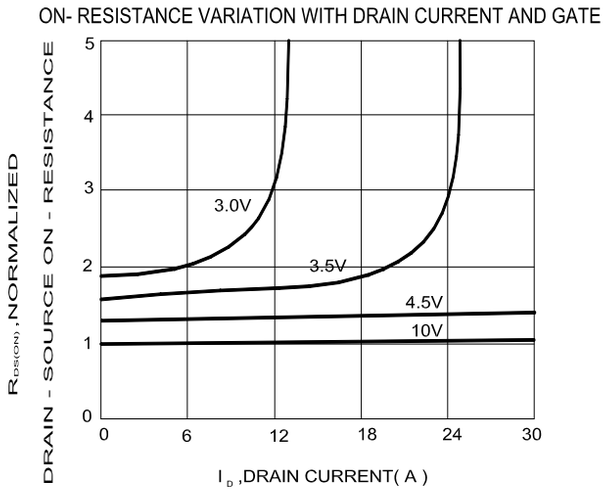
Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
STATIC PARAMETERS							
Drain-source breakdown voltage	BVdss	Id=250μA, Vgs=0V	30			V	
Zero gate voltage drain current	Idss	Vds=24V, Vgs=0V			1	μA	
		Vds=20V, Vgs=0V, Tj=55°C			10		
Gate-body leakage current	Igss	Vds=0V, Vgs=±20V			±100	nA	
Gate threshold voltage	Vgs(th)	Vds=Vgs, Id=250μA	1.0	1.5	3.0	V	
On state drain current	Id(on)	Vgs=10V, Vds=5V	25			A	1
Static drain-source on-resistance	Rds(on)	Vgs=10V, Id=7A		15	21	mΩ	1
		Vgs=4.5V, Id=6A		21	35	mΩ	
Forward transconductance	Gfs	Vds=15V, Id=5A		24		S	1
Diode forward voltage	Vsd	If=1A, Vgs=0V			1.2	V	1
Max.body-diode continuous current	Is				1.3	A	
Pulsed current	Ism				2.5	A	3
DYNAMIC PARAMETERS							
Input capacitance	Ciss	Vgs=0V, Vds=15V, f=1MHz		1650		pF	
Output capacitance	Coss			365		pF	
Reverse transfer capacitance	Crss			170		pF	
SWITCHING PARAMETERS							
Total gate charge	Qg	Vgs=5V, Vds=15V, Id=7A		18.0	25.0	nC	2
Gate-source charge	Qgs			5.5		nC	2
Gate-drain charge	Qgd			6.7		nC	2
Turn-on delay time	td(on)	Vgs=10V, Vds=15V, Id≈1A Rgen=6Ω		11	20	ns	2
Turn-on rise time	tr			9	18	ns	2
Turn-off delay time	td(off)			25	40	ns	2
Turn-off fall time	tf			11	20	ns	2
Body diode reverse recovery time	trr	If=5A, dI/dt=100A/μs		15.5		ns	
Body diode reverse recovery charge	Qrr			7.9		nC	

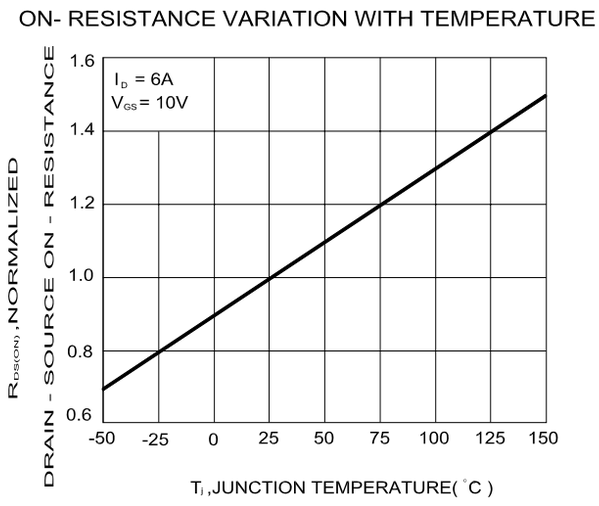
NOTE :

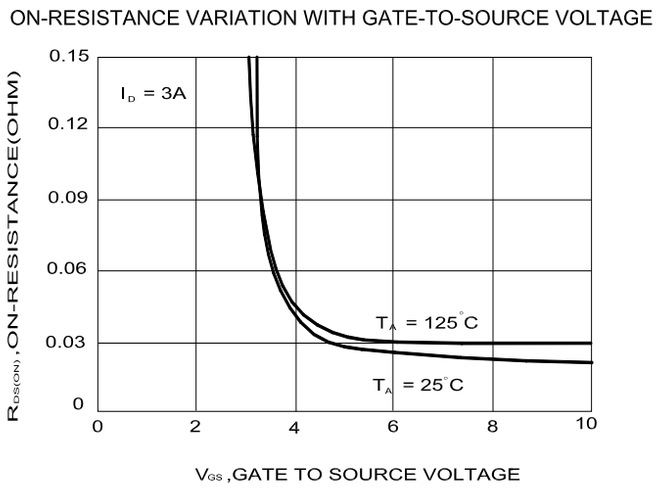
1. Pulsed width≤300μsec and Duty cycle≤2%.
2. Independent of operating temperature.
3. Pulsed width limited by maximum junction temperature.
4. Duty cycle ≤ 1%.

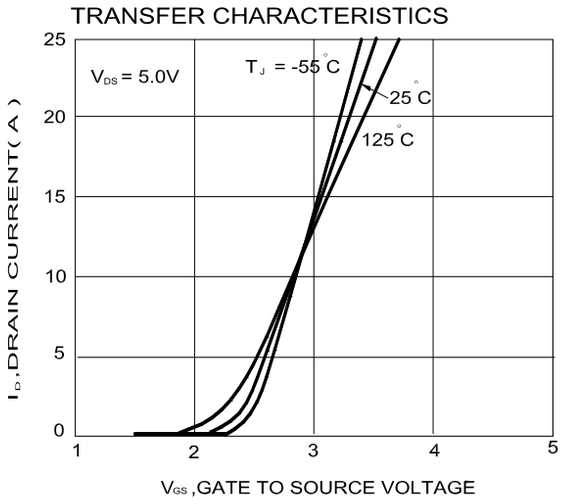
■Typical electrical and thermal characteristics

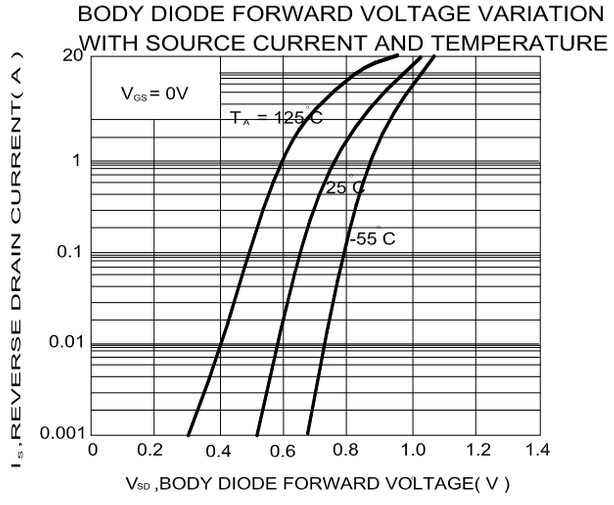




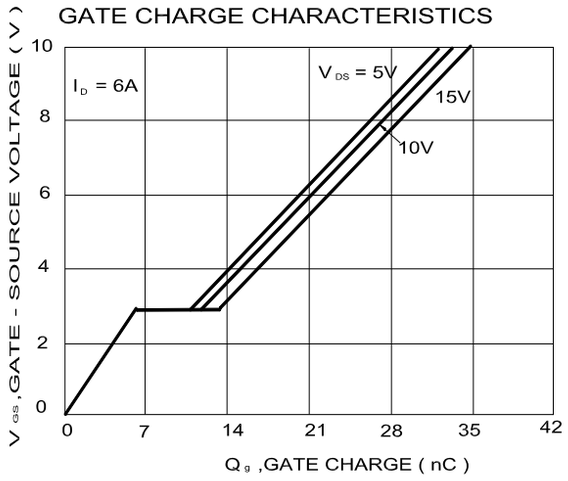




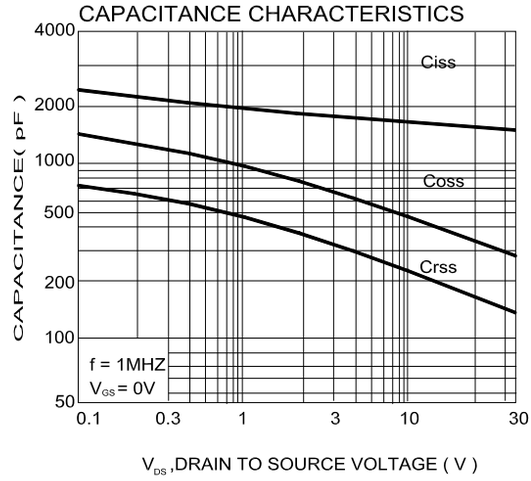




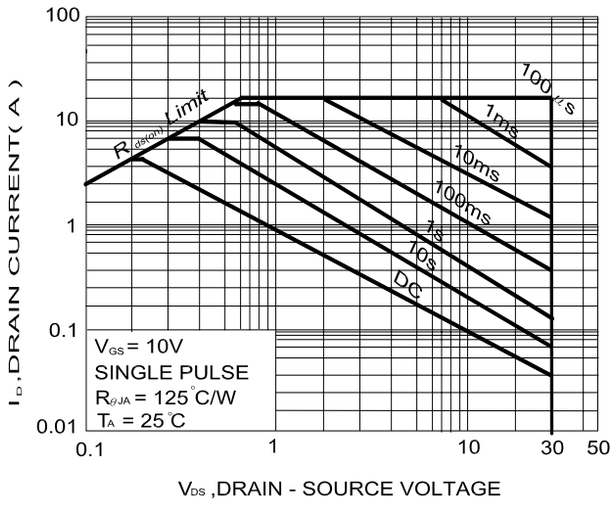
GATE CHARGE CHARACTERISTICS



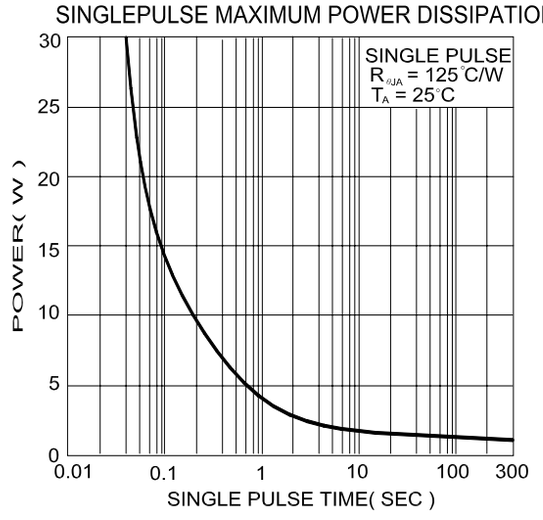
CAPACITANCE CHARACTERISTICS



MAXIMUMSAFE OPERATINGAREA



SINGLEPULSE MAXIMUM POWER DISSIPATION



TRANSIENT THERMAL RESPONSE CURVE