

DESCRIPTION

The SSF2429 uses advanced trench technology to provide excellent $R_{\text{DS(ON)}}$, low gate charge and operation with gate voltages as low as 2.5V.

GENERAL FEATURES

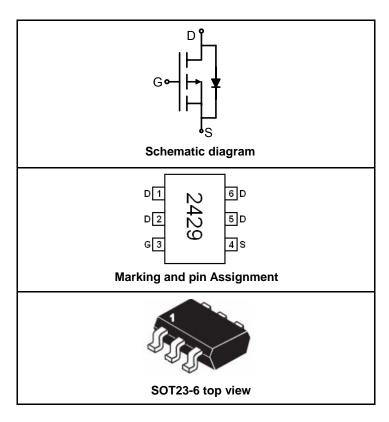
• $V_{DS} = -20V, I_D = -5A$

 $R_{DS(ON)}$ < 35m Ω @ V_{GS} =-4.5V $R_{DS(ON)}$ < 48m Ω @ V_{GS} =-2.5V

- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package



- Battery protection
- Load switch
- Power management



PACKAGE MARKING AND ORDERING INFORMATION

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
2429	SSF2429	SOT23-6	Ø180mm	8mm	3000 units

ABSOLUTE MAXIMUM RATINGS(TA=25℃ unless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	VDS	-20	V	
Gate-Source Voltage	V _G S	±12	V	
Design Comment Continuous & Comment Dules d (Note 1)	I _D	-5	А	
Drain Current-Continuous@ Current-Pulsed (Note 1)	I _{DM}	-20	Α	
Maximum Power Dissipation	P _D	1.4	W	
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 150	$^{\circ}$ C	

THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ heta JA}$	90	°C/W	I
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ELECTRICAL CHARACTERISTICS (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit	
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250μA	-20			V	



Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-20V,V _{GS} =0V			-1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±12V,V _{DS} =0V			±100	nA
ON CHARACTERISTICS (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} ,I _D =-250μA	-0.5	-0.7	-1	V
Drain-Source On-State Resistance	Б	V _{GS} =-4.5V, I _D =-5A		29	35	mΩ
Dialii-Source Oil-State Resistance	$R_{DS(ON)}$	V _{GS} =-2.5V, I _D =-3A		37	48	mΩ
Forward Transconductance	g FS	V _{DS} =-10V,I _D =-3A	4			S
DYNAMIC CHARACTERISTICS (Note4)						
Input Capacitance	C _{lss}			1450		PF
Output Capacitance	C _{oss}	V_{DS} =-10V, V_{GS} =0V, F=1.0MHz		200		PF
Reverse Transfer Capacitance	C _{rss}			160		PF
SWITCHING CHARACTERISTICS (Note 4)						
Turn-on Delay Time	t _{d(on)}			5		nS
Turn-on Rise Time	t _r	V _{DD} =-10V,I _D =-1A		13		nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =-4.5V, R_{GEN} =6 Ω		80		nS
Turn-Off Fall Time	t _f			35		nS
Total Gate Charge	Qg			17		nC
Gate-Source Charge	Q _{gs}	V_{DS} =-10V, I_{D} =-4.5A, V_{GS} =-5V		4		nC
Gate-Drain Charge	Q_{gd}	33 31		4.5		nC
DRAIN-SOURCE DIODE CHARACTERISTICS	•	•	•	•		
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =-1.3A			-1.3	V
	_					

NOTES:

- Repetitive Rating: Pulse width limited by maximum junction temperature.
 Surface Mounted on 1in² FR4 Board, t ≤ 10 sec.
 Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.

- 4. Guaranteed by design, not subject to production testing.



ELECTRICAL AND THERMAL CHARACTERISTICS

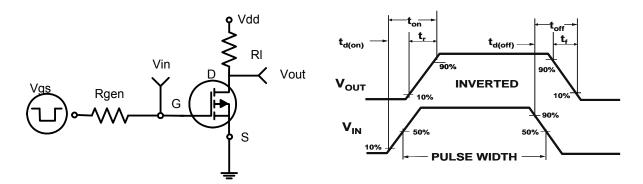
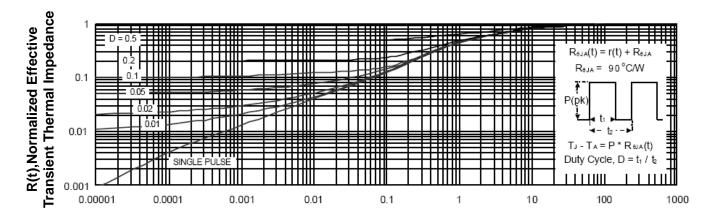


Figure 1:Switching Test Circuit

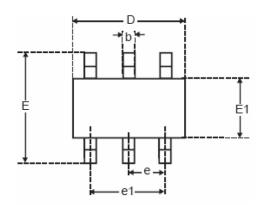
Figure 2:Switching Waveforms

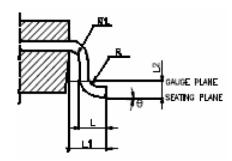


Square Wave Pluse Duration(sec)
Figure 3: Normalized Maximum Transient Thermal Impedance

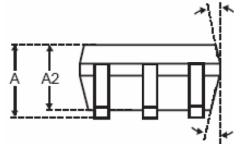


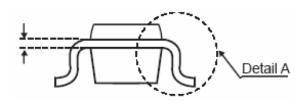
SOT23-6 PACKAGE INFORMATION





Dimensions in Millimeters (UNIT:mm)





CZZ (DOT C	MILLMETERS			
SYMBOLS	MIN.	NOM.	MAX.	
A			1.45	
A1			0.15	
A2	0.90	1.15	1.30	
ь	0.30		0.50	
с	0.08		0.22	
D		2.90 BSC.		
E	2.80 BSC.			
E1	1.60 BSC.			
e	0.95 BSC.			
e1	1.90 BSC.			
L	0.30	0.45	0.60	
L1	0.60 REF			
L2	0.25 BSC.			
R	0.10			
R1	0.10		0.25	
θ	0.	4	8.	
θ 1	5	10	15	

NOTES:

- All dimensions are in millimeters.
 Dimensions are inclusive of plating
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 6 mils.
- 4. Dimension L is measured in gauge plane.
- $5. \ Controlling \ dimension \ is \ millimeter, \ converted \ inch \ dimensions \ are \ not \ necessarily \ exact.$



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