

2N7002ZDW

300mA, 60V DUAL N-CHANNEL ENHANCEMENT MODE POWER MOSFET

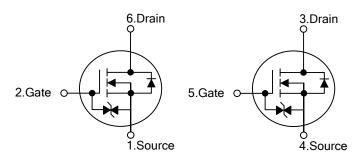
DESCRIPTION

The UTC **2N7002ZDW** uses advanced technology to provide excellent $R_{DS(ON)}$, low gate charge and low gate voltages during operation. This device is suitable for use as a load switch or in PWM applications.

FEATURES

- * Low Reverse Transfer Capacitance (C_{RSS} = typical 3.0 pF)
- * ESD Protected
- * Fast Switching Capability
- * Avalanche Energy Specified
- * Improved dv/dt Capability, High Ruggedness

SYMBOL

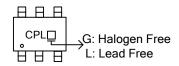


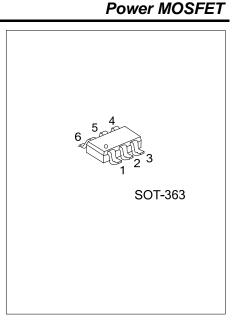
ORDERING INFORMATION

Ordering Number		Deekege	Pin Assignment					Decking		
Lead Free	Halogen Free	Package	1	2	3	4	5	6	Packing	
2N7002ZDWL-AL6-R	2N7002ZDWG-AL6-R	SOT-363	S1	G1	D2	S2	G2	D1	Tape Reel	

2N7002ZDWG-AL6-R (1)Packing Type (2)Package Type (3)Halogen Free	(1) R: Tape Reel (2) AL6: SOT-363 (3) G: Halogen Free, L: Lead Free
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MARKING





2N7002ZDW

■ **ABSOLUTE MAXIMUM RATINGS** (T_A = 25°C, unless otherwise specified.)

PARAMETER		SYMBOL	RATINGS	UNIT	
Drain-Source Voltage		V _{DSS}	60	V	
Gate-Source Voltage		V _{GSS}	±20	V	
Drain Current	Continuous		300		
	Pulse(Note 2)	I _D	800	mA	
Power Dissipation		P	200	mW	
Derating above T _A =25°C		PD	1.6	mW/°C	
Junction Temperature		ΤJ	+150	°C	
Storage Temperature		T _{STG}	-55 ~ +150	°C	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ ELECTRICAL CHARACTERISTICS (T_A=25°C, unless otherwise specified.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =10µA	60			V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =60V, V _{GS} =0V			1.0	μA
Gate-Source Leakage Current	I _{GSS}	$V_{DS}=0V, V_{GS}=\pm 20V$			±10	μA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =10V, I _D =1mA	1.0	1.85	2.5	V
Otatia Duain Ocume On Desistance (Nata)	_	V _{GS} =10V, I _D =0.3A, T _J =125°C			13.5	
Static Drain-Source On-Resistance (Note)	R _{DS(ON)}	V _{GS} =5V, I _D =0.05A			7.5	Ω
DYNAMIC PARAMETERS						
Input Capacitance	CISS			25	50	рF
Output Capacitance	C _{oss}	V _{DS} =25V, V _{GS} =0V, f=1.0MHz		10	25	рF
Reverse Transfer Capacitance	C _{RSS}			3.0	5.0	рF
SWITCHING PARAMETERS						
Turn-ON Delay Time	t _{D(ON)}	I _D =0.2 A, V _{DD} =30V, V _{GS} =10V,		12	20	ns
Turn-OFF Delay Time	t _{D(OFF)}	R _L =150Ω, R _G =10Ω		20	30	ns
DRAIN-SOURCE DIODE CHARACTERIST	ICS AND MA	XIMUM RATINGS				
Drain-Source Diode Forward Voltage	V_{SD}	V _{GS} =0V, Is=300mA (Note)		0.88	1.5	V
Maximum Pulsed Drain-Source Diode	l				0.8	А
Forward Current	I _{SM}				0.0	А
Maximum Continuous Drain-Source Diode	ls				300	mA
Forward Current	13				300	

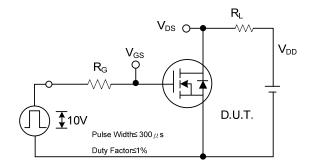
Note: 1. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch. Minimum land pad size.

2. Pulse width ${\leq}300\mu s,$ Duty cycle ${\leq}1\%$

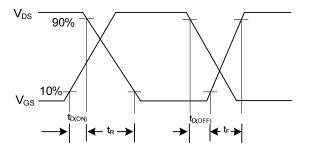


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TEST CIRCUITS AND WAVEFORMS



Switching Test Circuit



Switching Waveforms

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