ARCHIVE INFORMATION

The RF Line NPN Silicon RF Power Transistor

Designed for 24 Volt UHF large–signal, common emitter, class–AB linear amplifier applications in industrial and commercial FM/AM equipment operating in the range 800–970 MHz.

• Specified 24 Volt, 900 MHz Characteristics

Output Power = 30 Watts

Minimum Gain = 10 dB @ 900 MHz, class-AB

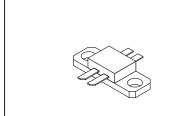
Minimum Efficiency = 30% @ 900 MHz, 30 Watts (PEP)

Maximum Intermodulation Distortion -30 dBc @ 30 Watts (PEP)

- Characterized with Series Equivalent Large—Signal Parameters from 800 to 960 MHz
- Silicon Nitride Passivated
- 100% Tested for Load Mismatch Stress at all Phase Angles with 5:1 VSWR
 26 Vdc, and Rated Output Power
- Gold Metalized, Emitter Ballasted for Long Life and Resistance to Metal— Migration
- Circuit board photomaster available upon request by contacting RF Tactical Marketing in Phoenix, AZ.

MRF897

30 W, 900 MHz RF POWER TRANSISTOR NPN SILICON



CASE 395B-01, STYLE 1

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	V _{CEO}	30	Vdc
Collector–Emitter Voltage	V _{CES}	60	Vdc
Emitter-Base Voltage	V _{EBO}	4.0	Vdc
Collector-Current — Continuous	I _C	4.0	Adc
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	105 0.60	Watts W/°C
Storage Temperature Range	T _{stg}	-65 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{\theta JC}$	1.67	°C/W

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted.)

Output Capacitance ($V_{CB} = 24 \text{ Vdc}$, $I_E = 0$, f = 1.0 MHz)

Characteristic	Symbol	Min	Тур	Max	Unit	
OFF CHARACTERISTICS						
Collector–Emitter Breakdown Voltage (I _C = 50 mAdc, I _B = 0) V _{(BR)CEO} 30 33 —						
Collector–Emitter Breakdown Voltage (I _C = 50 mAdc, V _{BE} = 0)	V _{(BR)CES}	60	80	_	Vdc	
Emitter–Base Breakdown Voltage ($I_E = 5 \text{ mAdc}, I_C = 0$)	_	Vdc				
Collector Cutoff Current (V _{CE} = 30 Vdc, V _{BE} = 0)	I _{CES}	_	_	10.0	mAdc	
ON CHARACTERISTICS						
DC Current Gain (I _{CE} = 1.0 Adc, V _{CE} = 5 Vdc)	h _{FE}	30	80	120	_	
DYNAMIC CHARACTERISTICS						

 C_{ob}

14

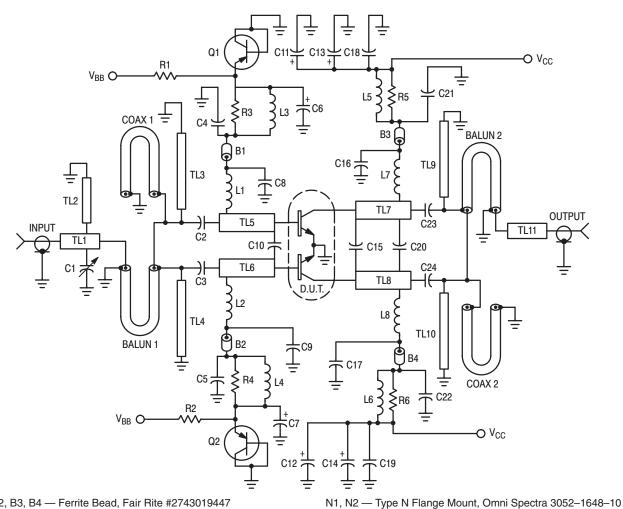
(continued)

REV 6



ELECTRICAL CHARACTERISTICS — **continued** (T_C = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
FUNCTIONAL CHARACTERISTICS					
Common–Emitter Amplifier Power Gain (V_{CC} = 24 Vdc, P_{out} = 30 Watts (PEP), I_{cq} = 125 mA, f_1 = 900 MHz, f_2 = 900.1 MHz)	G _{pe}	10.0	12.0	_	dB
Collector Efficiency (V_{CC} = 24 Vdc, P_{out} = 30 Watts (PEP), I_{cq} = 125 mA, f_1 = 900 MHz, f_2 = 900.1 MHz)	η	35	38	_	%
Intermodulation Distortion (V_{CC} = 24 Vdc, P_{out} = 30 Watts (PEP), I_{cq} = 125 mA, f_1 = 900 MHz, f_2 = 900.1 MHz)	IMD	_	-37	-30	dBc
Output Mismatch Stress (V_{CC} = 26 Vdc, P_{out} = 30 Watts (PEP), I_{cq} = 125 mA, f_1 = 900 MHz, f_2 = 900.1 MHz, Load VSWR = 5:1 (all phase angles))	Ψ	No Degradation in Output Power Before and After Test			



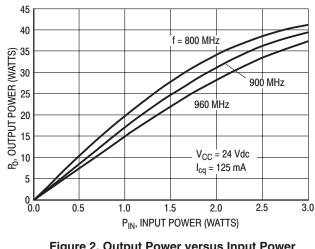
B1, B2, B3, B4 — Ferrite Bead, Fair Rite #2743019447
C1 — 0.8–8.0 pF Trimmer Capacitor, Johanson
C2, C3, C23, C24 — 43 pF, 100 mil, ATC Chip Capacitor
C4, C5, C18, C19, C21, C22 — 820 pF, 100 mil, Chip Capacitor, Kemet
C6, C7, C11, C12 — 10 μ F, Lytic Capacitor, Panasonic
C8, C9, C16, C17 — 100 pF, 100 mil, Chip Capacitor, Murata Erie
C10 — 13 pF, 50 mil, ATC Chip Capacitor
C13, C14 — 250 μ F Lytic Capacitor, Mallory
C15 — 1.1 pF, 50 mil, ATC Chip Capacitor
C20 — 6.8 pF, 100 mil, ATC Chip Capacitor
L1, L2, L3, L4, L5, L6 — 5 Turns 20 AWG, IDIA 0.126" choke

Q1 — Bias Transistor BD136 PNP
R1, R12 — 39 Ohm, 2.0 W
R3, R4, R5, R6 — 4.0 x 39 Ohm, 1/8 W, Chips in Parallel,
Rohm 390–J
TL1–TL11 — See Photomaster
Balun1, Balun2, Coax 1, Coax 2 — 2.20" 50 Ohm, 0.088" o.d.

semi-rigid coax, Micro Coax
UT-85-M17

Board — 1/32" Glass Teflon, Arlon GX–0300–55–22, ϵ_{r} = 2.55

Figure 1. MRF897 Broadband Test Circuit



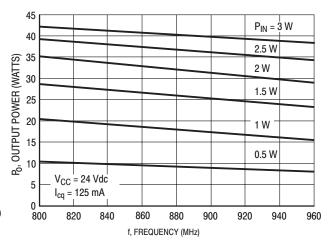


Figure 2. Output Power versus Input Power

Figure 3. Output Power versus Frequency

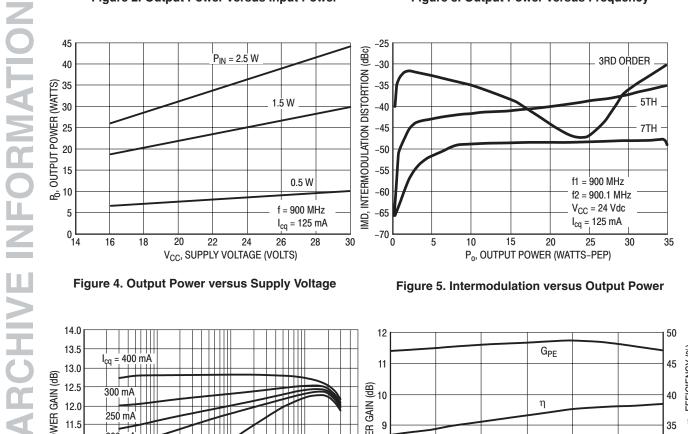


Figure 4. Output Power versus Supply Voltage

Figure 5. Intermodulation versus Output Power

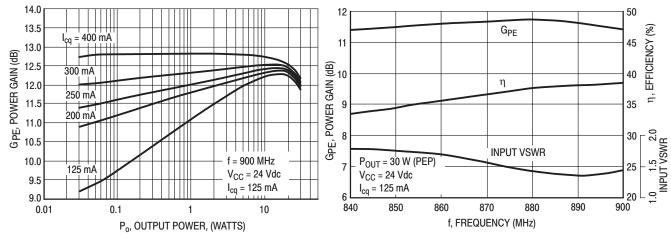


Figure 6. Power Gain versus Output Power

Figure 7. Broadband Test Fixture Performance

MOTOROLA RF DEVICE DATA **MRF897** \mathbf{Z}_{in}

Ohms

1.0 + j10.3

MHz

800

 Z_{OL}^{*}

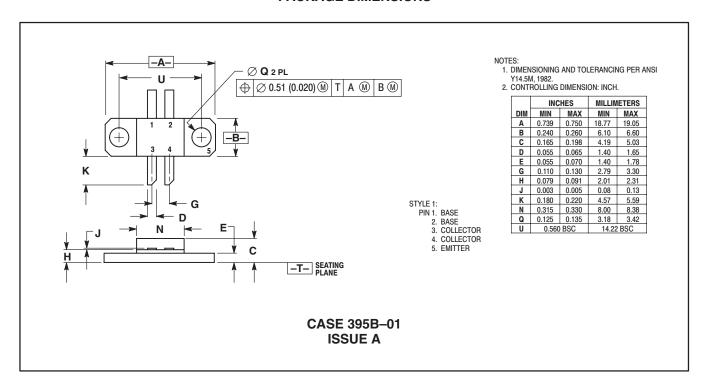
Ohms

5.9 - j0.4

850	1.5 + j10.5	5.7 + j2.6	
900	1.8 + j11.0	5.9 + j3.4	0,10 0,12 0,13 0,14 0,15 0,36 0,37 0,16
960	2.2 + j11.4	6.2 + j4.4	003
into w	gate of the optimu hich the device op t power, voltage ar	d frequency.	960 2 _{in} 960 2 _o 10 Ohms 2 _o 10 Ohms 2 _o 11 Ohms 2 _o 12 Ohms 2 _o 13 Ohms 2 _o 14 Ohms 2 _o 15 Ohms 2 _o 16 Ohms 2 _o 17 Ohms 2 _o 18 Ohms 2 _o
		NOTE: Z _{in} & Z from base–to–	
			ollector respectively.

Figure 8. Series Equivalent Input/Output Impedances

PACKAGE DIMENSIONS



MOTOROLA RF DEVICE DATA MRF897

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