



# UT5003Z

Power MOSFET

## DUAL ENHANCEMENT MODE (N-CHANNEL/P-CHANNEL)

### DESCRIPTION

The **UT5003Z** can provide excellent  $R_{DS(ON)}$  and low gate charge by using UTC's advanced trench technology. This device is suitable for use as a load switch or in PWM applications.

### FEATURES

\* N-Channel: 30V/7A

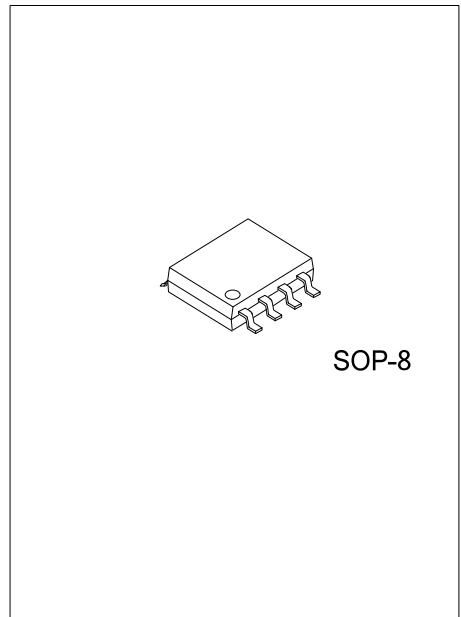
$R_{DS(ON)} = 27.5m\Omega @ V_{GS} = 10V$

$R_{DS(ON)} = 40m\Omega @ V_{GS} = 4.5V$

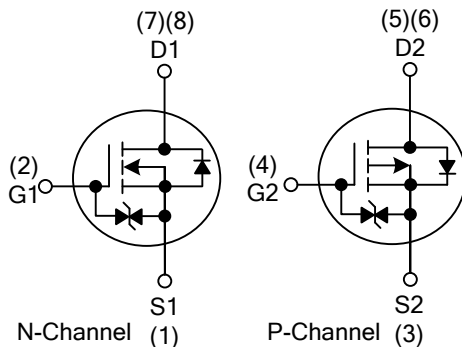
\* P-Channel: -30V/-5A

$R_{DS(ON)} = 45m\Omega @ V_{GS} = -10V$

$R_{DS(ON)} = 80m\Omega @ V_{GS} = -4.5V$



### SYMBOL

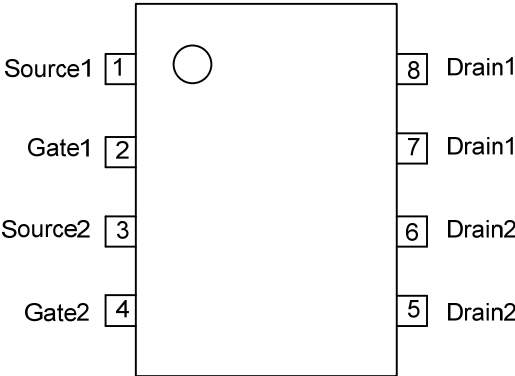


### ORDERING INFORMATION

Ordering Number		Package	Packing
Lead Free	Halogen Free		
UT5003ZL-S08-R	UT5003ZG-S08-R	SOP-8	Tape Reel

<p>UT5003ZL-S08-R</p> <p>(1) Packing Type (2) Package Type (3) Lead Free</p>	<p>(1) R: Tape Reel (2) S08: SOP-8 (3) G: Halogen Free, L: Lead Free</p>
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■ PIN CONFIGURATION



■ ABSOLUTE MAXIMUM RATINGS ( $T_A=25^\circ\text{C}$  unless otherwise specified)

**N-Channel:**

PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	$V_{DSS}$	30	V
Gate-Source Voltage	$V_{GSS}$	$\pm 20$	V
Continuous Drain Current (Note3)	$I_D$	7	A
Pulsed Drain Current (Note3)	$I_{DM}$	20	A
Power Dissipation	$P_D$	2	W
Junction Temperature	$T_J$	+150	$^\circ\text{C}$
Storage Temperature	$T_{STG}$	-55 ~ +150	$^\circ\text{C}$

**P-Channel:**

PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	$V_{DSS}$	-30	V
Gate-Source Voltage	$V_{GSS}$	$\pm 20$	V
Continuous Drain Current (Note3)	$I_D$	-5	A
Pulsed Drain Current (Note3)	$I_{DM}$	-20	A
Power Dissipation	$P_D$	2	W
Junction Temperature	$T_J$	+150	$^\circ\text{C}$
Storage Temperature	$T_{STG}$	-55 ~ +150	$^\circ\text{C}$

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Junction to Ambient (Note3)	$\theta_{JA}$			62.5	$^\circ\text{C}/\text{W}$

■ ELECTRICAL CHARACTERISTICS ( $T_A=25^\circ\text{C}$ , unless otherwise specified)

**N-CHANNEL**

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	30			V
Drain-Source Leakage Current	$I_{DSS}$	$V_{DS}=24\text{V}, V_{GS}=0\text{V}$			1	$\mu\text{A}$
Gate-Source Leakage Current	$I_{GSS}$	$V_{DS}=0\text{V}, V_{GS}=\pm 20\text{V}$			$\pm 5$	$\mu\text{A}$
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	1	1.5	2.5	V
Drain-Source On-State Resistance (Note2)	$R_{DS(ON)}$	$V_{GS}=10\text{V}, I_D=7\text{A}$		20.5	27.5	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}, I_D=6\text{A}$		30	40	$\text{m}\Omega$
<b>DYNAMIC CHARACTERISTICS</b>						
Input Capacitance	$C_{ISS}$	$V_{GS}=0\text{V}, V_{DS}=15\text{V}, f=1\text{MHz}$		680		pF
Output Capacitance	$C_{OSS}$			105		pF
Reverse Transfer Capacitance	$C_{RSS}$			75		pF
<b>SWITCHING CHARACTERISTICS</b>						
Turn-ON Delay Time (Note2)	$t_{D(ON)}$	$V_{DS}=10\text{V}, V_{GS}=10\text{V}, I_D=1\text{A}, R_G=3\Omega$		4.6	7	ns
Turn-ON Rise Time	$t_R$			4	6	ns
Turn-OFF Delay Time	$t_{D(OFF)}$			20	30	ns
Turn-OFF Fall Time	$t_F$			5	8	ns
Total Gate Charge (Note2)	$Q_G$				14	
Gate-Source Charge	$Q_{GS}$	$V_{DS}=0.5 \cdot BV_{DSS}, V_{GS}=10\text{V}, I_D=7\text{A}$		1.9		nC
Gate-Drain Charge	$Q_{GD}$			3.3		nC

### ■ ELECTRICAL CHARACTERISTICS(Cont.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Drain-Source Diode Forward Voltage(Note2)	$V_{SD}$	$I_S=1A, V_{GS}=0V$			1	V
Diode Continuous Forward Current	$I_S$				1.3	A

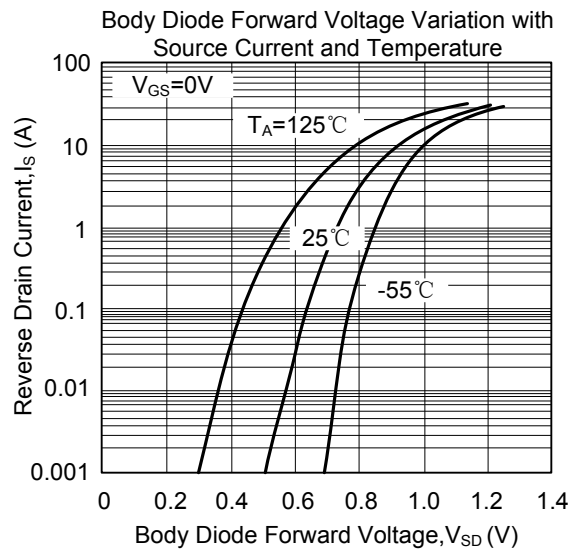
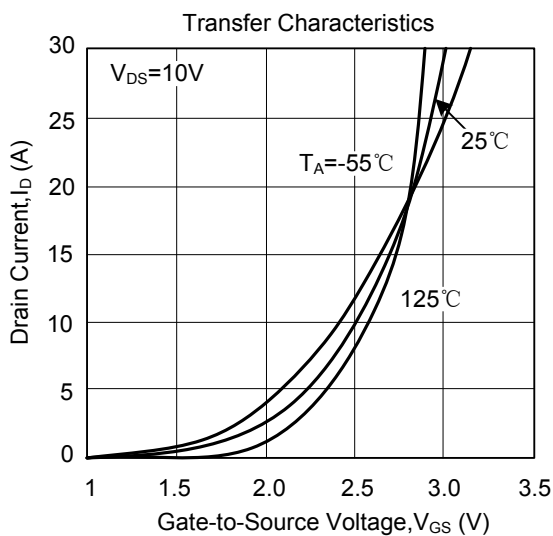
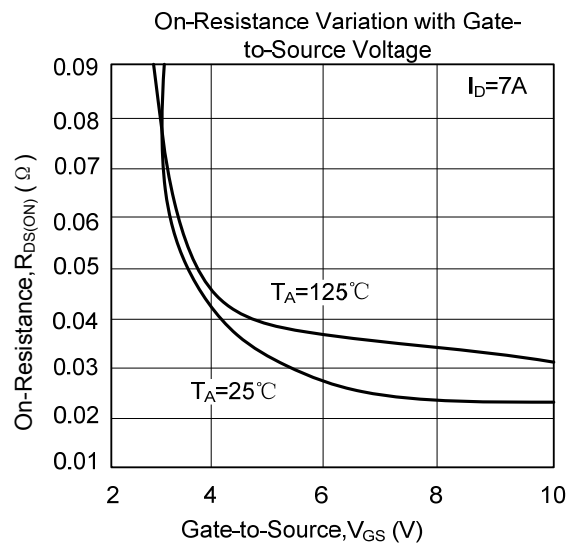
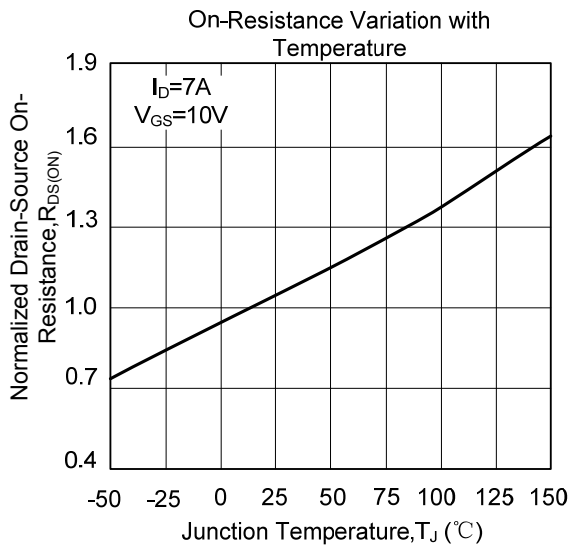
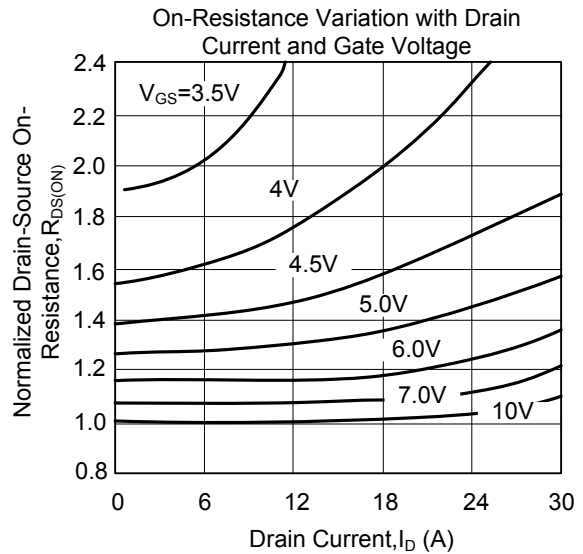
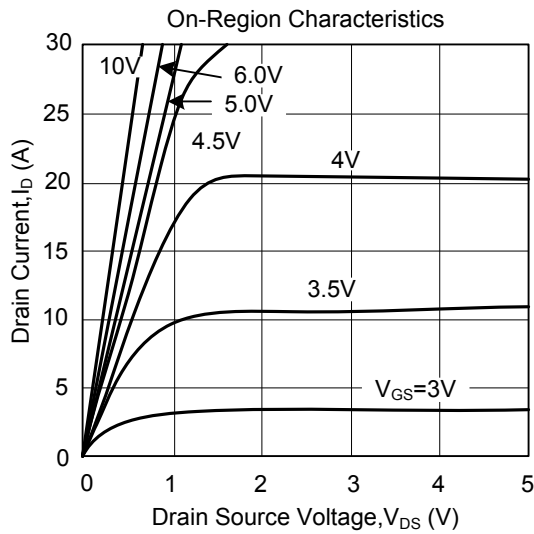
### P-CHANNEL

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=-250\mu A$	-30			V
Drain-Source Leakage Current	$I_{DSS}$	$V_{DS}=-24V, V_{GS}=0V$			-1	$\mu A$
Gate-Source Leakage Current	$I_{GSS}$	$V_{DS}=0V, V_{GS}=\pm 20V$			$\pm 5$	$\mu A$
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-1	-1.5	-2.5	V
Drain-Source On-State Resistance (Note2)	$R_{DS(ON)}$	$V_{GS}=-10V, I_D=-5A$		37.5	45	$m\Omega$
		$V_{GS}=-4.5V, I_D=-4A$		62	80	$m\Omega$
<b>DYNAMIC CHARACTERISTICS</b>						
Input Capacitance	$C_{ISS}$	$V_{GS}=0V, V_{DS}=-15V, f=1MHz$		780		pF
Output Capacitance	$C_{OSS}$			145		pF
Reverse Transfer Capacitance	$C_{RSS}$			79		pF
<b>SWITCHING CHARACTERISTICS</b>						
Turn-ON Delay Time (Note2)	$t_{D(ON)}$	$V_{DS}=-10V, V_{GS}=-10V, I_D=1A, R_G=3\Omega$		7.7	11.5	ns
Turn-ON Rise Time	$t_R$			5.7	8.5	ns
Turn-OFF Delay Time	$t_{D(OFF)}$			20	30	ns
Turn-OFF Fall Time	$t_F$			9.5	14	ns
Total Gate Charge (Note2)	$Q_G$	$V_{DS}=0.5*BV_{DSS}, V_{GS}=-10V, I_D=-5A$		15.1		nC
Gate-Source Charge	$Q_{GS}$			2.1		nC
Gate-Drain Charge	$Q_{GD}$			4.0		nC
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Drain-Source Diode Forward Voltage(Note2)	$V_{SD}$	$I_S=-1A, V_{GS}=0V$			-1	V
Diode Continuous Forward Current	$I_S$				-1.3	A

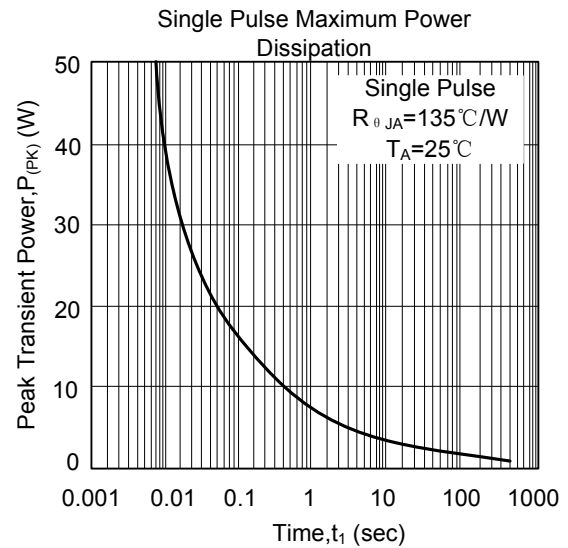
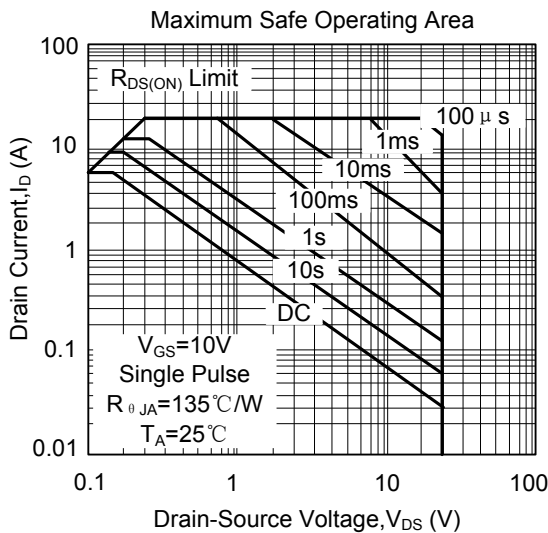
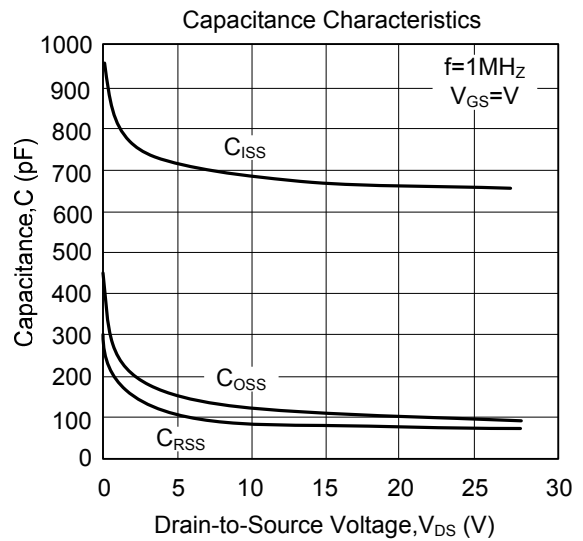
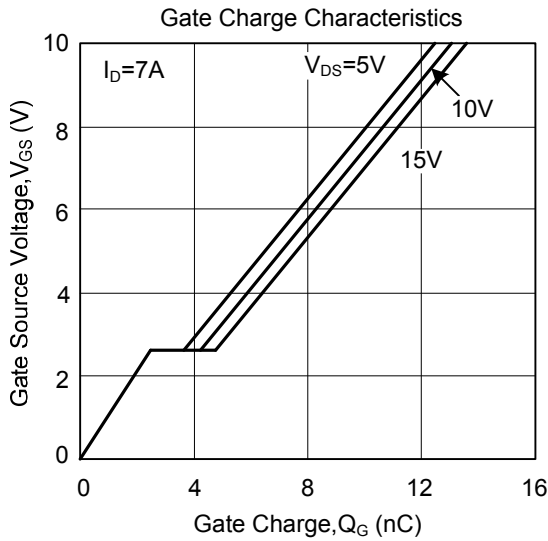
- Notes: 1. Pulse width limited by  $T_{J(MAX)}$   
 2. Pulse width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$ .  
 3. Surface Mounted on  $1in^2$  pad area,  $t \leq 10sec$ .

## TYPICAL CHARACTERISTICS

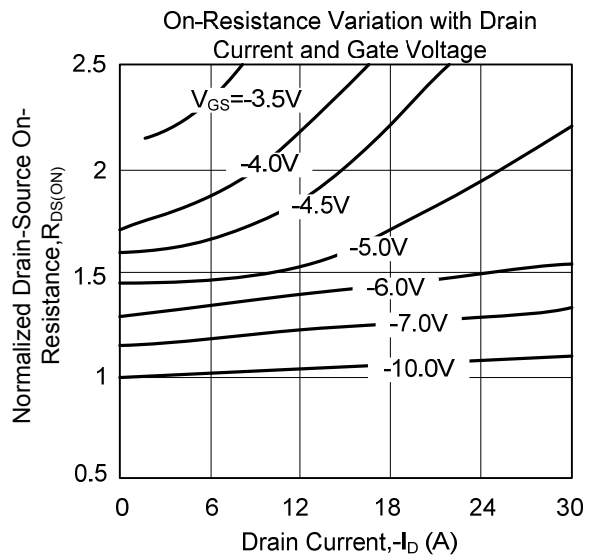
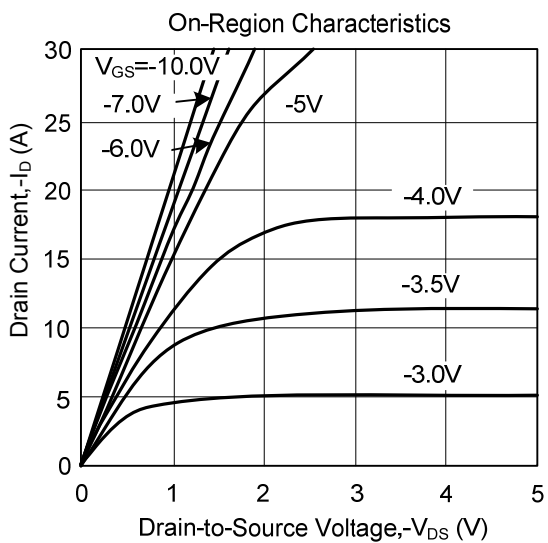
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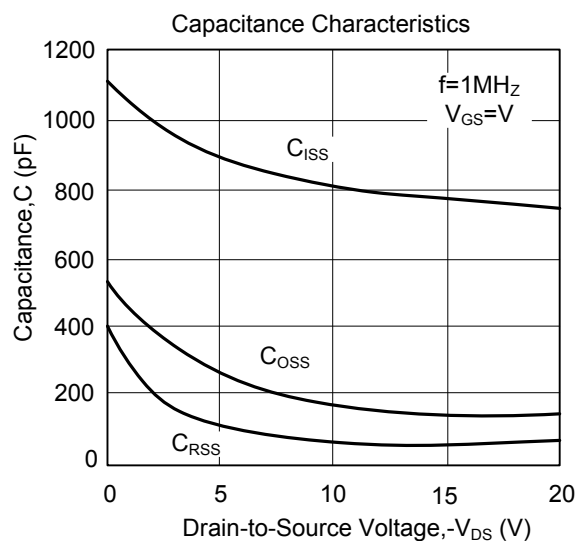
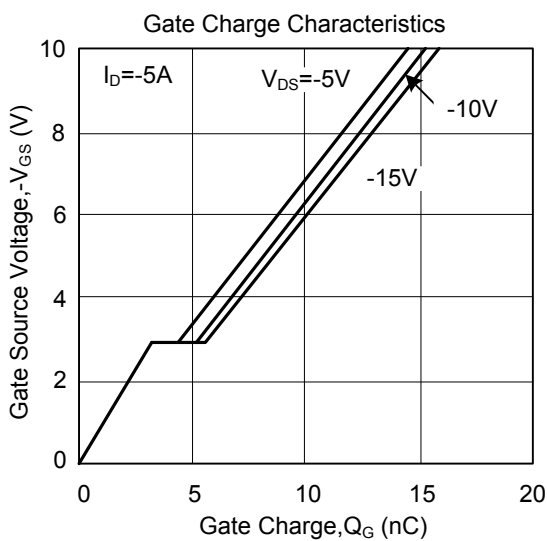
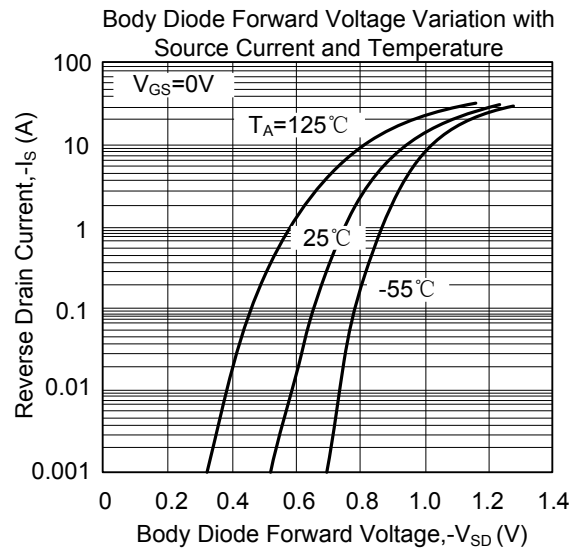
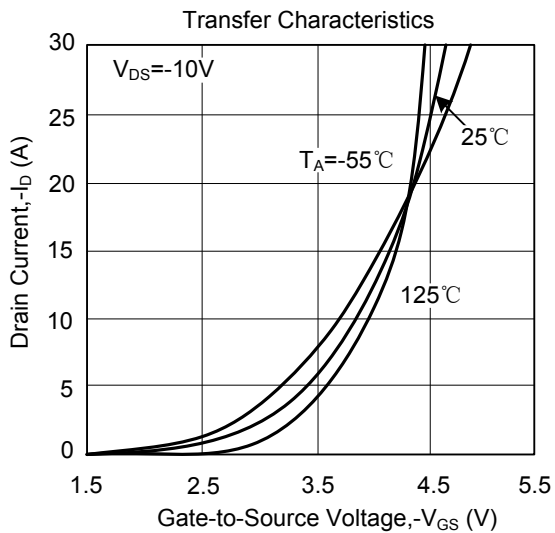
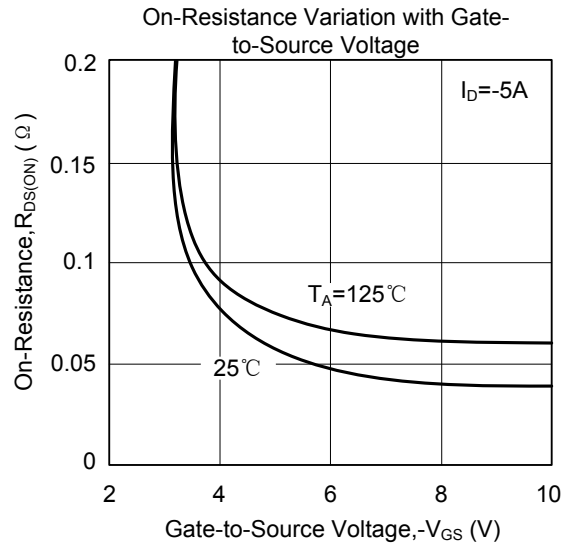
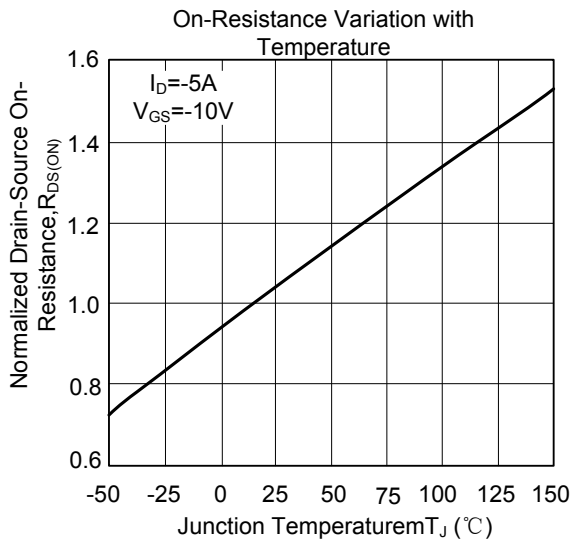
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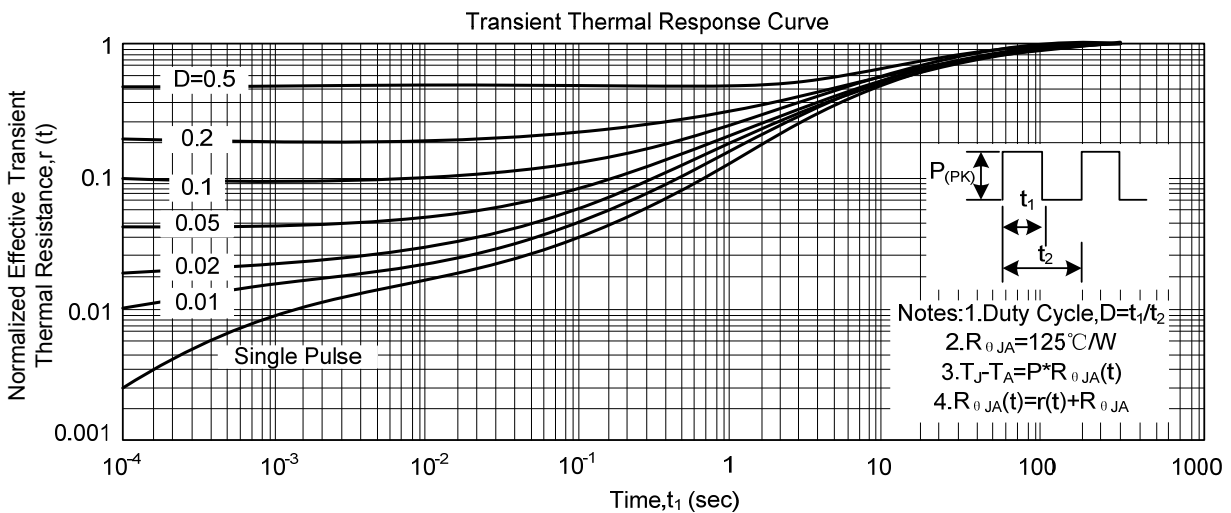
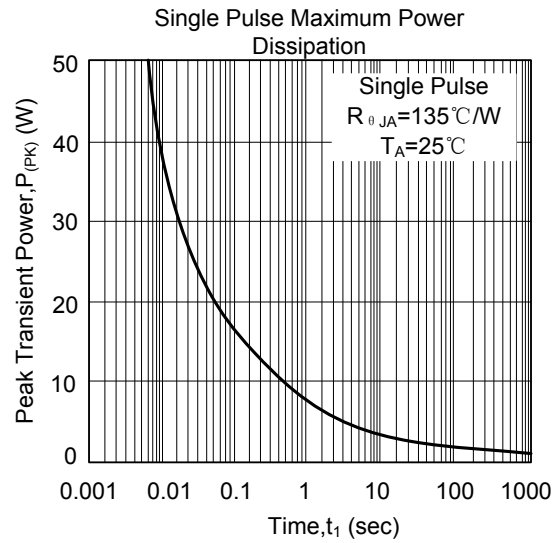
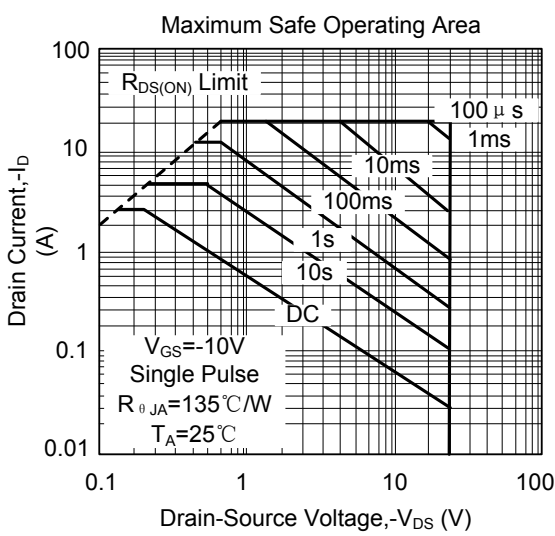
### P-CHANNEL



## TYPICAL CHARACTERISTICS(Cont.)



## ■ TYPICAL CHARACTERISTICS(Cont.)



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