BFU760F NPN wideband silicon germanium RF transistor

Rev. 1 — 29 April 2011

Product data sheet

1. Product profile

1.1 General description

NPN silicon germanium microwave transistor for high speed, low noise applications in a plastic, 4-pin dual-emitter SOT343F package.

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A or equivalent standards.

1.2 Features and benefits

- Low noise high linearity RF transistor
- High maximum output third-order intercept point 32 dBm at 1.8 GHz
- 110 GHz f_T silicon germanium technology

1.3 Applications

- Ka band oscillators DRO's
- High linearity applications
- Medium output power applications
- Wi-Fi / WLAN / WiMAX
- GPS
- ZigBee
- SDARS first stage LNA
- LTE, cellular, UMTS



NPN wideband silicon germanium RF transistor

1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	-	10	V
V _{CEO}	collector-emitter voltage	open base		-	-	2.8	V
V_{EBO}	emitter-base voltage	open collector		-	-	1.0	V
I _C	collector current			-	25	70	mA
P _{tot}	total power dissipation	$T_{sp} \le 90 \ ^{\circ}C$	[1]	-	-	220	mW
h _{FE}	DC current gain	$ I_C = 10 \text{ mA}; \text{V}_{CE} = 2 \text{V}; $		155	330	505	
C _{CBS}	collector-base capacitance	V _{CB} = 2 V; f = 1 MHz		-	175	-	fF
f _T	transition frequency	$I_C = 50 \text{ mA}; V_{CE} = 1 \text{ V};$ f = 2 GHz; T _{amb} = 25 °C		-	45	-	GHz
G _{p(max)}	maximum power gain	I_C = 50 mA; V_{CE} = 1 V; f = 2.4 GHz; T_{amb} = 25 °C	[2]	-	22	-	dB
NF	noise figure	$I_{C} = 12 \text{ mA}; V_{CE} = 2 \text{ V};$ f = 2.4 GHz; $\Gamma_{S} = \Gamma_{opt}$		-	0.50	-	dB
IP3	third-order intercept point	$ \begin{split} &I_{C} = 30 \text{ mA; } V_{CE} = 2.5 \text{ V;} \\ &Z_{S} = Z_{L} = 50 \ \Omega; \\ &f = 2.4 \text{ GHz; } T_{amb} = 25 \ ^{\circ}\text{C} \end{split} $		-	32	-	dBm

[1] T_{sp} is the temperature at the solder point of the emitter lead.

[2] $G_{p(max)}$ is the maximum power gain, if K > 1. If K < 1 then $G_{p(max)}$ = Maximum Stable Gain (MSG).

2. Pinning information

Table 2.	Discrete pinning		
Pin	Description	Simplified outline	Graphic symbol
1	emitter		
2	base		4
3	emitter		2
4	collector		1, 3 mbb159

3. Ordering information

Table 3. Ordering information							
Type number	Package						
	Name	Description	Version				
BFU760F	-	plastic surface-mounted flat pack package; reverse pinning; 4 leads	SOT343F				

BFU760F Product data sheet

NPN wideband silicon germanium RF transistor

4. Marking

Table 4. Marking		
Type number	Marking	Description
BFU760F	D7*	* = p : made in Hong Kong
		* = t : made in Malaysia
		* = w : made in China

5. Limiting values

Table 5.	Limiting values
----------	-----------------

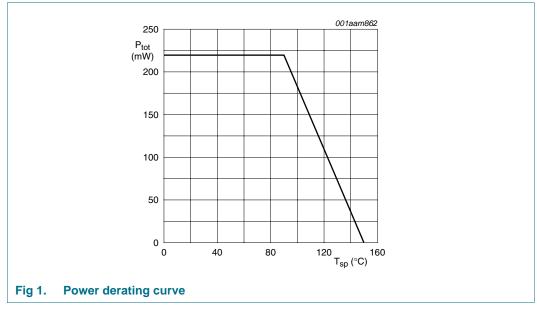
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter	-	10	V
V _{CEO}	collector-emitter voltage	open base	-	2.8	V
V _{EBO}	emitter-base voltage	open collector	-	1.0	V
I _C	collector current		-	70	mA
P _{tot}	total power dissipation	$T_{sp} \le 90 \ ^{\circ}C$	<u>[1]</u> -	220	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		-	150	°C

[1] T_{sp} is the temperature at the solder point of the emitter lead.

6. Thermal characteristics

Table 6.	Thermal characteristics			
Symbol	Parameter	Conditions	Тур	Unit
R _{th(j-sp)}	thermal resistance from junction to solder point		270	K/W



All information provided in this document is subject to legal disclaimers

BFU760F

NPN wideband silicon germanium RF transistor

7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	$I_{C} = 2.5 \ \mu A; I_{E} = 0 \ mA$	10	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	$I_{C} = 1 \text{ mA}; I_{B} = 0 \text{ mA}$	2.8	-	-	V
lc	collector current		-	25	70	mA
I _{CBO}	collector-base cut-off current	$I_E = 0 \text{ mA}; V_{CB} = 4.5 \text{ V}$	-	-	100	nA
h _{FE}	DC current gain	$I_{C} = 10 \text{ mA}; V_{CE} = 2 \text{ V}$	155	330	505	
C _{CES}	collector-emitter capacitance	V _{CB} = 2 V; f = 1 MHz	-	292	-	fF
C _{EBS}	emitter-base capacitance	V _{EB} = 0.5 V; f = 1 MHz	-	1054	-	fF
C _{CBS}	collector-base capacitance	V _{CB} = 2 V; f = 1 MHz	-	175	-	fF
f _T	transition frequency	I_{C} = 50 mA; V_{CE} = 1 V; f = 2 GHz; T_{amb} = 25 °C	-	45	-	GHz
G _{p(max)}	maximum power gain	I_{C} = 50 mA; V_{CE} = 1 V; T_{amb} = 25 °C	<u>[1]</u>			
		f = 1.5 GHz	-	25.5	-	dB
		f = 1.8 GHz	-	24	-	dB
		f = 2.4 GHz	-	22	-	dB
		f = 5.8 GHz	-	13.5	-	dB
s ₂₁ ²	insertion power gain	I_{C} = 50 mA; V_{CE} = 1 V; T_{amb} = 25 °C				
		f = 1.5 GHz	-	22	-	dB
		f = 1.8 GHz	-	20.5	-	dB
		f = 2.4 GHz	-	18	-	dB
		f = 5.8 GHz	-	10.5	-	dB
NF	noise figure	I_{C} = 12 mA; V_{CE} = 2 V; Γ_{S} = Γ_{opt} ; T_{amb} = 25 °C				
		f = 1.5 GHz	-	0.40	-	dB
		f = 1.8 GHz	-	0.45	-	dB
		f = 2.4 GHz	-	0.50	-	dB
		f = 5.8 GHz	-	0.75	-	dB
G _{ass}	associated gain	I_{C} = 12 mA; V_{CE} = 2 V; Γ_{S} = Γ_{opt} ; T_{amb} = 25 °C				
		f = 1.5 GHz	-	23	-	dB
		f = 1.8 GHz	-	21.5	-	dB
		f = 2.4 GHz	-	19.5	-	dB
		f = 5.8 GHz	-	12.5	-	dB
P _{L(1dB)}	output power at 1 dB gain compression	$\label{eq:lc} \begin{array}{l} I_{C} = 30 \text{ mA}; \ V_{CE} = 2.5 \text{ V}; \\ Z_{S} = Z_{L} = 50 \ \Omega; \ T_{amb} = 25 \ ^{\circ}C \end{array}$				
		f = 1.5 GHz	-	18	-	dBm
		f = 1.8 GHz	-	18	-	dBm
		f = 2.4 GHz	-	17	-	dBm
		f = 5.8 GHz	-	18.5	-	dBm

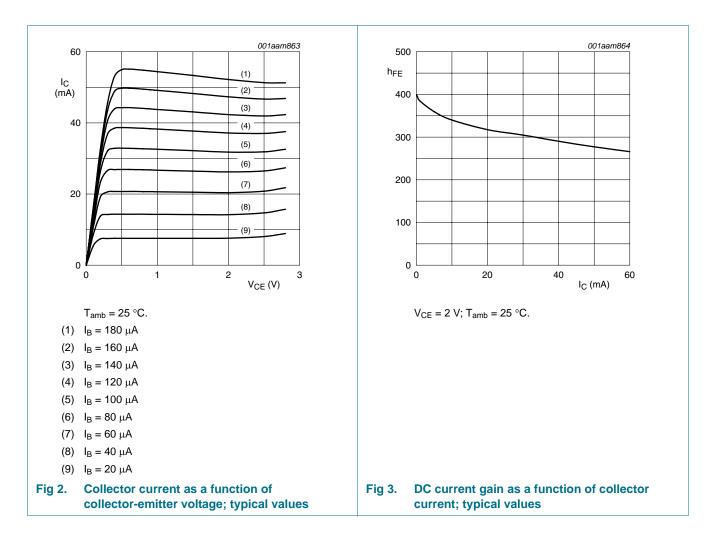
NPN wideband silicon germanium RF transistor

Table 7. Characteristics ...continued

 $T_j = 25 \text{ °C}$ unless otherwise specified

,						
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
IP3	third-order intercept point	$\label{eq:lc} \begin{array}{l} I_{C} = 30 \text{ mA}; \ V_{CE} = 2.5 \text{ V}; \\ Z_{S} = Z_{L} = 50 \ \Omega; \ T_{amb} = 25 \ ^{\circ}C \end{array}$				
		f = 1.5 GHz	-	32	-	dBm
		f = 1.8 GHz	-	32	-	dBm
		f = 2.4 GHz	-	32	-	dBm
		f = 5.8 GHz	-	33	-	dBm
-						

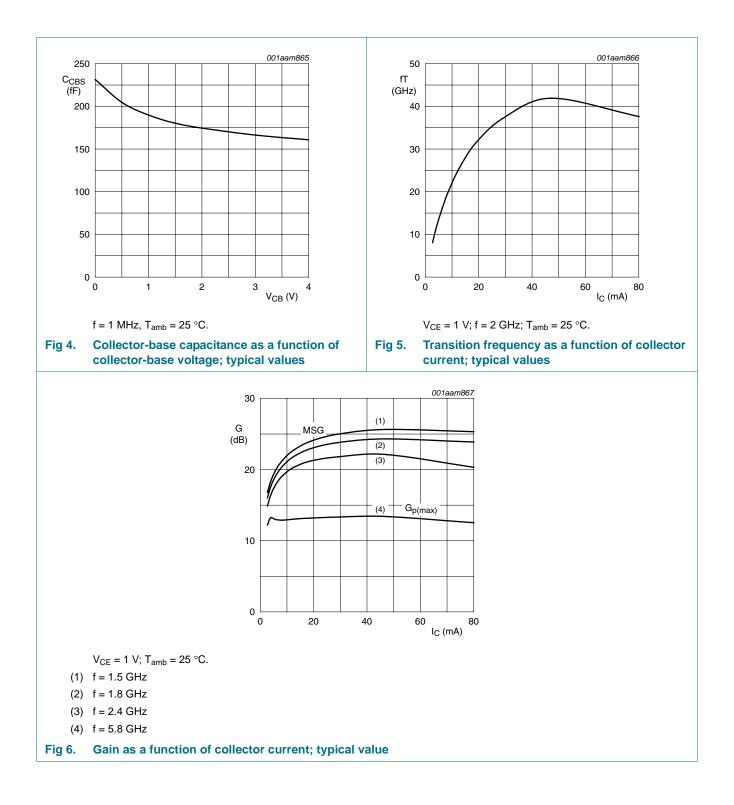
 $[1] \quad G_{p(max)} \text{ is the maximum power gain, if } K > 1. \text{ If } K < 1 \text{ then } G_{p(max)} = MSG.$



NXP Semiconductors

BFU760F

NPN wideband silicon germanium RF transistor

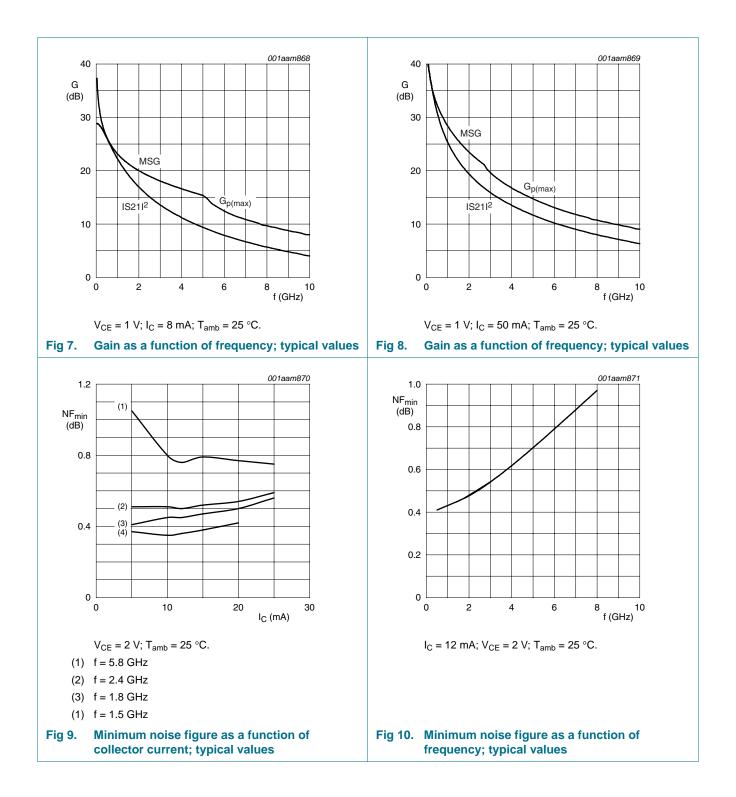


BFU760F

NXP Semiconductors

BFU760F

NPN wideband silicon germanium RF transistor



NPN wideband silicon germanium RF transistor

8. Package outline

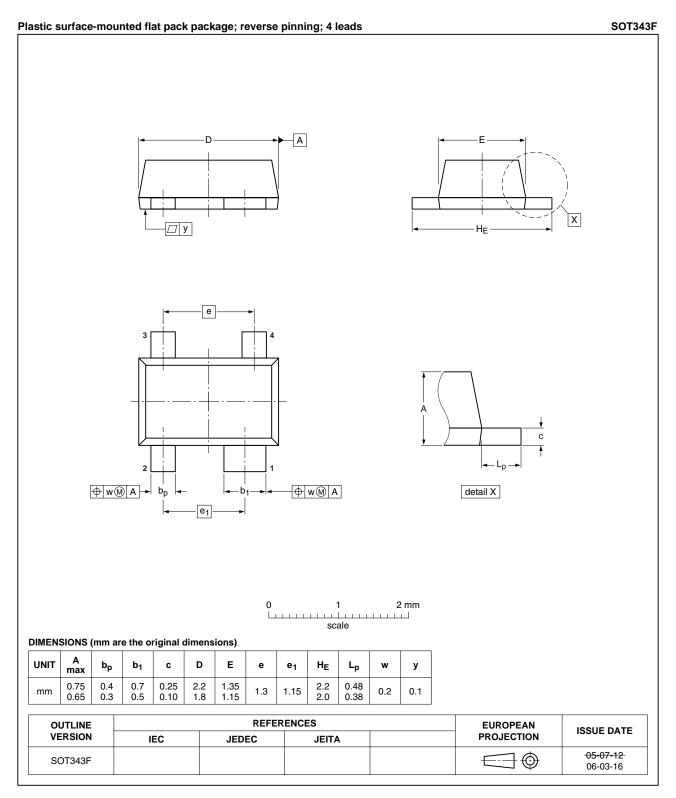


Fig 11. Package outline SOT343F

All information provided in this document is subject to legal disclaimers.

BFU760F

NPN wideband silicon germanium RF transistor

9. Abbreviations

Table 8.	Abbreviations
Acronym	Description
DBS	Direct Broadcast Satellite
DC	Direct Current
DRO	Dielectric Resonator Oscillator
GPS	Global Positioning System
Ka	Kurtz above
LNA	Low Noise Amplifier
LNB	Low Noise Block
LTE	Long Term Evolution
NPN	Negative-Positive-Negative
RF	Radio Frequency
SDARS	Satellite Digital Audio Radio Service
UMTS	Universal Mobile Telecommunications System
WiMAX	Worldwide Interoperability for Microwave Access
WLAN	Wireless Local Area Network

10. Revision history

Table 9. Revision his	Revision history							
Document ID	Release date	Data sheet status	Change notice	Supersedes				
BFU760F v.1	20110429	Product data sheet	-	-				

9 of 12

NPN wideband silicon germanium RF transistor

11. Legal information

11.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

11.2 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local NXP Semiconductors sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between NXP Semiconductors and its customer, unless NXP Semiconductors and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the NXP Semiconductors product is deemed to offer functions and qualities beyond those described in the Product data sheet.

11.3 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nxp.com/profile/terms, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. NXP Semiconductors hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of NXP Semiconductors products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from national authorities.

BFU760F

© NXP B.V. 2011. All rights reserved.

NPN wideband silicon germanium RF transistor

Non-automotive qualified products — Unless this data sheet expressly states that this specific NXP Semiconductors product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. NXP Semiconductors accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without NXP Semiconductors' warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond NXP Semiconductors' specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies NXP Semiconductors for any

12. Contact information

liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond NXP Semiconductors' standard warranty and NXP Semiconductors' product specifications.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

11.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

For more information, please visit: http://www.nxp.com

For sales office addresses, please send an email to: salesaddresses@nxp.com

NPN wideband silicon germanium RF transistor

13. Contents

1	Product profile 1
1.1	General description 1
1.2	Features and benefits 1
1.3	Applications 1
1.4	Quick reference data 2
2	Pinning information 2
3	Ordering information 2
4	Marking 3
5	Limiting values 3
6	Thermal characteristics 3
7	Characteristics 4
8	Package outline 8
9	Abbreviations 9
10	Revision history 9
11	Legal information 10
11.1	Data sheet status 10
11.2	Definitions 10
11.3	Disclaimers
11.4	Trademarks 11
12	Contact information 11
13	Contents 12

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.

© NXP B.V. 2011.

All rights reserved.

For more information, please visit: http://www.nxp.com For sales office addresses, please send an email to: salesaddresses@nxp.com

Date of release: 29 April 2011 Document identifier: BFU760F