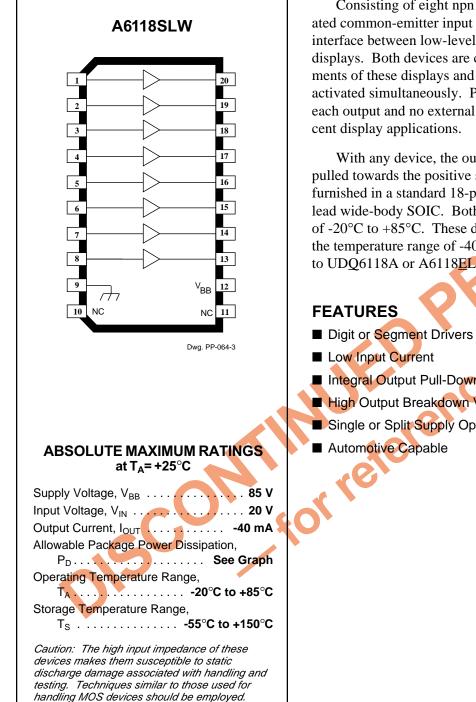
6118

VACUUM FLUORESCENT **DISPLAY DRIVER**



Consisting of eight npn Darlington output stages and the associated common-emitter input stages, these drivers are designed to interface between low-level digital logic and vacuum fluorescent displays. Both devices are capable of driving the digits and/or segments of these displays and are designed to permit all outputs to be activated simultaneously. Pull-down resistors are incorporated into each output and no external components are required for most fluores-

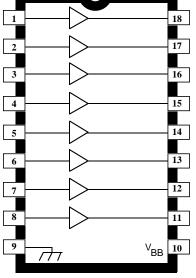
With any device, the output load is activated when the input is pulled towards the positive supply (active 'high'). The UDN6118A is furnished in a standard 18-pin plastic DIP; the A6118SLW is in a 20lead wide-body SOIC. Both units operate over the temperature range of -20°C to +85°C. These devices are also available for operation over the temperature range of -40° C to $+85^{\circ}$ C by changing the part number to UDQ6118A or A6118ELW.

- Integral Output Pull-Down Resistors
- High Output Breakdown Voltage
- Single or Split Supply Operation

Always order by complete part number, e.g., UDN6118A.

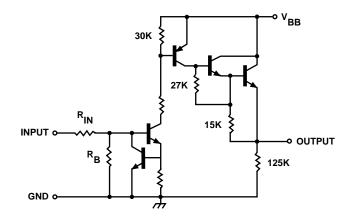






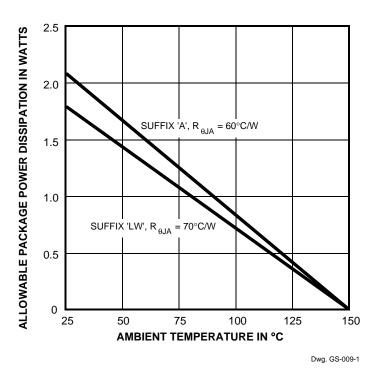
Dwg. PP-065

PARTIAL SCHEMATIC ONE DRIVER (ALL TYPES)



Dwg. No. A-10,592C

R _{IN}	R _B
10 kΩ	30 kΩ



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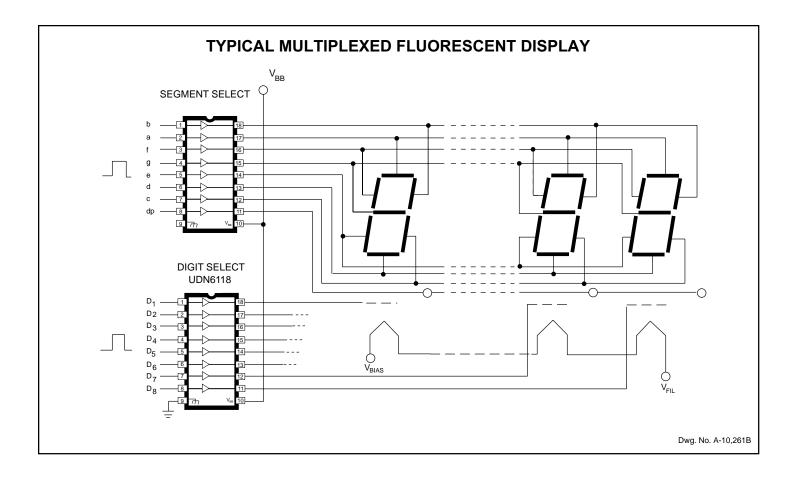
ELECTRICAL CHARACTERISTICS (over operating temperature range) at $V_{_{BB}}$ = 80 V.

			Limits			
Characteristic	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Output Leakage Current	I _{OUT}	V _{IN} = 0.4 V	_	_	15	μA
Output OFF Voltage	V _{OUT}	V _{IN} = 0.4 V	_	_	1.0	V
Output Pull-Down Current	I _{OUT}	Input Open, V _{OUT} = V _{BB}	450	650	1100	μA
Output ON Voltage	V _{OUT}	V _{IN} = 2.4 V, I _{OUT} = -25 mA	77	78	_	V
Input ON Current	I _{IN}	V _{IN} = 2.4 V	—	120	225	μA
		V _{IN} = 5.0 V	_	375	650	μA
Supply Current	I _{BB}	All Inputs Open	—	10	100	μA
		All Inputs = 2.4 V	_	6.0	9.0	mA

RECOMMENDED OPERATING CONDITIONS

			Limits			
Characteristic	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Supply Voltage	V _{BB}		5.0	_	70	V
Input ON Voltage	V _{IN}		2.4	_	15	V
Output ON Current	I _{OUT}		—	_	-25	mA

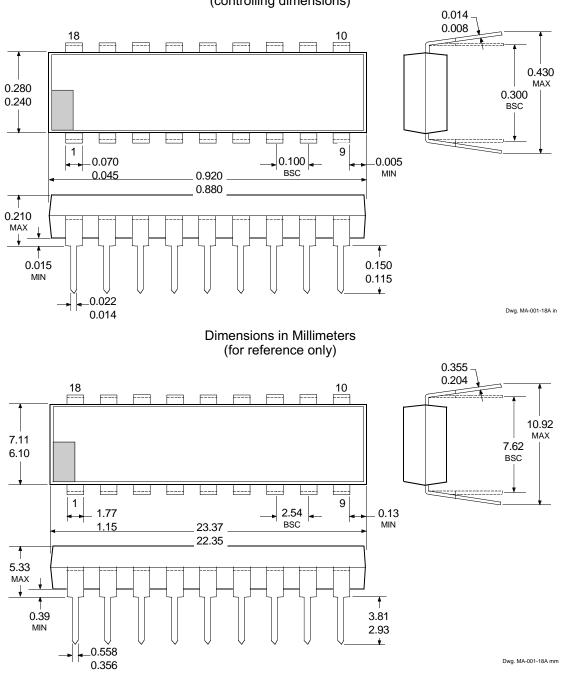
NOTE: Positive (negative) current is defined as going into (coming out of) the specified device terminal.





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UDN6118A

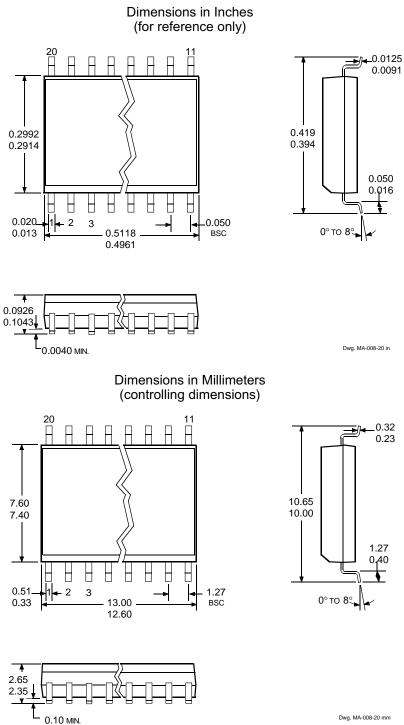


Dimensions in Inches (controlling dimensions)

NOTES: 1. Exact body and lead configuration at vendor's option within limits shown.

- 2. Lead spacing tolerance is non-cumulative.
- 3. Lead thickness is measured at seating plane or below.
- 4. Supplied in standard sticks/tubes of 21 devices.

A6118SLW



NOTES:1. Exact body and lead configuration at vendor's option within limits shown.

- 2. Lead spacing tolerance is non-cumulative.
 - Supplied in standard sticks/tubes of 37 devices or add "TR" to part number for tape and reel. 3.



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Dwg. MA-008-20 mm

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HIGH-VOLTAGE (≥60 V) PERIPHERAL POWER AND DISPLAY DRIVERS

IN ORDER OF 1) OUTPUT VOLTAGE, 2) OUTPUT CURRENT, 3) NUMBER OF DRIVERS

Output Ratings*		Features						
v	mA	#	Serial Input	Latched Drivers	Diode Clamp	Saturated Outputs	Internal Protection	Part Number †
60	-25	8	_	Х	_	-	_	5815
	-25	10	х	х	Active Pull-Dow	/n –	_	5810-F and 6810
	-25	12	х	Х	Active Pull-Dow	/n –	_	5811 and 6811
	-25	20	х	х	Active Pull-Dow	/n –	_	5812-F and 6812
	-25	32	х	Х	Active Pull-Dow	/n –	_	5818-F and 6818
	300	4	_	_	Х	Х	х	2557
	600	4	_	_	_	Х	Х	2547
	600	4	_	_	Х	Х	х	2549
	700	4	_	_	Х	Х	х	2559
	700	4	_	_	Х	Х	Х	2543
	4000	4	_	_	Х	_	_	2944
80	-350	8	_	_	Х	_	_	2983
	350	8	Х	Х	_	_	_	5822
	350	8	Х	Х	Х	-	_	5842
	-350	8	Х	Х	Х	_	_	5890
95	300	7	_	_	Х	_	_	2023
	350	7	_	-	х	-	_	2024

* Current is maximum test condition; voltage is absolute maximum allowable. Negative current is defined as coming out of (sourcing) the output.

[†] Complete part number includes additional characters to indicate operating temperature range and package style.



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