



# MMBD4448HCQW /AQW /ADW /CDW /SDW /TW

#### SURFACE MOUNT FAST SWITCHING DIODE ARRAY

#### **Features**

- Fast Switching Speed
- Small Surface Mount Package
- For General Purpose Switching Applications
- **High Conductance**
- Lead Free/RoHS Compliant (Note 1)
- Qualified to AEC-Q101 Standards for High Reliability
- "Green" Device (Notes 2 and 3)

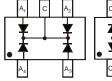
#### **Mechanical Data**

- Case: SOT-353 or SOT-363
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Alloy 42 leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Orientation: See Diagrams Below
- Weight: 0.006 grams (approximate)

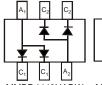
SOT-353/SOT-363

















SOT-353 TOP VIEW

SOT-363 TOP VIEW

MMBD4448HCQW

MMBD4448HAQW

MMBD4448HADW

MMBD4448HCDW

MMBD4448HSDW

MMBD4448HTW

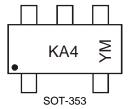
## **Ordering Information** (Note 4)

Part Number	Case	Packaging
MMBD4448HADW-7-F	SOT-363	3000/Tape & Reel
MMBD4448HAQW-7-F	SOT-363	3000/Tape & Reel
MMBD4448HCDW-7-F	SOT-363	3000/Tape & Reel
MMBD4448HCQW-7-F	SOT-353	3000/Tape & Reel
MMBD4448HSDW-7-F	SOT-363	3000/Tape & Reel
MMBD4448HTW-7-F	SOT-363	3000/Tape & Reel

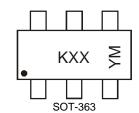
Notes:

- 1. No purposefully added lead.
- 2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com.
- 3. Product manufactured with Date Code UO (week 40, 2007) and newer are built with Green Molding Compound. Product manufactured prior to Date Code UO are built with Non-Green Molding Compound and may contain Halogens or Sb<sub>2</sub>O<sub>3</sub> Fire Retardants.
- 4. For packaging details, go to our website at http://www.diodes.com.

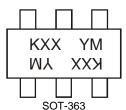
# **Marking Information**



KA4 = Product Type Marking Code, KA4 = MMBD4448HCQW YM = Date Code Marking Y = Year (ex: N = 2002)M = Month (ex: 9 = September)



KXX = Product Type Marking Code, ex. KA5 = MMBD4448HAQW KAA = MMBD4448HTW YM = Date Code Marking Y = Year (ex: N = 2002)M = Month (ex: 9 = September)



KXX = Product Type Marking Code, ex. KA6 = MMBD4448HADW KA7 = MMBD4448HCDW KAB = MMBD4448HSDW YM = Date Code Marking Y = Year (ex: N = 2002)M = Month (ex: 9 = September)

Date Code Kev

Year	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015
Code	L	М	N	Р	R	S	Т	U	V	W	Χ	Υ	Z	Α	В	С
Month	Jan	F	eb	Mar	Apr	М	ay	Jun	Jul	Αι	ıg	Sep	Oct	N	ov	Dec
Code	1	:	2	3	4		5	6	7	3	3	9	0	1	١	D



# **Maximum Ratings** @ $T_A = 25$ °C unless otherwise specified

Characteristic		Symbol	Value	Unit
Non-Repetitive Peak Reverse Voltage		$V_{RM}$	100	V
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage		V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	80	V
RMS Reverse Voltage		$V_{R(RMS)}$	57	V
Forward Continuous Current (Note 5)		I <sub>FM</sub>	500	mA
Average Rectified Output Current (Note 5)		lo	250	mA
Non-Repetitive Peak Forward Surge Current	@ t = 1.0μs @ t = 1.0s	I <sub>FSM</sub>	4.0 1.0	А

## **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	$P_{D}$	200	mW
Thermal Resistance Junction to Ambient Air (Note 5)	$R_{ hetaJA}$	625	°C/W
Operating and Storage Temperature Range	$T_J$ , $T_{STG}$	-65 to +150	°C

## Electrical Characteristics @TA = 25°C unless otherwise specified

Characteristic	Symbol	Min	Max	Unit	Test Condition							
Reverse Breakdown Voltage (Note 6)	$V_{(BR)R}$	80	_	V	$I_R = 100 \mu A$							
		0.62	0.72	V	$I_F = 5.0 \text{mA}$							
Forward Voltage	\/_	_	0.855		$I_F = 10 \text{mA}$							
Forward voilage	V <sub>F</sub>	_	1.0		I <sub>F</sub> = 100mA							
			1.25		I <sub>F</sub> = 150mA							
	I <sub>R</sub>		100	nA	$V_R = 70V$							
Reverse Current (Note 6)		$I_R$	$I_R$		50	μΑ	$V_R = 75V, T_J = 150^{\circ}C$					
Reverse Current (Note 6)				IR	IR	IR	IR	IR	IR.	IK		30
			25	nA	$V_R = 20V$							
Total Capacitance	Ст	_	3.5	pF	V <sub>R</sub> = 6V, f = 1.0MHz							
Reverse Recovery Time	t <sub>rr</sub>		4.0	ns	$V_R = 6V, I_F = 5mA$							

Notes:

- 5. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com.
- 6. Short duration pulse test used to minimize self-heating effect.

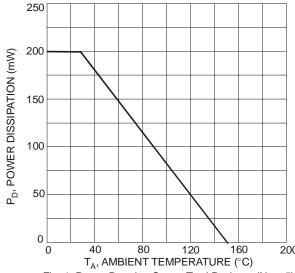
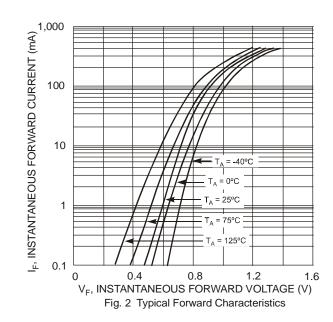
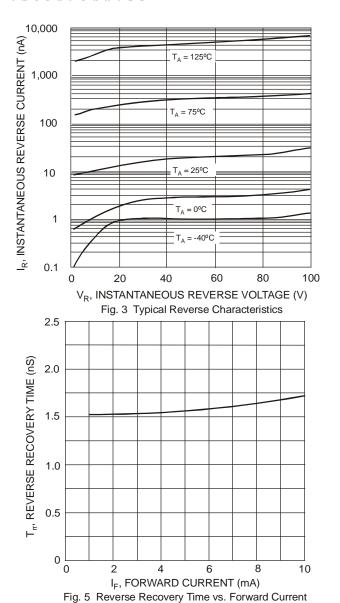
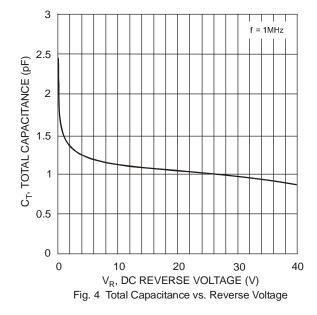


Fig. 1 Power Derating Curve, Total Package (Note 5)

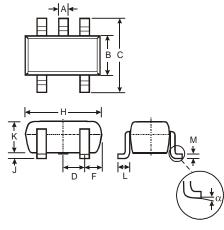




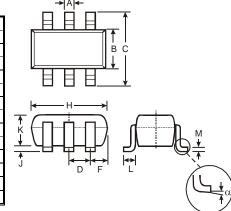




# Package Outline Dimensions



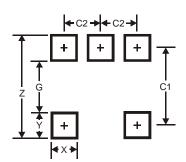
SOT-353					
Dim	Min	Max			
Α	0.10	0.30			
В	1.15	1.35			
С	2.00	2.20			
D	0.65	Тур			
F	0.40	0.45			
Н	1.80	2.20			
J	0	0.10			
K	0.90	1.00			
١	0.25	0.40			
М	0.10	0.22			
α	0°	8°			
All Di	mensions	in mm			



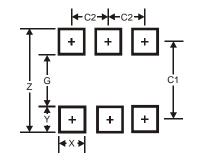
SO1-363					
Dim	Min	Max			
Α	0.10	0.30			
В	1.15	1.35			
С	2.00	2.20			
D	0.65	Тур			
F	0.40	0.45			
Н	1.80	2.20			
J	0	0.10			
K	0.90	1.00			
L	0.25	0.40			
M	0.10	0.22			
α	0°	8°			
All Di	mensions	in mm			



#### Suggested Pad Layout



SOT-353		
Dimensions	Value (in mm)	
Z	2.5	
G	1.3	
Х	0.42	
Y	0.6	
C1	1.9	
C2	0.65	



SOT-363		
Dimensions	Value (in mm)	
Z	2.5	
G	1.3	
Х	0.42	
Y	0.6	
C1	1.9	
C2	0.65	

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