

DATA SHEET

SURFACE-MOUNT CERAMIC MULTILAYER CAPACITORS

General purpose & High capacitance

Class 2, Y5V 6.3 V TO 50 V I0 nF to 47 μF RoHS compliant & Halogen Free



YAGEO Phicomp

Surface-Mount Ceramic Multilayer Capacitors General Purpose & High Cap. Y5V 6.3 V to 50 V

<u>SCOPE</u>

This specification describes Y5V series chip capacitors with lead-free terminations.

APPLICATIONS

- Consumer electronics, for example:
 - Tuners
 - Television receivers
 - Video recorders
 - All types of cameras
 - Mobile telephones

FEATURES

- Supplied in tape on reel
- Nickel-barrier end termination
- RoHS compliant
- Halogen Free compliant

ORDERING INFORMATION - GLOBAL PART NUMBER, PHYCOMP

CTC & 12NC

All part numbers are identified by the series, size, tolerance, TC material, packing style, voltage, process code, termination and capacitance value.

YAGEO BRAND ordering code

GLOBAL PART NUMBER (PREFERRED)

CC	<u>xxxx</u>	<u>X</u>	<u>x</u>	Y5V	<u>X</u>	BB	<u>xxx</u>
	(1)	(2)	(3)		(4)		(5)

(I) SIZE - INCH BASED (METRIC)

0201 (0603) 0402 (1005) 0603 (1608) 0805 (2012) 1206 (3216) 1210 (3225)

(2) TOLERANCE

 $M = \pm 20\%$

Z = -20% to +80%

(3) PACKING STYLE

R = Paper/PE taping reel; Reel 7 inch

- K = Blister taping reel; Reel 7 inch
- P = Paper/PE taping reel; Reel 13 inch
- F = Blister taping reel; Reel 13 inch
- C = Bulk case

(4) RATED VOLTAGE

- 5 = 6.3 V
- 6 = 10 V
- 7 = 16 V
- 8 = 25 V
- 9 = 50 V

(5) CAPACITANCE VALUE

2 significant digits+number of zeros

The 3rd digit signifies the multiplying factor, and letter ${\sf R}$ is decimal point

Example: $103 = 10 \times 10^3 = 10,000 \text{ pF} = 10 \text{ nF}$



Surface-Mount Ceramic Multilayer Capacitors General Purpose & High Cap. Y5V 6.3 V to 50 V

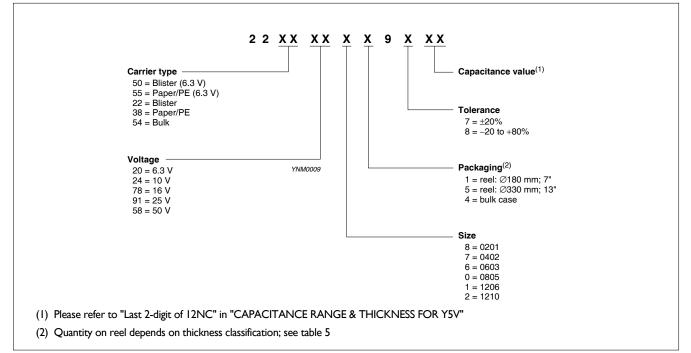
PHYCOMP BRAND ordering codes

GLOBAL PART NUMBER (preferred), PHYCOMP CTC (for North America) and I2NC (traditional) codes are acceptable to order Phycomp brand products.

GLOBAL PART NUMBER (PREFERRED)

For detailed information of GLOBAL PART NUMBER and ordering example, please refer to page 2.

12NC CODE



PHYCOMP CTC CODE (FOR NORTH AMERICA)

O Example: 12062F105M8BB0D

1206	2F	105	М	8	В	В	0	D
Size code	Temp. Char.	Capacitance in pF	Tolerance	Voltage	Termination	Packing	Marking	Range identifier
0201 0402 0603 0805 1206 1210	2F = Y5V	101 = 100 pF; the third digit signifies the multiplying factor: $0 = \times 1$ $1 = \times 10$ $2 = \times 100$ $3 = \times 1,000$ $4 = \times 10,000$ $5 = \times 100,000$ $6 = \times 1,000,000$	$M = \pm 20\%$ Z = -20% to +80%	$5 = 6.3 \vee$ $6 = 10 \vee$ $7 = 16 \vee$ $8 = 25 \vee$ $9 = 50 \vee$	B = NiSn	2 = 180 mm 7" Paper/PE 3 = 330 mm 13" Paper/PE B = 180 mm 7" Blister F = 330 mm 13" Blister P = Bulk case	0 = no marking	D = Class 2 MLCC

Product specification 4

Surface-Mount Ceramic Multilayer Capacitors General Purpose & High Cap. Y5V 6.3 V to 50 V

ceramic material

-Fig. I Surface mounted multilayer ceramic capacitor construction

electro

CONSTRUCTION

The capacitor consists of a rectangular block of ceramic dielectric in which a number of interleaved metal electrodes are contained. This structure gives rise to a high capacitance per unit volume.

The inner electrodes are connected to the two end terminations and finally covered with a layer of plated tin (NiSn). The terminations are lead-free. A cross section of the structure is shown in Fig. I.

Table I For outlines see fig. 2

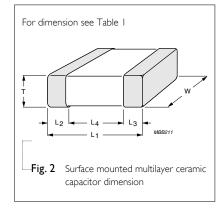


		0				
TYPE	E L _I (mm) W (mm)		T (MM)	L ₂ / L ₃	3 (mm)	L ₄ (mm)
IIFE		** (mm)	1 (1.11.1)	min.	max.	min.
0201	0.6 ±0.03	0.3 ±0.03	_	0.10	0.20	0.20
0402	1.0 ±0.05	0.5 ±0.05	_	0.20	0.30	0.40
0603	1.6 ±0.10	0.8 ±0.10	_	0.20	0.60	0.40
0805	2.0 ±0.10 ⁽¹⁾	1.25 ±0.10 ⁽¹⁾		0.25	0.75	0.55
0005	2.0 ±0.20 ⁽²⁾	1.25 ±0.20 ⁽²⁾		0.25	0.75	0.55
1206	3.2 ±0.15 ⁽¹⁾	Ⅰ.6 ±0.15 ⁽¹⁾	Refer to table 2 to 4	0.25	0.75	1.40
1200	3.2 ±0.20 ⁽²⁾	1.6 ±0.20 ⁽²⁾	LaDie 2 to 4	0.25	0.75	1.40
1210	3.2 ±0.20 ^(I)	2.5 ±0.20 ⁽¹⁾		0.25	0.75	1.40
1210	3.2 ±0.30 ⁽²⁾	2.5 ±0.30 ⁽²⁾		0.25	0.75	1.40
1812	4.5 ±0.20 ⁽¹⁾	22 4020	_	0.25	0.75	2.20
1012	4.5 ±0.40 ⁽²⁾	3.2 ±0.20		0.25	0.75	2.20

OUTLINES

terminations

MLB457



NOTE

I. Dimension for size 0805 to 1812, C < I μF

2. Dimension for size 0805 to 1812, C \geq 1 μF



Surface-Mount Ceramic Multilayer Capacitors General Purpose & High Cap. Y5V 6.3 V to 50 V

CAPACITANCE RANGE & THICKNESS FOR Y5V

Table 2 Sizes from 0201 to 0402

CAP.	Last 2-digit of	0201		0402				
	I2NC	6.3 V	25 V	6.3 V	10 V	16 V	25 V	50 V
10 nF	36		0.3±0.03					0.5±0.05
22 nF	41						0.5±0.05	
47 nF	45						0.5±0.05	
100 nF	49	0.3±0.03			0.5±0.05	0.5±0.05		
220 nF	52			0.5±0.05				
470 nF	58			0.5±0.05				
Ι.0 μF	63							
2.2 μF	67							
4.7 µF	72							
IO μF	76							
22 µF	81							
47 µF	85							

Table 3 Sizes from 0603 to 0805

CAP.	Last 2-digit of	0603					0805				
	12NC	6.3 V	10 V	16 V	25 V	50 V	6.3 V	10 V	16 V	25 V	50 V
10 nF	36										
22 nF	41									0(101	0(10)
47 nF	45				00101	0.8±0.1				0.6±0.1	0.6±0.1
100 nF	49				0.8±0.1						
220 nF	52								0.6±0.1		0.05 + 0.4
470 nF	58			0.8±0.1						0.85±0.1	0.85±0.1
Ι.0 μF	63		00101						0.85±0.1		1.25±0.2
2.2 µF	67	0.8±0.1	0.8±0.1					0.85±0.1		1.25±0.2	
4.7 µF	72						0.85±0.1	0.85±0.1 1.25±0.2	1.25±0.2		
ΙΟ μF	76						1.25.0.0				
22 µF	81						1.25±0.2	1.25±0.2			
47 µF	85										

NOTE

I. Values in shaded cells indicate thickness class in mm

2. Capacitance value of non E-3 series is on request

Surface-Mount Ceramic Multilayer Capacitors General Purpose & High Cap. Y5V 6.3 V to 50 V

Table 4	Sizes from 120	06 to 1210								
CAP.	Last 2-digit of	1206					1210			
	I2NC	6.3 V	10 V	16 V	25 V	50 V	6.3 V	10 V	16 V	25 V
I0 nF	36									
22 nF	41									
47 nF	45				0.6±0.1	0.6±0.1				
100 nF	49									
220 nF	52									
470 nF	58					0.05 + 0 +				
Ι.0 μF	63				0.85±0.1	0.85±0.1				
2.2 µF	67			0.05 + 0.4						
4.7 µF	72		0.85±0.1	0.85±0.1						
IO μF	76	0.85±0.1	-	1.15±0.1				1.5±0.1	1.5±0.1	1.5±0.1
22 µF	81	1.6±0.2	1.6±0.2	1.6±0.2	1.6±0.2			1.6±0.2	1.6±0.2	
47 µF	85						2.0±0.2			

CAPACITANCE RANGE & THICKNESS FOR Y5V

ΝΟΤΕ

I. Values in shaded cells indicate thickness class in mm

2. Capacitance value of non E-3 series is on request



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THICKNESS CLASSES AND PACKING QUANTITY

Table 5							
SIZE	THICKNESS	TAPE WIDTH -	Ø180 MM	/ 7 INCH	Ø330 MM	/ 13 INCH	QUANTITY
CODE	CLASSIFICATION	QUANTITY PER REEL	Paper	Blister	Paper	Blister	PER BULK CASE
0201	0.3 ±0.03 mm	8 mm	15,000		50,000		
0402	0.5 ±0.05 mm	8 mm	10,000		50,000		50,000
0603	0.8 ±0.1 mm	8 mm	4,000		15,000		15,000
	0.6 ±0.1 mm	8 mm	4,000		20,000		10,000
0805	0.85 ±0.1 mm	8 mm	4,000		15,000		8,000
	1.25 ±0.2 mm	8 mm		3,000		10,000	5,000
	0.6 ±0.1 mm	8 mm	4,000		20,000		
	0.85 ±0.1 mm	8 mm	4,000		15,000		
1206	1.00 / 1.15 ±0.1 mm	8 mm		3,000		10,000	
1200	1.25 ±0.2 mm	8 mm		3,000		10,000	
	1.6 ±0.15 mm	8 mm		2,500		10,000	
	1.6 ±0.2 mm	8 mm		2,000		10,000	
	0.6 / 0.7 ±0.1 mm	8 mm		4,000		15,000	
	0.85 ±0.1 mm	8 mm		4,000		10,000	
	1.15 ±0.1 mm	8 mm		3,000		10,000	
	1.15 ±0.15 mm	8 mm		3,000		10,000	
	1.25 ±0.2 mm	8 mm		3,000			
1210	1.5 ±0.1 mm	8 mm		2,000			
	1.6 / 1.9 ±0.2 mm	8 mm		2,000			
	2.0 ±0.2 mm	8 mm		2,000 1,000			
	2.5 ±0.2 mm	8 mm		l ,000 500			
	1.15 ±0.15 mm	l 2 mm		3,000			
	1.25 ±0.2 mm	l2 mm		3,000			
1808	1.35 ±0.15 mm	l 2 mm		2,000			
1000	1.5 ±0.1 mm	l2 mm		2,000			
	1.6 ±0.2 mm	l2 mm		2,000			
	2.0 ±0.2 mm	l2 mm		2,000			
	0.6 / 0.85 ±0.1 mm	l2 mm		2,000			
	1.15 ±0.1 mm	l2 mm		1,500			
	1.15 ±0.15 mm	l2 mm		1,500			
1812	1.35 ±0.15 mm	l2 mm		1,000			
1012	1.5 ±0.1 mm	l2 mm		1,000			
	1.6 ±0.2 mm	l2 mm		1,000			
	2.0 ±0.2 mm	l2 mm		1,000			
	2.5 ±0.2 mm	l2 mm		500	50,000		

Surface-Mount Ceramic Multilayer Capacitors General Purpose & High Cap. Y5V 6.3 V to 50 V

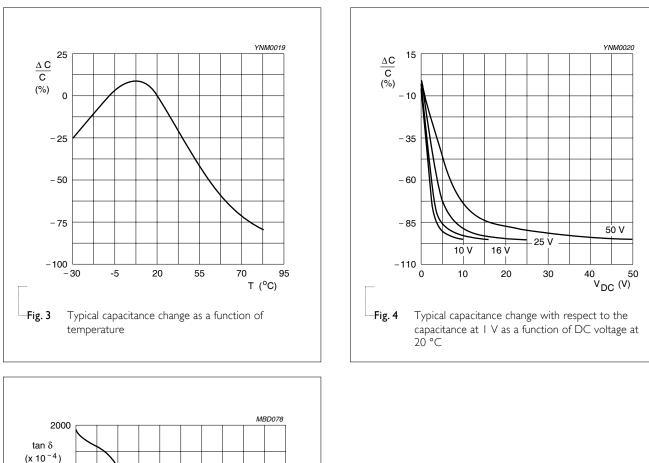
ELECTRICAL CHARACTERISTICS

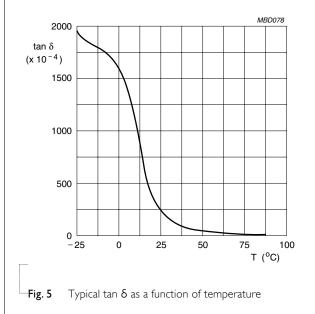
Y5V DIELECTRIC CAPACITORS; NISN TERMINATIONS

Unless otherwise stated all electrical values apply at an ambient temperature of 20 ± 1 °C, an atmospheric pressure of 86 to 106 kPa, and a relative humidity of 63 to 67%.

Table 6					
DESCRIPTION					VALUE
Capacitance range					10 nF to 47 μF
Capacitance tolerance					±20% -20% to +80%
Dissipation factor (D.F.)					
	≤ 6.3 V				≤ 15%
		Exception: (0805 ≥ 22 µF		≤ 20%
	10 V				≤ 12.5%
		Exception: (0402 ≥ 680 nF;	0603 ≥ 2.2 µF;	≤ 5%
		(0805 ≥ 10 µF;	1206 ≥ 10 µF	≤ 20%
	16 V				≤ 12.5%
		Exception: (0603 ≥ 4.7 µF		≤ 15%
	≥ 25 V				≤ 9%
		Exception: (0201 ≥ 10 nF		≤ 12.5%
Insulation resistance after I minute at U _r (DC)				$R_{ins} \ge 10 \text{ G}\Omega$	or $R_{ins} \times C_r \ge 500$ seconds whichever is less
Maximum capacitance ch	-	•	ature		
(temperature characteris		<i></i>			+22% to -82%
Operating temperature r	ange:				−30 °C to +85 °C

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SOLDERING RECOMMENDATION

Table 7					
SOLDERING METHOD	SIZE 0402	0603	0805	1206	≥ 1210
Reflow	≥ 0.1 µF	≥ 1.0 µF	≥ 2.2 µF	≥ 4.7 µF	Reflow only
Reflow/Wave	< 0.1 µF	< 1.0 µF	< 2.2 µF	< 4.7 µF	

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TESTS AND REQUIREMENTS

TEST	TEST METH	HOD	PROCEDURE	REQUIREMENTS
Mounting	IEC 60384- 21/22	4.3	The capacitors may be mounted on printed-circuit boards or ceramic substrates	No visible damage
Visual inspection and dimension check		4.4	Any applicable method using × 10 magnification	In accordance with specification
Capacitance		4.5.I	Class 2: $f = 1$ KHz for C $\leq 10 \mu$ F, measuring at voltage 1 V _{rms} at 20 °C $f = 120$ Hz for C $>10 \mu$ F, measuring at voltage 0.5 V _{rms} at 20 °C	Within specified tolerance
Dissipation factor (D.F.)		4.5.2	Class 2: $f = 1$ KHz for C $\leq 10 \mu$ F, measuring at voltage 1 V _{rms} at 20 °C $f = 120$ Hz for C $> 10 \mu$ F, measuring at voltage 0.5 V _{rms} at 20 °C	In accordance with specification
Insulation resistance		4.5.3	At U _r (DC) for 1 minute	In accordance with specification
Temperature characteristic		4.6	Class 2: Between minimum and maximum temperature Y5V: -30 °C to +85 °C Normal Temperature: 20 °C	<general purpose="" series=""> $\Delta C/C$ Class 2: Y5V: 22% to -82% <high capacitance="" series=""> $\Delta C/C$ Class 2: Y5V: 22% to -82%</high></general>
Adhesion		4.7	A force applied for 10 seconds to the line joining the terminations and in a plane parallel to the substrate	Force size ≥ 0603: 5N size = 0402: 2.5N size = 0201: 1N

Surface-Mount Ceramic Multilayer Capacitors General Purpose & High Cap. Y5V 6.3 V to 50 V

TEST	TEST METH	HOD	PROCEDURE	REQUIREMENTS
Bond strength of	IEC 60384- 21/22	4.8	Mounting in accordance with IEC 60384-22 paragraph 4.3	No visible damage
olating on			- Conditions: bending 1 mm at a rate of 1 mm/s,	<general purpose="" series=""></general>
end face			radius jig 340 mm	$\Delta C/C$
				Class2:
				Y5V: ±10%
				<high capacitance="" series=""></high>
				$\Delta C/C$
				Class2:
				Y5V: ±10%
Resistance to soldering heat		4.9	Precondition: $150 \pm 0/-10$ °C for 1 hour, then keep for 24 ±1 hours at room temperature Preheating: for size ≤ 1206: 120 °C to 150 °C for 1	Dissolution of the end face plating shall not exceed 25% of the length of the edge concerned
			minute	
			- Preheating: for size >1206: 100 °C to 120 °C for 1	<general purpose="" series=""></general>
			minute and 170 °C to 200 °C for 1 minute	$\Delta C/C$
			Solder bath temperature: 260 \pm 5 °C	Class2:
			Dipping time: 10 ± 0.5 seconds	Y5V: ±20%
			Recovery time: 24 \pm 2 hours	<high capacitance="" series=""></high>
			,	
				Class2:
				Y5V: ±20%
				154. 120/0
			-	D.F. within initial specified value
				R_{ins} within initial specified value
Solderability		4.10	Preheated the temperature of 80 °C to 140 °C and	The solder should cover over 95% of the
,			maintained for 30 seconds to 60 seconds.	critical area of each termination
			Test conditions for lead containing solder alloy	
			Temperature: 235 ±5 °C	
			Dipping time: 2 \pm 0.2 seconds	
			Depth of immersion: 10 mm	
			Alloy Composition: 60/40 Sn/Pb	
			Number of immersions: I	
			Test conditions for leadfree containing solder alloy	
			Temperature: 245 ±5 °C	
			Dipping time: 3 ± 0.3 seconds	
			Depth of immersion: 10 mm	
			Alloy Composition: SAC305 Number of immersions: 1	

Surface-Mount Ceramic Multilayer Capacitors General Purpose & High Cap. Y5V 6.3 V to 50 V

TEST	TEST METH	OD	PROCEDURE	REQUIREMENTS
Rapid change of	IEC 60384- 21/22	4.11	Preconditioning; 150 +0/–10 °C for 1 hour, then keep for	No visual damage
temperature			24 ± 1 hours at room temperature5 cycles with following detail:30 minutes at lower category temperature	<general purpose="" series=""> ΔC/C Class2: Y5V: ±20%</general>
			30 minutes at upper category temperature Recovery time 24 ±2 hours	<high capacitance="" series=""> ΔC/C</high>
				Class2: Y5V: ±20%
				D.F. meet initial specified value R _{ins} meet initial specified value
Damp heat with U _r load		4.13	I. Preconditioning, class 2 only: 150 +0/-10 °C /1 hour, then keep for	No visual damage after recovery
			24 ± 1 hour at room temp	<general purpose="" series=""></general>
			2. Initial measure:	$\Delta C/C$
			Spec: refer initial spec C, D, IR	Class2:
			3. Damp heat test:	Y5V: ±30%
			500 ± 12 hours at 40 ±2 °C;	D.F.
			90 to 95% R.H. 1.0 U _r applied	Class2:
			4. Recovery: Class 2: 24 ±2 hours	Y5V: ≤ 15%
				R _{ins}
			5. Final measure: C, D, IR	Class2:
			P.S. If the capacitance value is less than the minimum value permitted, then after the other	Y5V: ≥ 500 M Ω or R _{ins} × C _r ≥ 25s whichever is less
			measurements have been made the capacitor shall	<high capacitance="" series=""></high>
			be precondition according to "IEC 60384 4.1" and	ΔC/C
			then the requirement shall be met.	Class2:
				Y5V: ±30%
				D.F.
				Class2:
				Y5V: 2 × initial value max
				R _{ins}
				Class2: Y5V: 500 M Ω or R _{ins} x C _r ≥ 25s whichever is less

Surface-Mount Ceramic Multilayer Capacitors General Purpose & High Cap. Y5V 6.3 V to 50 V

TEST	TEST METHOD	PROCEDURE	REQUIREMENTS
<u>TEST</u> Endurance	TEST METHOD IEC 60384- 4.14 21/22 4.14	 PROCEDURE 1. Preconditioning, class 2 only: 150 +0/-10 °C /1 hour, then keep for 24 ± 1 hour at room temp 2. Initial measure: Spec: refer initial spec C, D, IR 3. Endurance test: Temperature: Y5V: 85 °C Specified stress voltage applied for 1,000 hours: Applied 2.0 × Ur for general product. Applied 1.5 × Ur for high cap. product. 4. Recovery time: 24 ± 2 hours 5. Final measure: C, D, IR P.S. If the capacitance value is less than the minimum value permitted, then after the other measurements have been made the capacitor shall be precondition according to "IEC 60384 4.1" and then the requirement shall be met. 	REQUIREMENTSNo visual damage General purpose series> $\Delta C/C$ Class2: $\gamma 5 \lor \pm 30\%$ D.F. Class2: $\gamma 5 \lor \le 15\%$ R_{ins} Class2: $\gamma 5 \lor \le 1,000 \text{ M}\Omega \text{ or } R_{ins} \times C_r \ge 50 \text{ s}$ whichever is less High Capacitance series>$\Delta C/C$Class 2:$\gamma 5 \lor \pm 30\%$D.F.Class 2:$\gamma 5 \lor \pm 30\%$D.F.Class 2:$\gamma 5 \lor \cdot \pm 30\%$D.F.Class 2:$\gamma 5 \lor \cdot 2 \times \text{ initial value max}$$R_{ins}$Class 2:$\gamma 5 \lor \cdot 1,000 \text{ M}\Omega \text{ or } R_{ins} \times C_r \ge 50 \text{ s}$whichever is less
Voltage proof	IEC 60384-1 4.6	Specified stress voltage applied for 1 minute $U_r \le 100 \text{ V}$: series applied 2.5 U_r $100 \text{ V} < U_r \le 200 \text{ V}$ series applied (1.5 $U_r + 100$) $200 \text{ V} < U_r \le 500 \text{ V}$ series applied (1.3 $U_r + 100$) $U_r > 500 \text{ V}$: 1.3 U_r I: 7.5 mA	No breakdown or flashover

 Surface-Mount Ceramic Multilayer Capacitors
 General Purpose & High Cap.
 Y5V
 6.3 V to 50 V

<u>REVISION HISTORY</u>

REVISION	DATE	CHANGE NOTIFICATION	DESCRIPTION
Version 2	Feb 04, 2010	-	- The statement of "Halogen Free" on the cover added
Version I	Nov 04, 2009	-	- Ordering code updated
_			- Dimension updated
Version 0	Apr 15, 2009	-	- New datasheet for general purpose and high capacitance Y5V series with RoHS compliant
			- Replace the "6.3V to 50V" part of pdf files: Y5V_6.3V_10V_9_Preliminary, Y5V_10V-to-50V_10_Preliminary, Y5V_16V_25V_50V_11
			- Combine 0201 from pdf files: UP-NP0X5RX7RY5V_0201_6.3-to-50V_2 and UY-NPOX5RX7RY5V_0201_6.3-to-50V_2
			- Define global part number
			- Description of "Halogen Free compliant" added
			- Test method and procedure updated

