

Am99C88H

8192 x 8 CMOS Static Random-Access Memory

ADVANCE INFORMATION

DISTINCTIVE CHARACTERISTICS

- High Speed
 - 35 ns Commercial
 - 45 ns Military
- Low active power dissipation
 - 605 mW Maximum
- Low standby power dissipation
 - 138 mW Maximum
- Battery backup operation
 - 2-V data retention
- Single 5-V $\pm 10\%$ power-supply operation
- Common data inputs and outputs
- Fully static operation and interface
- Automatic power-down when deselected
- TTL-compatible inputs and outputs
- Standard 28-pin, 600-mil DIP, and 32-pin ceramic leadless and plastic leaded chip carriers

GENERAL DESCRIPTION

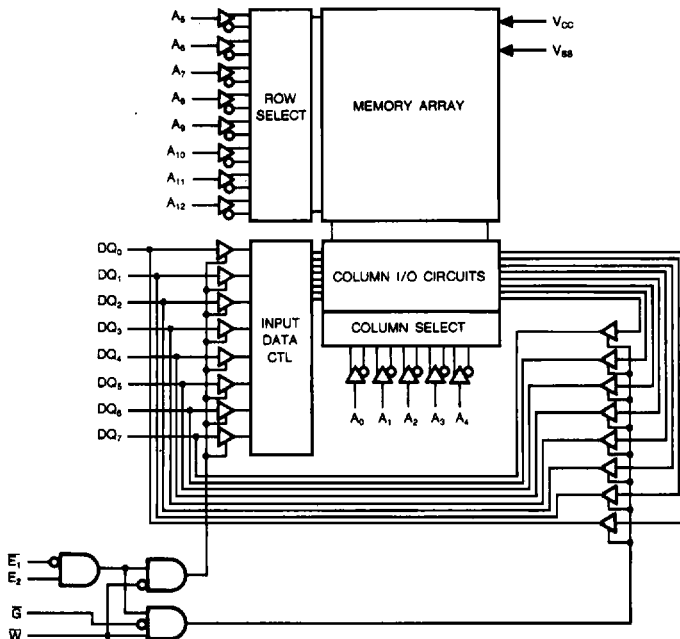
The Am99C88H is a high-performance CMOS Static RAM organized as 8192 words by 8 bits. It is manufactured using an advanced high-performance CMOS process that combines high speed with low-power consumption and increased reliability.

The Am99C88H operates from a single 5-V supply and is fully TTL-compatible. Four inputs, E_1 , E_2 , \bar{W} , and \bar{G} are used to control the device. Two Chip Enables (E_1 and E_2) select the device for operation, control the automatic

power-down feature, and provide for easy memory expansion. Write Enable (\bar{W}) controls write and read operations. Output Enable (\bar{G}) controls the three-state output buffers on the eight common data inputs and outputs. Data is retained by the device with V_{CC} as low as 2 V.

The Am99C88H is available in a 28-pin, 600-mil DIP, a 32-pin ceramic leadless chip carrier, and a 32-pin plastic leaded chip carrier.

BLOCK DIAGRAM



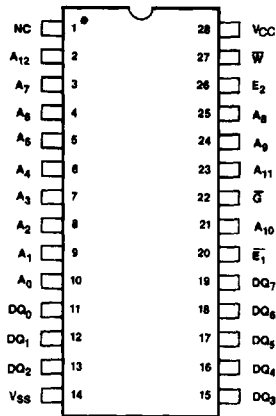
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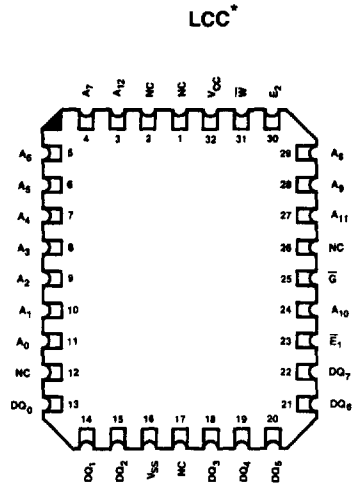
PRODUCT SELECTOR GUIDE

Part Number	Am99C88H			
	-35	-45	-55	-70
Access Time Max. (ns)	35	45	55	70
0 to +70°C	I_{CC} Max. (mA)	1f0	110	110
	I_{SB} Max. (mA)	25	25	25
	I_{SBC} Max. (mA)	5	5	5
-55 to +125°C	I_{CC} Max. (mA)	—	125	125
	I_{SB} Max. (mA)	—	30	30
	I_{SBC} Max. (mA)	—	10	10

CONNECTION DIAGRAMS Top View



CD009133

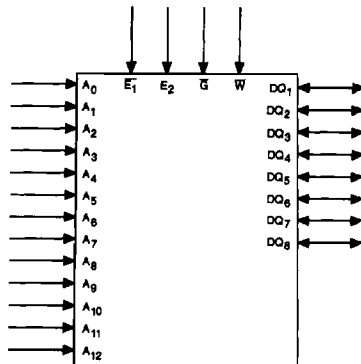


CD009125

*Same pinouts apply for PLCC.

Note: Pin 1 is marked for orientation.

LOGIC SYMBOL



LS002181