>> 131,072 x 16 Organization

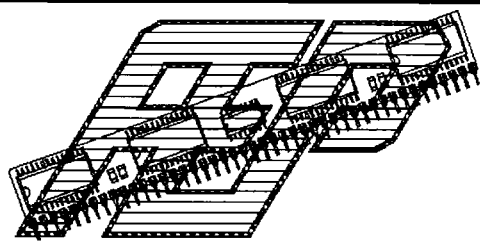
>> High speed, High density 45 pin SIP

>> Completely Static operation

>> TTL compatible

>> Low power, battery back-up operation capability

>> Uses single +5V power supply



128 KILOBYTES STATIC RAM MODULE

DESCRIPTION:

The AEPSS128K16-FCT139 is a high density 128 Kilo-word by 16 bit static random access memory module in a 45 pin single-in-line-package format. Physically it consists of an FR4 PC material substrate mounted with eight 32K x 8 SOP (small outline package) ICs or SOJ (J-leaded) ICs , one fast 138 CMOS dual 1 of 4 decoder logic, four 0.1 microfarad decoupling capacitors, and 45 edge-clip I/O pins.

The module can use any of the 32K x 8 SRAMs made by any of a large number of manufacturers in both Mix-MOS and CMOS technologies in SOJ or SOP pin format.

The module is activiated by using A15 and A16 to select lower and upper 32Kx8 RAMs in addition it is also using ENABLE LOW (active low) and ENABLE HIGH (active low) as extra chip select functions for lower byte I/O1-I/O8 and upper byte (I/O9-I/O16) control, respectively.

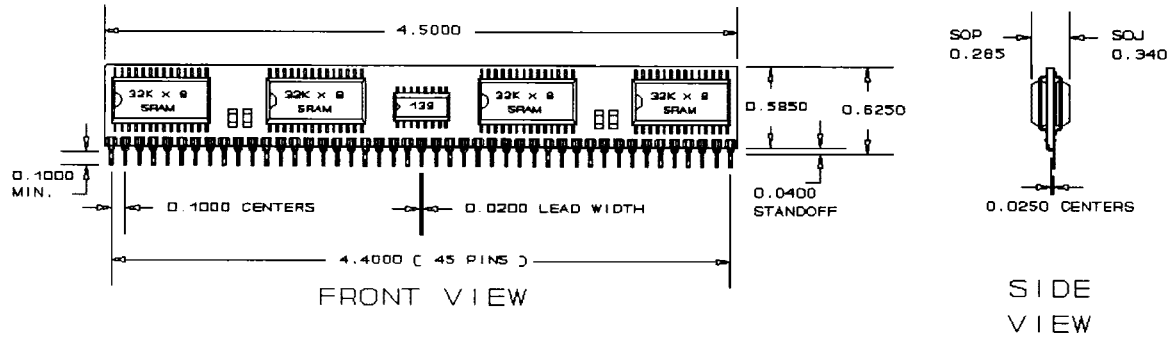
Performance specifications and electrical characteristics are determined by the IC devices used. A typical memory component module will draw 2mA (max.) in standby and 70mA (max.) during access. These items can very according to the type and manufacturer of the components. The necessary information is obtained from the IC vendors data sheets which are included here, or from data books.

Mechanical dimensions are 0.625 in. high by 4.50 in. long by 0.340 in. wide. The module is available with vertical lead pins, 64K x 8 - AEPSS64K x 8 SRAM format (single side), and AEPSS128K16 format (without 74_139).

SPECIFICATION DRAWING

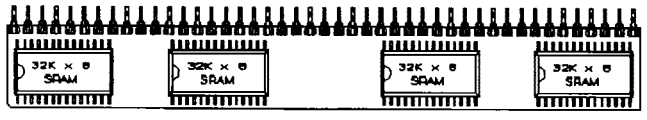
128K x 16-FCT139 STATIC RAM MODULE

DIMENSIONS IN INCHES, TOLERANCE: +/- 0.010 UNLESS SPECIFIED.



FRONT VIEW

SIDE VIEW



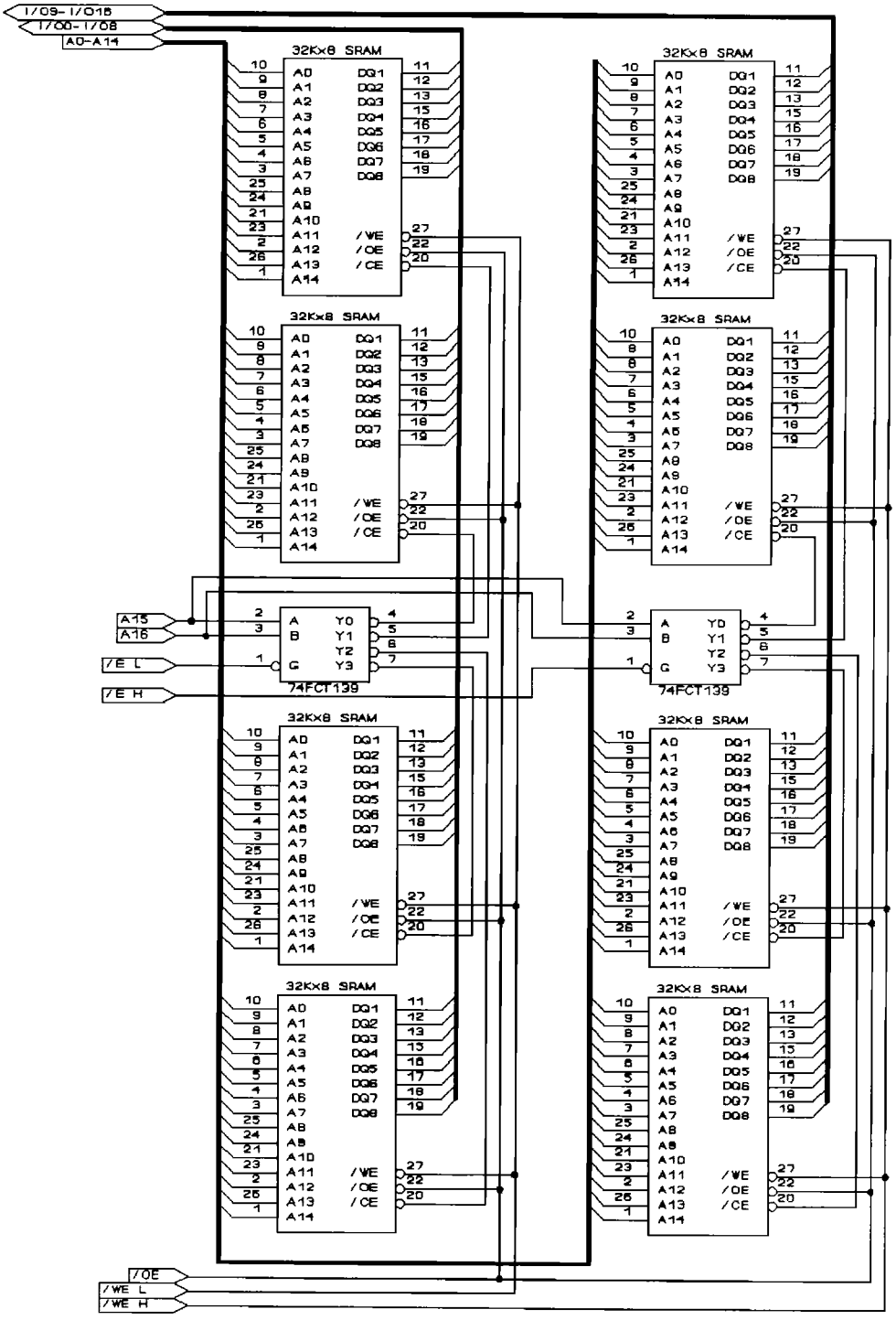
REAR VIEW

128K x 16 WITH 139 DECODER STATIC RAM MODULE

SIP PIN CONFIGURATION (TOP VIEW)

1	A_{14}
2	A_0
3	A_1
4	A_2
5	I/O_1
6	I/O_2
7	I/O_3
8	I/O_4
9	I/O_5
10	GND
11	I/O_6
12	WE_L*
13	A_3
14	A_4
15	A_5
16	A_6
17	I/O_7
18	I/O_8
19	NC
20	E_H*
21	A_{15}
22	NC
23	VCC
24	NC**
25	A_{16}
26	E_L*
27	NC
28	A_{13}
29	A_7
30	OE*
31	I/O_9
32	I/O_{10}
33	I/O_{11}
34	I/O_{12}
35	I/O_{13}
36	GND
37	I/O_{14}
38	WE_H*
39	A_8
40	A_9
41	A_{10}
42	A_{11}
43	A_{12}
44	I/O_{15}
45	I/O_{16}

FUNCTIONAL DIAGRAM



* ACTIVE WHEN LOW

** This line is still connected to CS7 in the 139 module so don't ground it.