

Absolute single-turn hollow shaft encoders

BOSH – *Signalizer*

SSI

features

- high resolution single-turn encoders 18 bit
- SSI interface programmable
- permanent self-test
- reference point programmable
- end hollow shaft



version
5 VDC

version
10 - 30 VDC

general data

voltage supply	5 VDC ±5% (05C) 10 - 30 VDC (24C)
supply current no load	typ. 120 mA (at 5VDC) (05C) typ. 50 mA (at 24 VDC) (24C)
output circuit	SSI, complementary RS 422, binary- and Gray code
max. resolution	18 Bit (1 step = 5'')
max. clock frequency	1,1 MHz
repeatability	0,012°
direction of rotation	looking at the flange, position counts up as the shaft rotates clockwise (CW), programmable

mechanical data

max. revolutions	mech. 6'000 rpm electr. 6'000 rpm
moment of inertia	typ. $18,4 \times 10^{-7}$ kgm ²
torque	typ. 1,75 cNm (3'000 rpm 20 °C/ IP 42)
product life	depending on ambient conditions (typ. 10 ⁹ revolutions)
max. protection class	IP 65
material	housing: aluminum
weight	approx. 300 g

ambient conditions

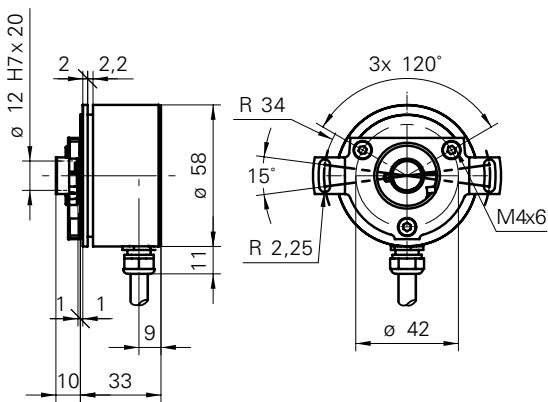
temperature range	-20...+85 °C
relative humidity	max. 95% non condensing
vibration (sine)	IEC 60068-2-6 (≤ 100 m/s ² / 10 - 2'000 Hz) 150 min per axis
vibration (random)	IEC 60068-2-64 (≤ 0,1 g ² /Hz / 20 - 1'000 Hz) 30 min per axis
shock	IEC 60068-2-27 (≤ 500 m/s ² , 11 ms) 10 pulses per axis and direction
noise immunity	EN 61000-6-2
emitted interference	EN 61000-6-3



dimensions connection dimensions

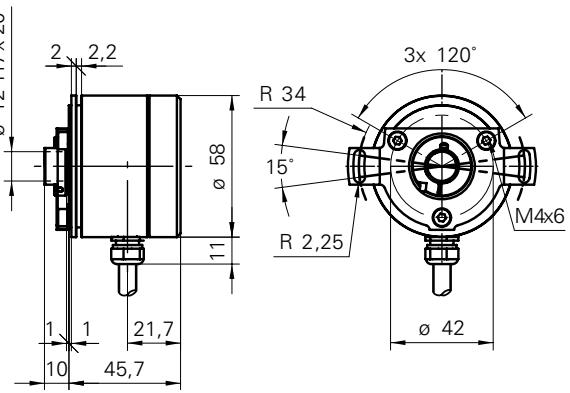
version 5 VDC

-5

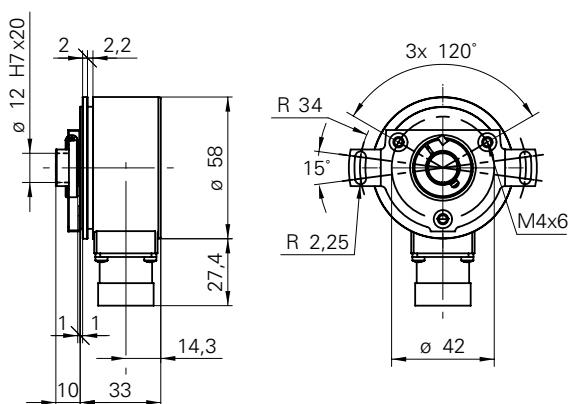


version 10 - 30 VDC

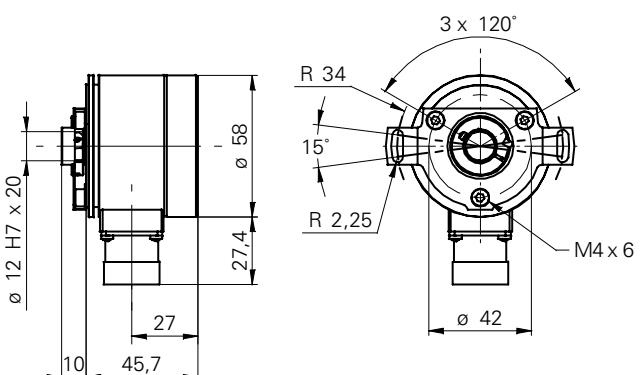
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-A



-A



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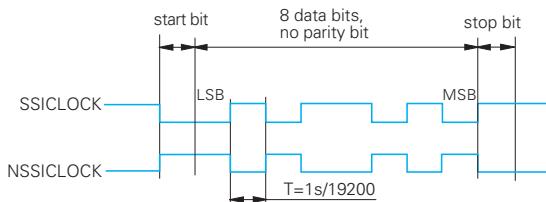
commands to encoder and setting of parameters

Multibyte commands* and parameters can be sent by asynchronous protocol with parameterized baud rate.

The multibyte protocol consists of length byte, address byte, command byte, data bytes and LRC byte.

length	slave address	command	(memory address), (data)	checksum LRC
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The 8 data bits have are embeded between a start bit (LOW) and a stop bit (HIGH).



Setting of parameters

It is always possible to set parameter over the SSI interface, even if the encoder is in operation.

For example:

- SSI bit format (position resolution)
- direction of rotation CW/CCW
- monoflop time
- interface mode
- compensation of propagation time
- baud rate for parameter setting and programming
- signal code (binary, Gray)

Additional features:

- efficient error handling
- memory for operating time
- incremental totalisator (number of rotations)
- preset function

The response on commands (data/„acknowledge“/„negative acknowledge“) has to be read out at the SSI interface before reading the next position value.

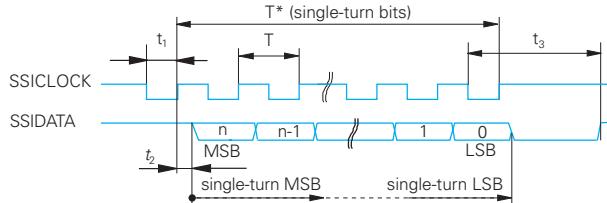
Reading out of position values and programmable of *Signalizer* is also possible via PCI-board.

See chapter accessories.

* With endshaft mounting, commands and parameter setting by singlebyte protocol. Description on request.

read out of position values

The SSI interface can be used especially for real time signal processing. It is possible to precisely scan the angle information with the first negative clock.



Important: The SSI protocol does not start with zeros. NSSICLOCK and NSSICLOCK are not symbolized.

$$\begin{array}{ll} T = 0,9 \text{ to } 14 \mu\text{s} & t_2 \geq 0,6 \mu\text{s} \\ 0,45 \mu\text{s} < t_1 < t_3 & t_3 = 2, 5, 10 \text{ or } 20 \mu\text{s} \end{array}$$

The monoflop-time (t_3) is programmable (Default 20 μ s). Please keep the condition $t_3 \geq 1,4 * T$.

I/O description

V/R* Input to set direction of rotation. Input is normally HIGH. V/R*-HIGH corresponds to increasing position values while looking at flange and shaft rotation in clockwise direction (CW). V/R*-LOW corresponds to increasing position values while looking at flange and shaft rotation in counter-clockwise direction (CCW). Read-in of V/R* voltage occurs during encoder power-on.

ZERO Input to set position to zero. Input is normally LOW. Zero setting procedure is initiated by a HIGH-pulse (pulse length ≥ 100 ms). For maximum disturbance immunity, connect input to 0 V.

DV* Output for error (data valid). Output is LOW-active, it indicates an error with LOW-voltage.

inputs and outputs

Control signals to V/R* and ZERO

Level HIGH $> 0,8 * +Vs$

Level LOW $< 0,2 * +Vs$

Connection V/R*-input with 15 kOhms against +Vs, ZERO-input with 15 kOhms against 0 V (3 kOhms for version **05C**).

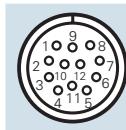
Output signal from DV*

Connection DV*-output push-pull max. 30 mA (with 10 kOhms against +Vs for version **05C**)



assignment connector 12-pin M23 male

for connection reference -A



pin-number	signals
1	NSSICLOCK
2	SSICLOCK
3	SSIDATA
4	NSSIDATA
5	ZERO
6	DV*
7	V/R*
8	n.c.
9	n.c.
10	n.c.
11	+Vs
12	0 V

order designation

BOSH 58S1 [] [] /00 []

connection

5 cable 2 m radial
A connector radial M23

shaft

B2 hollow shaft 12 mm IP 42
with clamping ring

P2 hollow shaft 12 mm IP 65
with clamping ring

I2 hollow shaft 12 mm IP 42
with clamping ring and
spring plate

M2 hollow shaft 12 mm IP 65
with clamping ring and
spring plate

resolution

18 18 bit

supply voltage, output signals

05C 5 VDC SSI interface
24C 10 – 30 VDC SSI interface

signal code

N binary code
G Gray code

Other versions on request.

assignment cable

for connection reference -5

cable color	signals
brown	+Vs (5 VDC)
green	SSICLOCK
yellow	NSSICLOCK
pink	SSIDATA
grey	NSSIDATA
black	ZERO
red	DV*
blue	V/R*
violet	n.c.
white	0 V
cable data	10 x 0,14 mm ² , PVC, screened, L = 2 m

accessories

connector 12 pin	part nr. 116717
clamp set	part nr. 110616
spring plate set	part nr. 136635
PCI-board with galvanic isolation	part nr. 139338 see chapter accessories