93Z667
8192 x 8-Bit Programmable
Read Only Memory

Memory and High Speed Logic

Description

The 93Z667 is a fully decoded 65,384-bit
Programmable Read Only Memories (PROMs),
organized 8192 words by eight bits per word.
The 93Z667 is manufactured using Fairchild's highly
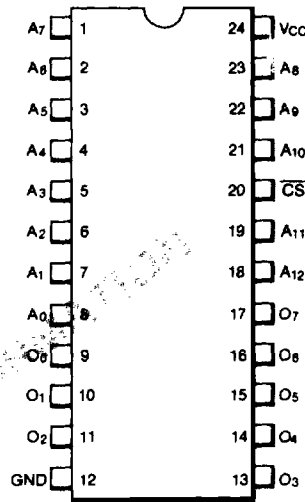reliable FAST-Z vertical fuse technology.

- **Available in 300 mil Side-Brazed DIP,**
- **Commercial Address Access Time — 40 ns Max**
- **Military Address Access Time — 45 ns Max**
- **Highly Reliable Vertical Fuses Ensure High
  Programming Yields**
- **Power-Up Three State Outputs**
- **Low Current PNP Inputs**

Pin Names

A_0–A_{12}	Address Inputs
$\overline{CS}$	Chip Select Input (Active LOW)
O_0–O_7	Data Outputs

Connection Diagrams
24-pin DIP (Top View)

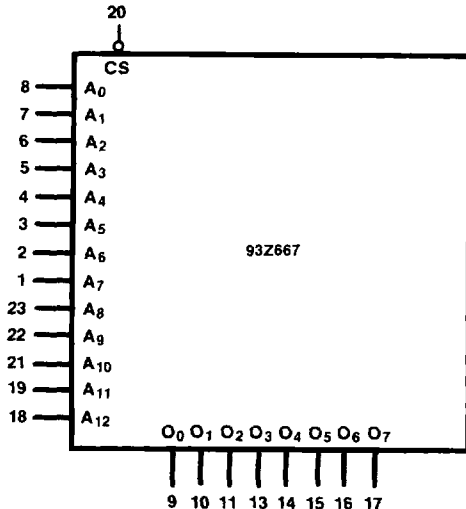| Pin			Pin
A_7	1	24	V_{CC}
A_6	2	23	A_8
A_5	3	22	A_9
A_4	4	21	A_{10}
A_3	5	20	$\overline{CS}$
A_2	6	19	A_{11}
A_1	7	18	A_{12}
A_0	8	17	O_7
O_0	9	16	O_6
O_1	10	15	O_5
O_2	11	14	O_4
GND	12	13	O_3

Note:
The 24-pin Flatpak has the same pinout (Connection Diagram) as the
24 pin DIP

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Logic Symbol

V_{CC} = Pin 24
GND = Pin 12