

SOT-25



Pin Definition:

1. EN
2. Ground
3. Switching Output
4. Input
5. Feedback

General Description

TS3410 is a high efficiency monolithic synchronous buck regulator using a constant frequency, current mode architecture. The device is available in an adjustable version. Supply current with no load is 350uA and drops to <1uA in shutdown. The 2.5V to 6.5V input voltage range makes TS3410 ideally suited for single Li-Ion, two to four AA battery-powered applications. 100% duty cycle provides low dropout operation, extending battery life in portable systems. PWM pulse skipping mode operation provides very low output ripple voltage for noise sensitive applications. Switching frequency is internally set at 1.5MHz, allowing the use of small surface mount inductors and capacitors. The internal synchronous switch increases efficiency and decreases need of an external Schottky diode. Low output voltages are easily supported with the 0.6V feedback reference voltage.

Features

- High Efficiency: Up to 96%
- 2.5V to 6.5V Input Voltage Range
- Short Circuit Protection (SCP)
- 1.5MHz Constant Frequency Operation
- Low Dropout Operation: 100% Duty Cycle
- 0.6V Reference Allows Low Output Voltages
- 1A Output Current at $V_{IN}=3V$ & $V_{OUT}=1.8V$
- Low Quiescent Current: 350uA
- No Schottky Diode Required in Application
- $\leq 1\mu A$ Shutdown Current
- Current Mode Operation for Excellent Line and Load Transient Response

Application

- Cellular Phones
- Digital Still Cameras
- Portable Electronics
- USB Devices

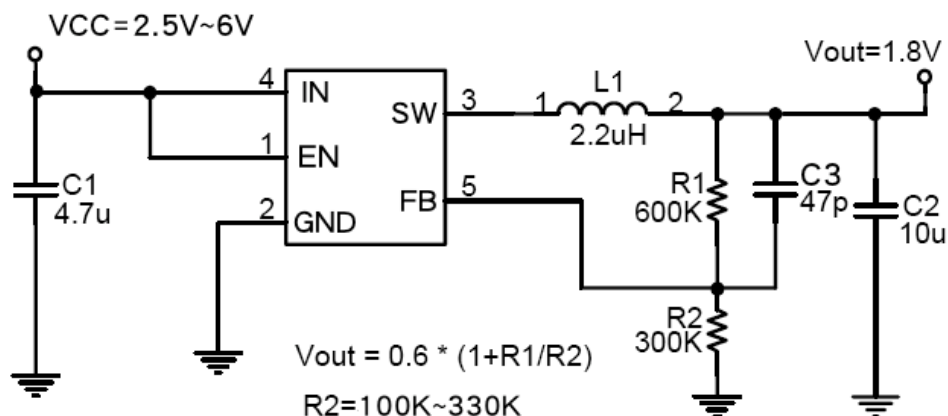
Pin Description

Name	Description
EN	Power-off pin H : normal operation L : Step-down operation stopped (All circuits deactivated)
GND	Ground pin
SW	Switch output pin. Connect external inductor here. Minimize trace area at this pin to reduce EMI.
VCC	IC power supply pin
FB	Output Feedback pin

Ordering Information

Part No.	Package	Packing
TS3410CX5 RF	SOT-25	3Kpcs/ 7" Reel

Application Circuit



Absolute Maximum Rating

Characteristics	Symbol	Rating	Unit
V _{IN} Pin Voltage	V _{IN}	Gnd - 0.3 to Gnd + 7	V
Feedback Pin Voltage	V _{FB}	Gnd - 0.3 to V _{IN} + 0.3	V
RUN Pin Voltage	V _{RUN}	Gnd - 0.3 to V _{IN} + 0.3	V
Switch Pin Voltage	V _{SW}	Gnd - 0.3 to V _{IN} + 0.3	V
Peak SW Sink & Source Current	I _{PSW}	1.4	A
Operating Supply Voltage	V _{OP}	+2.5 to +6	V
Power Dissipation	P _D	(T _J -T _A) / θ _{JA}	mW
Storage Temperature Range	T _{ST}	-40 to +150	°C
Operating Temperature Range	T _{OP}	-40 to +85	°C
Junction Temperature	T _J	+125	°C
Thermal Resistance from Junction to case	θ _{JC}	110	°C/W
Thermal Resistance from Junction to ambient	θ _{JA}	250	°C/W

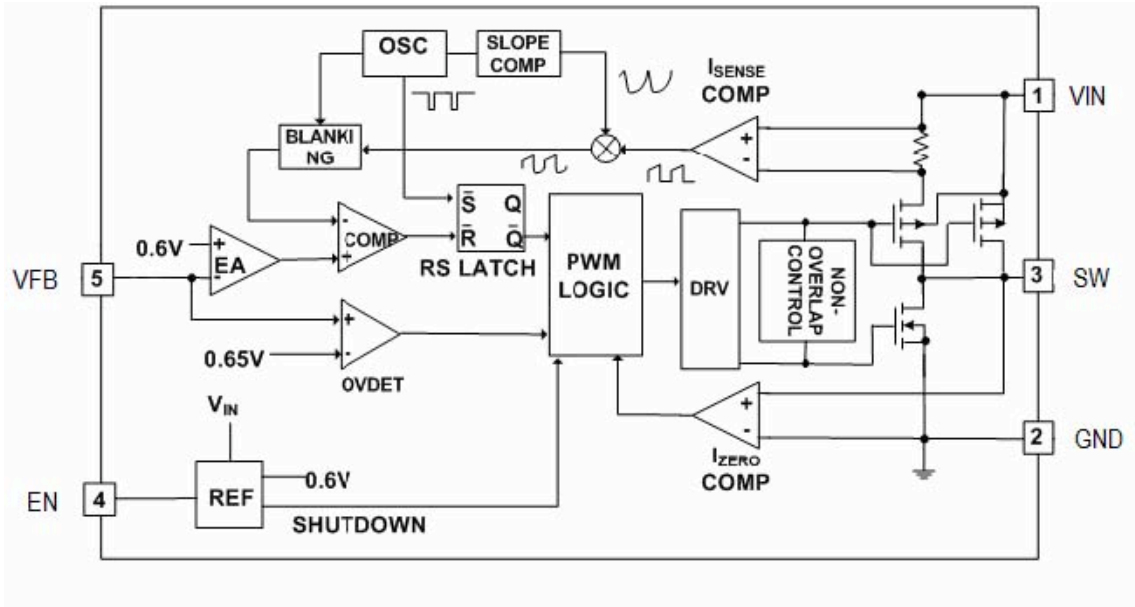
Note1: θ_{JA} is measured with the PCB copper area of approximately 1 in² (Multi-layer). that need connect to Gnd pin of the TS3410.

Electrical Specifications (T_a = 25°C, V_{IN}=V_{RUN}=3.6V unless otherwise noted)

Characteristics	Symbol	Conditions	Min	Typ	Max	Units
Feedback Voltage	V _{FB}	T _A = 25°C, I _{OUT} = 50mA	0.588	0.6	0.612	V
Quiescent Current	I _{CCQ}	V _{FB} = 0.5V	--	350	450	uA
Feedback Bias Current	I _{FB}	V _{FB} = 0.65V	--	--	±30	nA
Shutdown Supply Current	I _{SD}	V _{RUN} = 0V	--	0.1	1	uA
Maximum Output Current	I _{OUT(MAX)}	V _{CC} = 3V, V _{OUT} = 1.8V	1	--	--	A
Current Limit	I _{LIMIT}	V _{CC} = 3V	1.2	1.4	--	A
Line Regulation	ΔV _{OUT} /V _{OUT}	V _{CC} = 2.5V~5.5V	--	0.04	0.4	%
Load Regulation	ΔV _{OUT} /V _{OUT}	I _{OUT} = 0.01 to 0.6A	--	0.5	--	%
Oscillation Frequency	F _{OSC}	SW pin	1.2	1.5	1.8	MHz
Frequency of SCP	F _{OSC1}	SW pin	--	300	--	KHz
R _{DS(ON)} of P-CH MOSFET	R _{DSON}	I _{SW} = 300mA	--	0.40	0.45	Ω
R _{DS(ON)} of N-CH MOSFET	R _{DSON}	I _{SW} = -300mA	--	0.35	0.40	Ω
Efficiency	E _{FFI}	V _{OUT} = 3.3V, I _{OUT} = 0.5A	--	92	--	%
EN pin logic Input Threshold Voltage	V _{ENL}		0.6	--	--	V
	V _{ENH}		--	--	1.2	
EN Pin Input Current	I _{EN}		--	±0.1	±1	uA

Note2: 100% production test at +25°C. Specifications over the temperature range are guaranteed by design and characterization.

Block Diagram



Function Description

Operation

TS3410 is a monolithic switching mode Step-Down DC-DC converter. It utilizes internal MOSFETs to achieve high efficiency and can generate very low output voltage by using internal reference at 0.6V. It operates at a fixed switching frequency, and uses the slope compensated current mode architecture. This Step-Down DC-DC Converter supplies 1A output current at $V_{OUT} = 1.8V$ with input voltage range from 2.5V to 6V.

Current Mode PWM Control

Slope compensated current mode PWM control provides stable switching and cycle-by-cycle current limit for excellent load and line responses and protection of the internal main switch (P-CH MOSFET) and synchronous rectifier (N-CH MOSFET). During normal operation, the internal P-CH MOSFET is turned on for a certain time to ramp the inductor current at each rising edge of the internal oscillator, and switched off when the peak inductor current is above the error voltage. The current comparator, ICOMP, limits the peak inductor current. When the main switch is off, the synchronous rectifier will be turned on immediately and stay on until either the inductor current starts to reverse, as indicated by the current reversal comparator, IZERO, or the beginning of the next clock cycle. The OVDDET comparator controls output transient overshoots by turning the main switch off and keeping it off until the fault is no longer present.

Idle Mode Operation

At very light loads, TS3410 automatically enters pulse skipping Mode. In the pulse skipping Mode, the inductor current may reach zero or reverse on each pulse. The PWM control loop will automatically skip pulses to maintain output regulation. The bottom MOSFET is turned off by the current reversal comparator, IZERO, and the switch voltage will ring. This is discontinuous mode operation, and is normal behavior for the switching regulator.

Function Description (Continue)

Maximum Load Current

The TS3410 will operate with input supply voltage as low as 2.5V, however, the maximum load current decreases at lower input due to large IR drop on the main switch and synchronous rectifier. The slope compensation signal reduces the peak inductor current as a function of the duty cycle to prevent sub-harmonic oscillations at duty cycles greater than 50%. Conversely the current limit increases as the duty cycle decreases.

Application Information

Setting the Output Voltage

Application circuit item shows the basic application circuit with TS3410 adjustable output version. The external resistor sets the output voltage according to the following equation:

$$V_{out} = 0.6V \times \left(1 + \frac{R1}{R2}\right)$$

Table 1: Resistor Select for Output Voltage Setting

VOUT	R2	R1
1.2V	300K	300K
1.5V	300K	450K
1.8V	300K	600K
2.5V	300K	950K

Inductor Selection

For most designs, the TS3410 operates with inductors of 1μH to 4.7μH. Low inductance values are physically smaller but require faster switching, which results in some efficiency loss. The inductor value can be derived from the following equation:

$$L = \frac{V_{out} \times (V_{in} - V_{out})}{V_{in} \times \Delta I_L \times F_{osc}}$$

Table 2: Inductor Select for Output Voltage Setting (Vcc=3.6V)

VOUT	1.2V	1.5V	1.8V	2.5V
Inductor	2.2uH	2.2uH	2.2uH	2.2uH
Part Number WE-TPC	7440430027	7440430027	7440430027	7440430022

Note: Part Type MH or M (www.we-online.com)

Where is inductor Ripple Current. Large value inductors lower ripple current and small value inductors result in high ripple currents. Choose inductor ripple current approximately 35% of the maximum load current 1A, ΔIL=350mA.

For output voltages above 2.0V, when light-load efficiency is important, the minimum recommended inductor is 2.2μH. For optimum voltage-positioning load transients, choose an inductor with DC series resistance in the 50mΩ to 150mΩ range. For higher efficiency at heavy loads (above 200mA), or minimal load regulation (but some transient overshoot), the resistance should be kept below 100mΩ. The DC current rating of the inductor should be at least equal to the maximum load current plus half the ripple current to prevent core saturation (1A+170mA).

Input Capacitor Selection

The input capacitor reduces the surge current drawn from the input and switching noise from the device. The input capacitor impedance at the switching frequency shall be less than input source impedance to prevent high frequency switching current passing to the input. A low ESR input capacitor sized for maximum RMS current must be used. Ceramic capacitors with X5R or X7R dielectrics are highly recommended because of their low ESR and small temperature coefficients. A 4.7μF ceramic capacitor for most applications is sufficient.

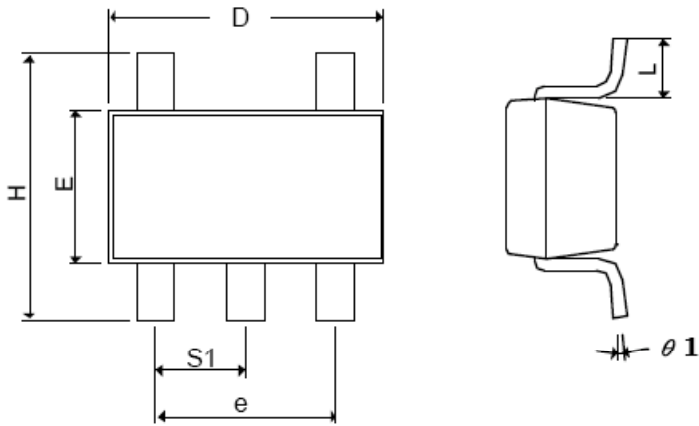
Application Information (Continue)**Output Capacitor Selection**

The output capacitor is required to keep the output voltage ripple small and to ensure regulation loop stability. The output capacitor must have low impedance at the switching frequency. Ceramic capacitors with X5R or X7R dielectrics are recommended due to their low ESR and high ripple current.

Compensation Capacitor Selection

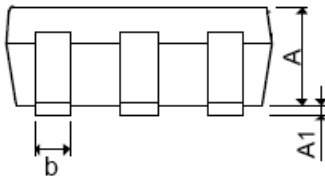
The compensation capacitors for increasing phase margin provide additional stability. It is required and more than 22p, Refer to Demo Board Schematic, The optimum values for C3 is 47pF.

SOT-25 Mechanical Drawing



DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX.
A+A1	0.09	1.25	0.0354	0.0492
B	0.30	0.50	0.0118	0.0197
C	0.09	0.25	0.0035	0.0098
D	2.70	3.10	0.1063	0.1220
E	1.40	1.80	0.0551	0.0709
E	1.90 BSC		0.0748 BSC	
H	2.40	3.00	0.09449	0.1181
L	0.35 BSC		0.0138 BSC	
θ1	0°	10°	0°	10°
S1	0.95 BSC		0.0374 BSC	

Front View



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