

>> 32 line bus interface

T-52-09

>> Individual control of each  
8 line group.

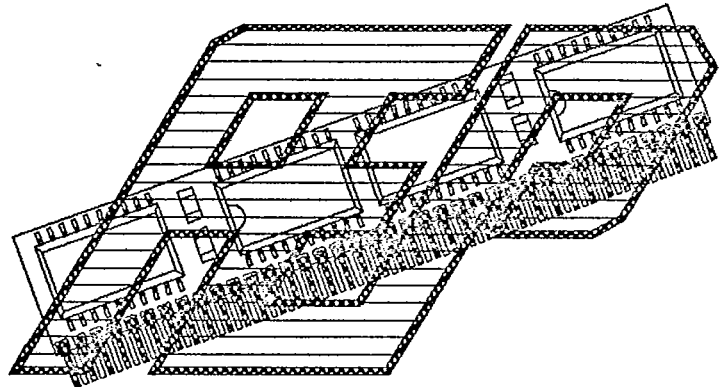
>> High density .070 center  
spaced ZIP leads for maximum  
I/O in minimum area.

>> Space saving vertical  
mounting orientation.

>> Convenient "in one side, out  
the other" broadside pin-out.

>> TTL compatible

>> Uses single +5V power supply



## 32 LINE BUFFER MODULE

### DESCRIPTION:

The AEPBZ32 is a high density 32 line buffer module ideal for driving 16 or 32 bit wide bus lines as well as general purpose applications. The vertical mounting orientation and compact I/O pin footprint make it superb for projects with tight space constraints.

Physically the module consists of an FR4 PC material substrate surface mounted with four 244 type buffer ICs, four 0.10 microfarad decoupling capacitors, and 75 I/O pins in a staggered ZIP package format. It can be ordered with any 244 type buffer from any family of IC manufacturing technology (such as LS, F, FCT or equivalent) that is available in a 20 pin SOP package. The module features four active low Output Enables, each controlling eight outputs.

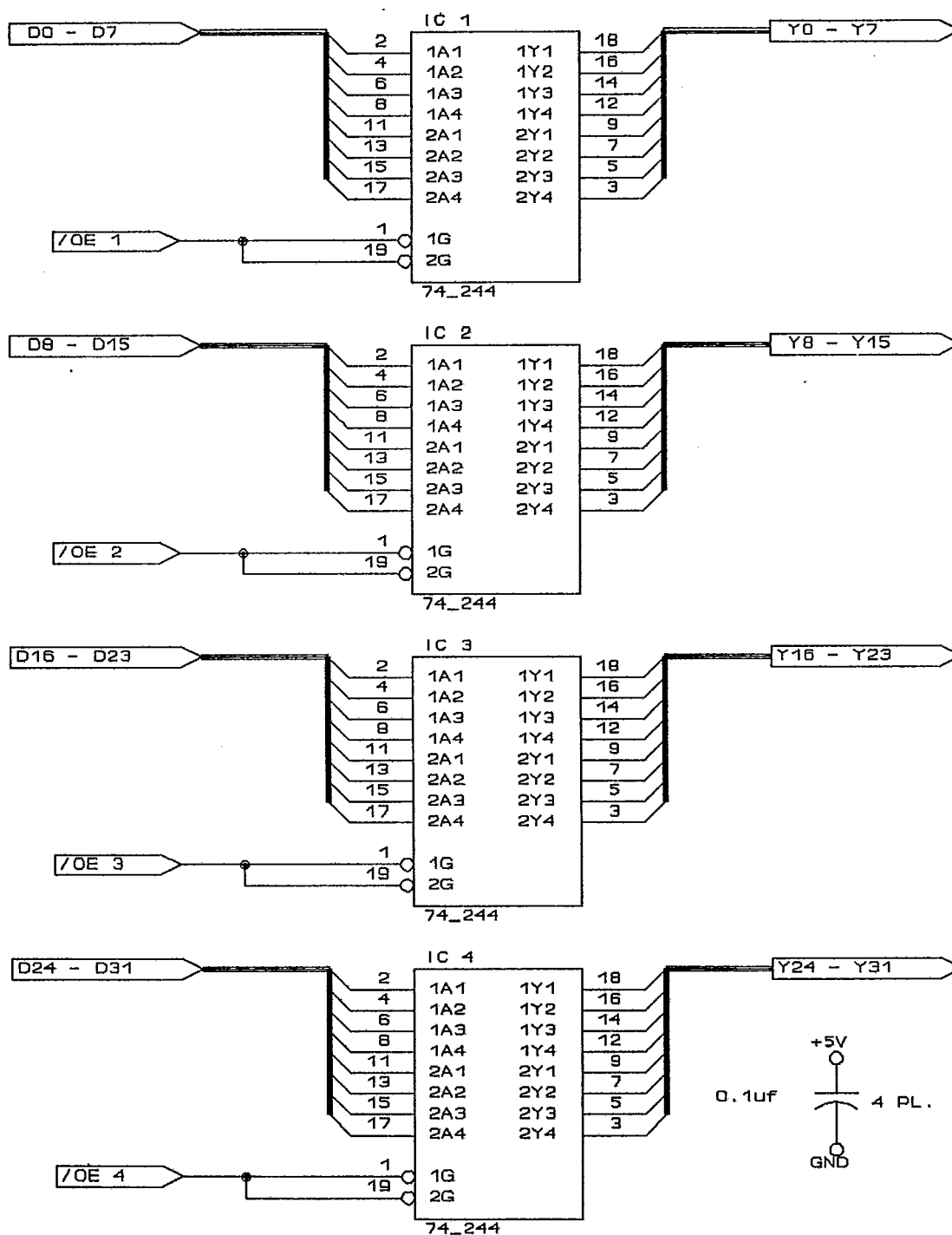
Performance specifications and electrical characteristics are determined by the IC devices used. These items can vary according to the type and manufacturer of the components. The necessary information is obtained from the IC vendors' data sheets, like those attached, or from their data books.

Mechanical dimensions are 0.505 in. high by 2.66 in. long by 0.22 in. wide. The I/O pins are in two rows which are 0.1 inch apart and offset longitudinally 0.035 inch. In each row the pins are on 0.070 inch center spacing. See included specification drawing.

The AEPBZ32 is one of a family of digital circuit support function modules which includes a 32 line transceiver, a 32 line register, and a 32 line latch module. These are natural companions for AEP memory modules.



### 32 LINE BUFFER MODULE AEPBZ32 FUNCTIONAL DIAGRAM



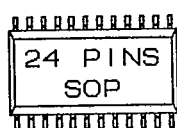
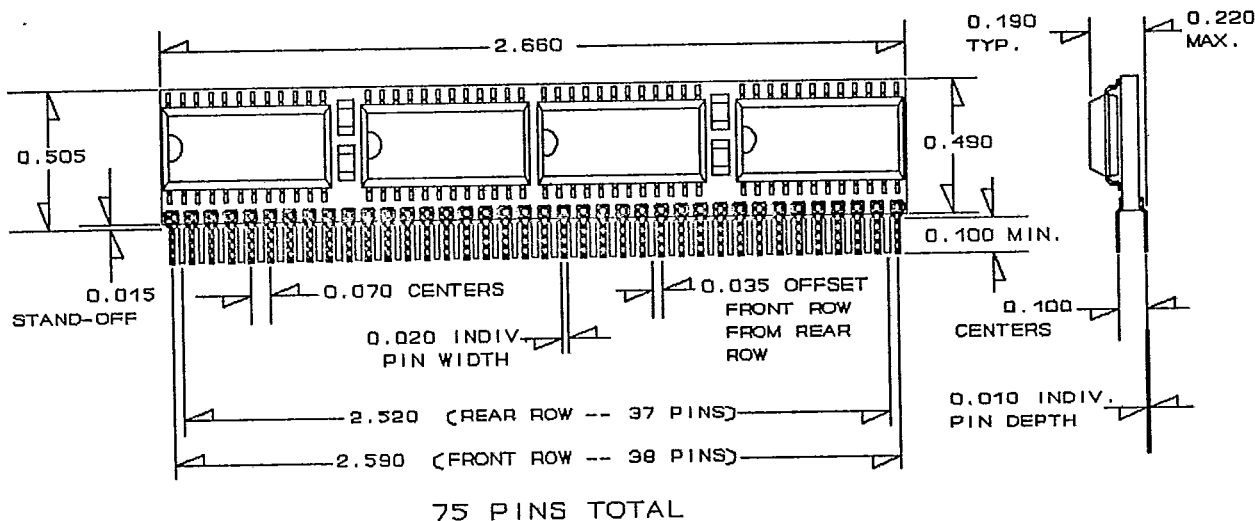
T-90-20

## AEP 32 LINE MODULE SERIES PACKAGING INFORMATION

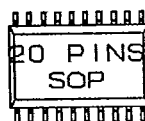
## SPECIFICATION DRAWING

APPLY TO: BZ, LZ, RZ, TZ, SRZ, TRZ, CZ

DIMENSIONS IN INCHES, TOLERANCE: +/- 0.010 UNLESS SPECIFIED.

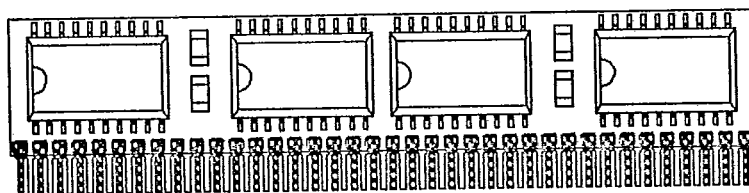


IC FOR LZ  
RZ  
SRZ  
TBZ



IC FORBZ  
CZ  
TZ

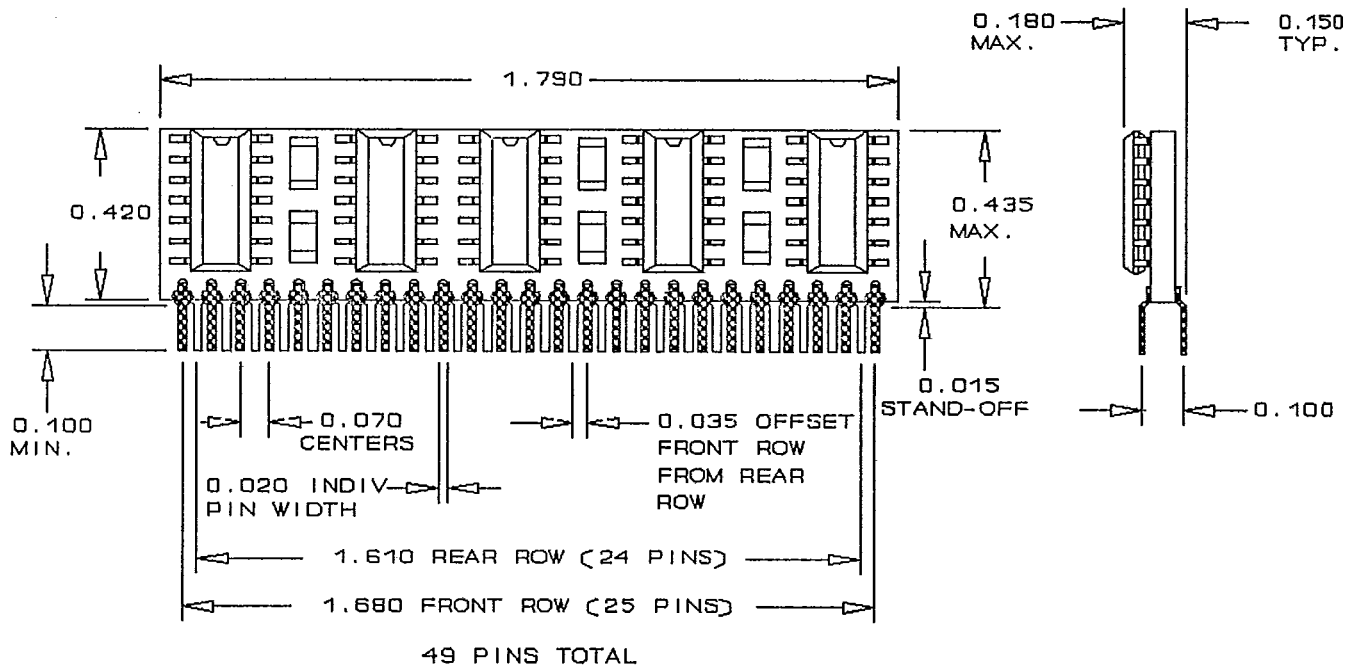
\*\* MODULE DIMENSIONS REMAIN UNCHANGE.



# AEP 32 LINE SUPPORT MODULE SERIES PARITY GENERATOR/CHECKER PACKAGING INFORMATION

APPLY TO: PZ-286, PZ-280

DIMENSIONS IN INCHES, TOLERANCE:  $\pm 0.010$  UNLESS SPECIFIED.



The AEPPZ32-280 uses only one pull up resistor and 3 capacitors.  
The AEPPZ32-286 uses three pull up resistors and 3 capacitors.



ADVANCED ELECTRONIC PACKAGING