Spec No.TQ3C-8EAF0-E1DEG05-00DateMay 22, 2008

### **TYPE : TCG057QVLBA-G20**

< 5.7 inch QVGA transmissive color TFT with LED backlight, Constant current circuit for LED backlight >

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### KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

This specification is subject to change without notice. Consult Kyocera before ordering.

	Original	Designed by: I	Engineering de	Confirmed by: QA dept.				
	Issue Date	Prepared	Checked	Approved	Checked	Approved		
	May 22, 2008	D. Ajisaka	79. Johnson	4 Matsumoto	.J. Sakaguchi	To , Jut		
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SPEC

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# Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnity, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

# Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.



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# 1. Application

This document defines the specification of TCG057QVLBA-G20. (RoHS Compliant)

### 2. Construction and outline

LCD	: Transmissive color dot matrix type TFT
Backlight system	: LED
Polarizer	: Glare treatment
Additional circuit	: Timing controller, Power supply (3.3V input)
	(with constant current circuit for LED Backlight)

### 3. Mechanical specifications

Item	Specification	Unit
Outline dimensions	$127.2 \text{ (W)} \times 100.4 \text{ (H)} \times 5.7 \text{ (D)}$	mm
Active area	115.2 (W) × 86.4 (H) (14.4cm / 5.7 inch (Diagonal))	mm
Dot format	$320 \times (B,G,R)$ (W) × 240 (H)	dot
Dot pitch	0.12 (W) × 0.36 (H)	mm
Base color *1	Normally White	-
Mass	110	g

\*1 Due to the characteristics of the LCD material, the color varies with environmental temperature.



#### 4. Absolute maximum ratings

#### 4-1. Electrical absolute maximum ratings

Item	Symbol	Min.	Max.	Unit
Supply voltage for logic	V <sub>DD</sub>	0	4.0	V
Input signal voltage *1	V <sub>IN</sub>	-0.3	6.0	V
Supply voltage for backlight	VINB	0	6.0	V
Backlight ON-OFF	BLEN	0	$V_{\rm IN}B$	V
Brightness adjust voltage	VBRT	0	$V_{\rm IN}B$	V

\*1 Input signal : CK, R0 ~ R5, G0 ~ G5, B0 ~ B5, H<sub>SYNC</sub>, V<sub>SYNC</sub>, ENAB, R/L, U/D

#### 4-2. Environmental absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Operating temperature	*1	TOP	-20	70	°C
Storage temperature	*2	Tsto	-30	80	°C
Operating humidity	*3	Нор	10	*4	%RH
Storage humidity	*3	H <sub>STO</sub>	10	*4	%RH
Vibration		-	*5	*5	-
Shock		-	*6	*6	-

\*1 Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.

```
*2 Temp. = -30°C < 48h , Temp. = 80°C < 168h
```

Store LCD panels at normal temperature/humidity. Keep them free from vibration and shock. An LCD panel that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard. (Please refer to "Precautions for Use" for details.)

- \*3 Non-condensing
- \*4 Temp. 40°C, 85%RH Max.

Temp. > 40°C, Absolute humidity shall be less than 85%RH at 40°C.

\*5

Frequency	$10 \thicksim 55 \ \mathrm{Hz}$	Acceleration value
Vibration width	0.15mm	$(0.3 \sim 9 \text{ m/s}^2)$
Interval	10-55-10	Hz 1 minutes

2 hours in each direction X, Y, Z (6 hours total) EIAJ ED-2531

- \*6 Acceleration: 490 m/s<sup>2</sup>, Pulse width: 11 ms
  - 3 times in each direction: ±X, ±Y, ±Z FIALED-2531

EIAJ ED-2531

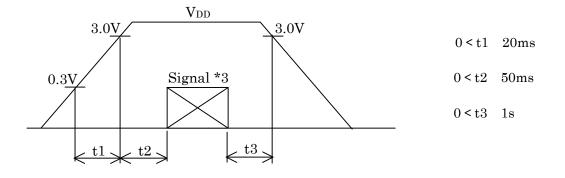


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# 5. Electrical characteristics

					Temp. = -2	0~70°C
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage for logic *1	V <sub>DD</sub>	-	3.0	3.3	3.6	V
Current consumption for logic	Idd	*2	-	60	80	mA
Permissive input ripple voltage	$V_{\rm RP}$	-	-	-	100	mVp-p
	V <sub>IL</sub>	"Low" level	0	-	$0.3 V_{DD}$	V
Input signal voltage *3	V <sub>IH</sub>	"High" level	$0.7 V_{DD}$	-	V <sub>DD</sub>	V

\*1  $V_{\rm DD}\mbox{-turn-on conditions}$ 



### \*2 Display pattern:

-etj petter	
$V_{DD} = 3.3 V, T_{CO}$	$emp. = 25^{\circ}C$
	123 456 789 • • • • • • • • • • • • • • • • • • •
1	
2	
3	
:	
:	
:	
239	
240	
(dot)	

\*3 Input signal : CK, R0 ~ R5, G0 ~ G5, B0 ~ B5, H\_{\rm SYNC}, V\_{\rm SYNC}, ENAB, R/L, U/D



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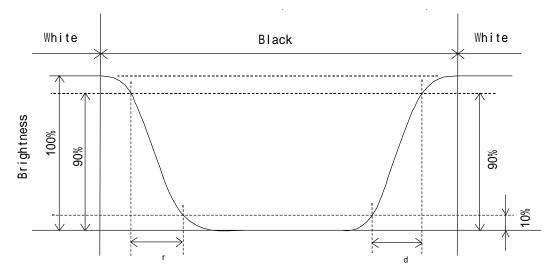
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# 6. Optical characteristics

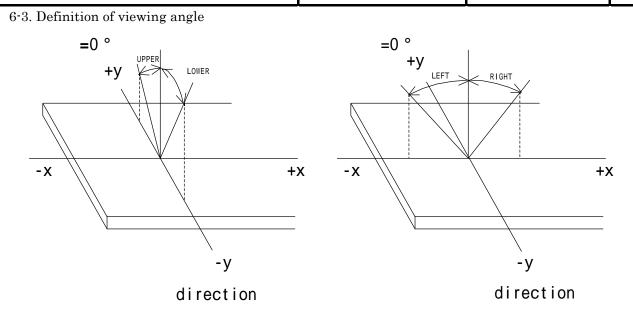
-				Meas	suring spot =	6.0mm, Te	emp. = 25°C
Item		Symbol	Condition	Min.	Тур.	Max.	Unit
December	Rise	$\tau_{\rm r}$	= =0°	-	10	-	ms
Response time	Down	τd	= =0°	-	25	-	ms
Viewing angle range View direction : 12 o'clock (Gray inversion)		UPPER		-	80	-	dam
		LOWER		-	80	-	deg.
		LEFT	CR 5	-	80	-	1
		$\phi$ right		-	80	-	deg.
Contrast ratio		CR	= =0°	300	500	-	-
Brightness		L	IF=15mA/Line	210	300	-	$cd/m^2$
	Ded	x	= =0°	0.57	0.62	0.67	
	Red	У		0.32	0.37	0.42	
	Constant	х	= =0°	0.28	0.33	0.38	
Chromaticity	Green	У	0	0.54	0.59	0.64	_
coordinates	Blue	х	= =0°	0.09	0.14	0.19	-
	Diue	У	0	0.04	0.09	0.14	
	White	x	= =0°	0.27	0.32	0.37	
	wnite	у	0	0.29	0.34	0.39	

6-1. Definition of contrast ratio

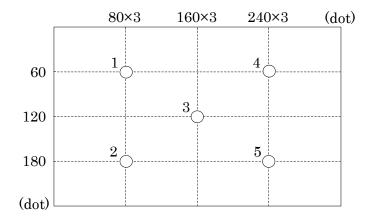
#### 6-2. Definition of response time







6-4. Brightness measuring points



1) Rating is defined on the average in the viewing area.

2) Measured 30 minutes after the LED is powered on. (Ambient temp. =  $25^{\circ}$ C)



# 7. Interface signals

No.	Symbol	Description	I/O	Note
1	GND	GND	-	
2	СК	Clock signal for sampling each data signal	Ι	
3	H <sub>SYNC</sub>	Horizontal synchronous signal (negative)	Ι	
4	V <sub>SYNC</sub>	Vertical synchronous signal (negative)	Ι	
5	GND	GND	-	
6	R0	RED data signal (LSB)	Ι	
7	R1	RED data signal	Ι	
8	R2	RED data signal	Ι	
9	R3	RED data signal	Ι	
10	R4	RED data signal	Ι	
11	R5	RED data signal (MSB)	Ι	
12	GND	GND	-	
13	G0	GREEN data signal (LSB)	Ι	
14	G1	GREEN data signal	Ι	
15	G2	GREEN data signal	Ι	
16	G3	GREEN data signal	Ι	
17	G4	GREEN data signal	Ι	
18	G5	GREEN data signal (MSB)	Ι	
19	GND	GND	-	
20	B0	BLUE data signal (LSB)	Ι	
21	B1	BLUE data signal	Ι	
22	B2	BLUE data signal	Ι	
23	B3	BLUE data signal	Ι	
24	B4	BLUE data signal	Ι	
25	B5	BLUE data signal (MSB)	Ι	
26	GND	GND	-	
27	ENAB	Signal to settle the horizontal display position (positive)	Ι	*1
28	V <sub>DD</sub>	3.3V power supply	-	
29	VDD	3.3V power supply	-	
30	R/L	Horizontal display mode select signal L : Normal , H : Left / Right reverse mode	I	*2
31	U/D	Vertical display mode select signal H : Normal , L : Up / Down reverse mode	I	
32	NC	No connect	-	
33	VINB	Power supply for LED backlight	-	
34	VINB	Power supply for LED backlight	-	
35	VINB	Power supply for LED backlight	-	
36	BLEN	Backlight ON-OFF (H:ON, L:OFF)	-	
37	VBRT	Brightness adjust voltage	-	
38	GNDB	GND for LED backlight	-	
39	GNDB	GND for LED backlight	-	
40	GNDB	GND for LED backlight	-	1

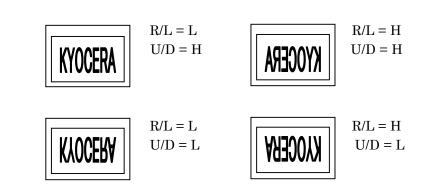
LCD connector Recommended matching FFC or FPC : IMSA-9681S-40A-GF (IRISO)

: 0.5mm pitch



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\*1 The horizontal display start timing is settled in accordance with a rising timing of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined. Don't keep ENAB "High" during operation.





\*2

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### 8. Input timing characteristics

	Item	Symbol	Min.	Typ.	Max.	Unit	Note
Clock	Frequency	1/Tc	-	6.3	7.0	MHz	
Clock	Duty ratio	Tch/Tc	40	50	60	%	
Data	Set up time	Tds	12	-	-	ns	
Data	Hold time	Tdh	12	-	-	ns	
	Cycle	TH	50.0	63.6	-	μs	
	-	П	360	400	450	clock	
Horizontal sync. signal	Pulse width	THp	5	30	-	clock	
Signal	Set up time	THs	12	-	-	ns	
	Hold time	THh	12	-	-	ns	
	Cycle	TV	251	262	280	line	
Vertical sync.	Pulse width	TVp	1	3	5	line	
signal	Set up time	TVs	12	-	-	ns	
	Hold time	TVh	12	-	-	ns	
	Pulse width	TEp		320		clock	
Enable signal (ENAB )	Set up time	TEs	12	-	-	ns	
	Hold time	TEh	12	-	-	ns	
H <sub>SYNC</sub> - Enable signal phase difference		THE	36	68	88	clock	
Vertical sync. sig	nal start position	TVE	2	18	38	line	
Horizontal displa	ay period	THd	320			clock	
Vertical display	period	TVd		240		line	

#### 8-1. Timing characteristics

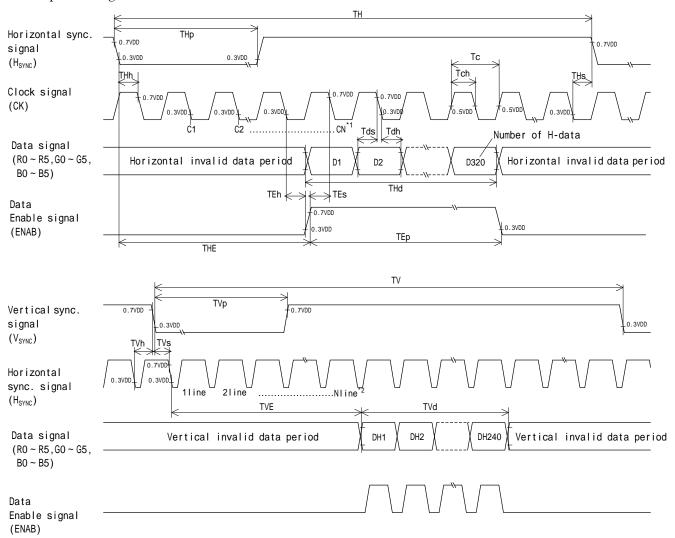
\*When ENAB is fixed at "Low", the horizontal display starts from the data of C68 (clock) as shown in 8-3.

\*When ENAB is fixed at "Low", the vertical sync. signal start position is 18 (line) as shown in 8-3. \*In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

8-2. Input Data Signals and Display position on the screen

D1,DH1	D2,DH1	D3,DH1		D320,DH1
D1,DH <b>2</b>	D2,DH2	D3,DH2		L
	_	_		
	÷			
			B G R	
	r r		1	
D1,DH <b>2</b> 40	D2,DH240	D3,DH240		





#### 8-3. Input timing characteristics

\*1 When ENAB is fixed at "Low", the horizontal display starts from the data of C68 (clock).

\*2 When ENAB is fixed at "Low", the vertical sync. signal start position is 18 (line).



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Temp.=25

#### 9. Backlight characteristics

						10mp. <b>2</b> 0
Item	Symbol	Min.	Тур.	Max.	Unit	Note
Supply voltage	$V_{\rm IN}B$	3.0	-	5.5	V	Ta=-20 ~ 70°C
ON-OFF (H)	DIEN	$0.8 V_{\rm IN} B$	-	$V_{\rm IN}B$	V	Ta=-20 ~ 70°C
ON-OFF (L)	BLEN	0	-	$0.2 \mathrm{V_{IN}B}$	V	Ta=-20 ~ 70°C
LED forward current *1, *2	IF	14	15	16	mA	VBRT=0 ~ 1.4V
LED forward current "1, "2		2.8	3.0	3.2		VBRT=2.8V
Currenter current	IINB	-	440	570	mA	V <sub>IN</sub> B=3.3V, IF=15mA
Supply current		-	280	360	mA	V <sub>IN</sub> B=5.0V, IF=15mA
Operating life time *3, *4	Т	-	40,000	-	h	IF=15mA, Ta=25

\*1 For each "LED"

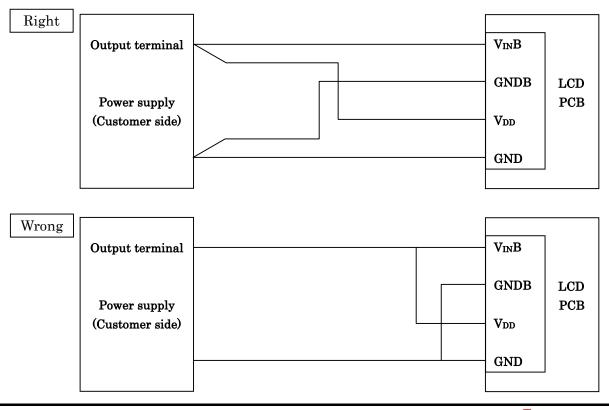
\*2 An input current below 5.0mA may reduce the brightness uniformity of the LED backlight. This is because the amount of light from each LED chip is different. Therefore, please evaluate carefully before finalizing the input current.

\*3 When brightness decrease 50% of initial brightness.

- \*4 Life time is estimated data. (Condition : IF=15mA, Ta=25 in chamber)
- \* When you start-up, please charge in sequence of  $V_{\rm IN}B\mbox{->}BLEN,$  or VBRT.

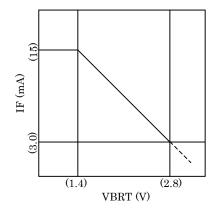
When you shut-down, please stop in sequence of BLEN and/or VBRT->VINB.

- \* Please do not connect the other than our backlight to this output connector on the PCB.
- \* In case V<sub>DD</sub> and V<sub>IN</sub>B are supplied by a single power source, V<sub>DD</sub> & V<sub>IN</sub>B, and GND are connected directly and separately from the output on the power source. If the common wire are used for V<sub>DD</sub> & V<sub>IN</sub>B, and for GND, and are split near the PCB, and connect to each LCD driving circuit and backlight driving circuit, a flicker might be occurred due to a ripple between the both circuit.





### \* VBRT-IF characteristics





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#### 10. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.

 $TCG057QVLBA-G20 - \Box\Box - \Box\Box - \Box MADE IN \Box\Box\Box\Box\Box$ 

$\downarrow\downarrow\downarrow$	$\downarrow$	$\downarrow$
$1\ 2$	3	4

- No1. No5. above indicate
  - 1. Year code
  - 2. Month code
  - 3. Date
  - 4. Version Number
  - 5. Country of origin (Japan or China)

 $\downarrow 5$ 

Year	2008	2009	2010	2011	2012	2013
Code	8	9	0	1	2	3

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Code	1	2	3	4	5	6

Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	Х	Y	Z

#### 11. Warranty

#### 11-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

#### 11-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.



#### 12. Precautions for use

- 12-1. Installation of the LCD
  - 1) A transparent protection plate shall be added to protect the LCD and its polarizer.
  - 2) The LCD shall be installed so that there is no pressure on the LSI chips.
  - 3) The LCD shall be installed flat, without twisting or bending.
  - 4) A transparent protection sheet is attached to the polarizer. Please remove the protection film slowly before use, paying attention to static electricity.

#### 12-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.

12-3. LCD operation

- 1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
- 12-4. Storage
  - 1) The LCD shall be stored within the temperature and humidity limits specified. Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
  - 2) Always store the LCD so that it is free from external pressure onto it.

12-5. Usage

- 1) <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) The front polarizer is easily scratched or damaged. Prevent touching it with any hard material, and from being pushed or rubbed.
- 3) The LCD screen may be cleaned by wiping the screen surface with a soft cloth or cotton pad using a little Ethanol.
- 4) Water may cause damage or discoloration of the polarizer. Clean condensation or moisture from any source immediately.
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not disassemble LCD module because it will result in damage.
- 7) This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 8) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 9) Liquid crystal may leak when the module is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.



### 13. Reliability test data

Test item	Test condition	Test time	Jud	gement
High temp. atmosphere	80°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Low temp. atmosphere	-30°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. humidity atmosphere	40°C 90% RH	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	10cycles	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. operation	70°C	500h	Display function Display quality Current consumption	: No defect : No defect : No defect

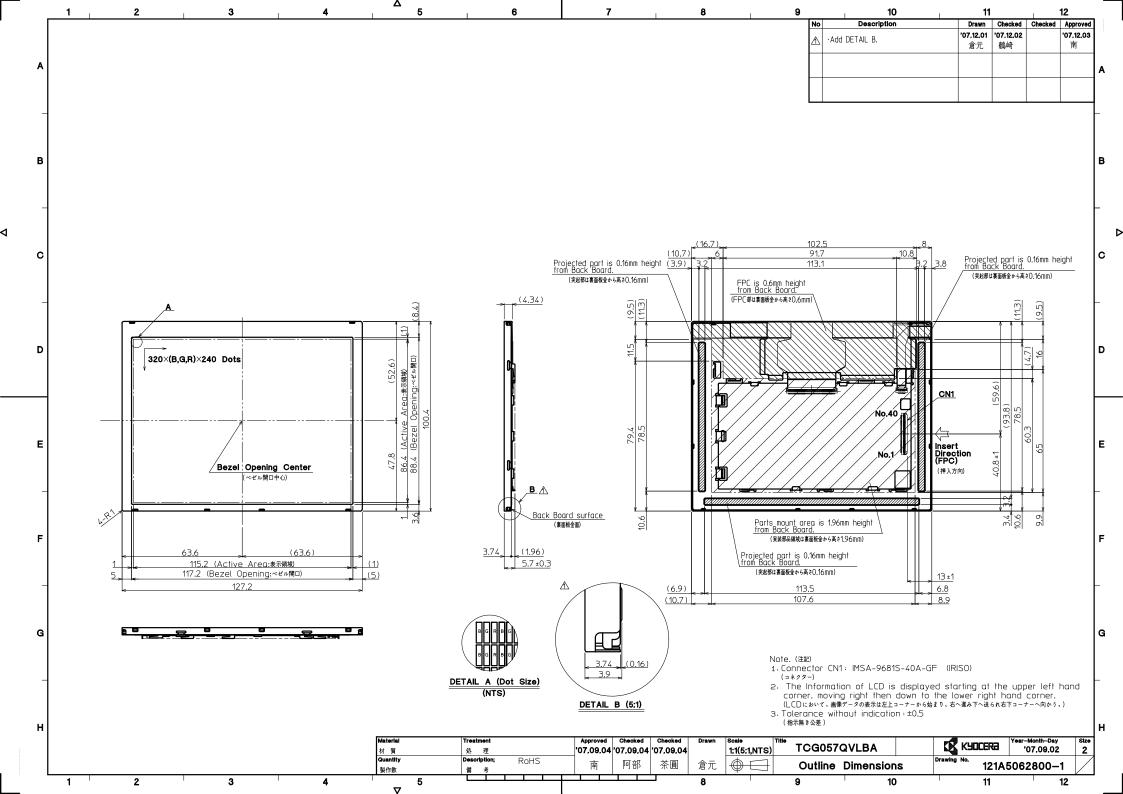
\* Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

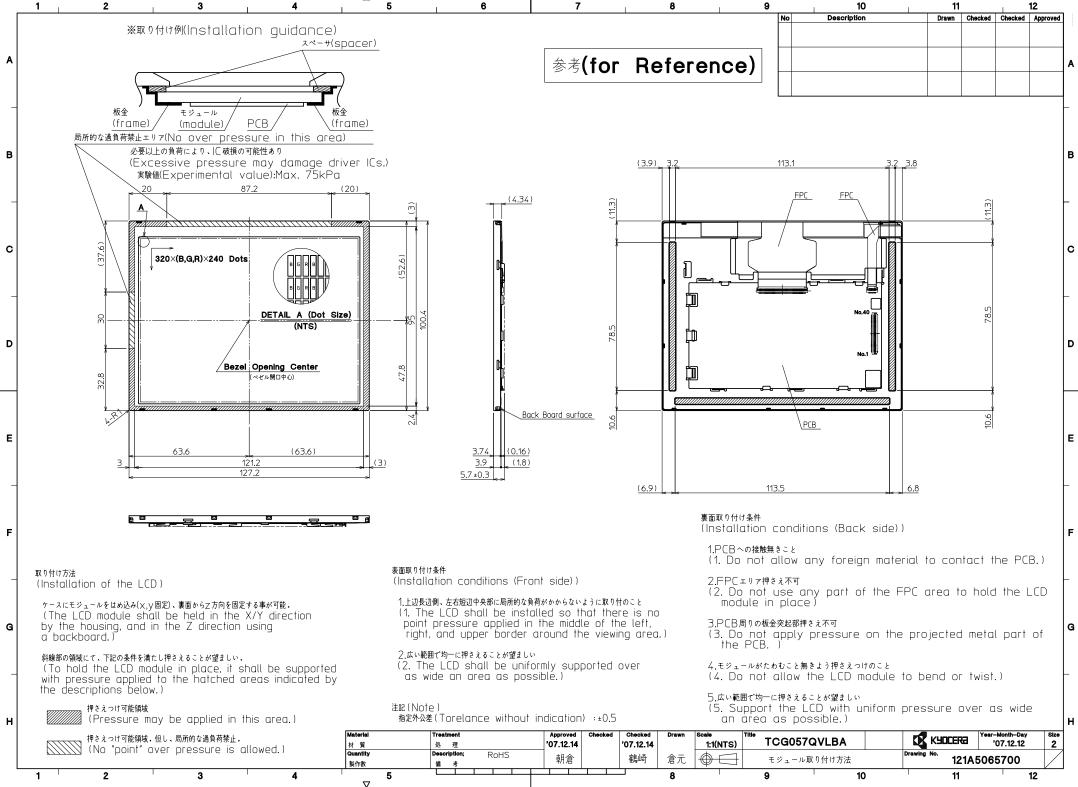
\* The LCD is tested in circumstances in which there is no condensation.

\* The reliability test is not an out-going inspection.

\* The result of the reliability test is for your reference purpose only. The reliability test is conducted only to examine the LCD's capability.







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Spec No.	TQ3C-8EAF0-E2DEG05-00
Date	May 22, 2008

# KYOCERA INSPECTION STANDARD

# **TYPE : TCG057QVLBA-G20**

### KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

Original	Designed by :	Engineering de	Confirmed by : QA dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved
May 22, 2008	D. Ajisaka	79. Johnsond	4. Matsumoto	.J. Sakaguchi	Zo , Jul



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	Revision record							
	Date		ed by :	Engineering of	lept.	Confirmed by : QA dept.		
			ared	Checked	Approved	Checked	Approved	
Rev.No.	Date	Page			Descripti	ons		



1	

1) Note							
			Note				
General	<ol> <li>Customer identified anomalies not defined within this inspection standard shall be reviewed by Kyocera, and an additional standard shall be determined by mutual consent.</li> </ol>						
	2. This inspection standard about the image quality shall be applied to any defect within the active area and shall not be applicable to outside of the area.						
	3. Inspection conditions						
	Lumina	ance	: 500 Lux min.				
	_	ion distance	: 300 mm. : 25 ± 5				
	Temper						
D (	Directio		: Directly above				
Definition of	Dot defect	Bright dot defect	The dot is constantly "on" when power applied to the				
inspection item			LCD, even when all "Black" data sent to the screen. Inspection tool: 5% Transparency neutral density filter.				
			Count dot: If the dot is visible through the filter.				
			Don't count dot: If the dot is not visible through the				
			filter.				
			R     G     B     R     G     B       R     G     B     R     G     B       R     G     B     R     G     B       R     G     B     R     G     B   dot defect				
		Black dot defect	The dot is constantly "off" when power applied to the LCD, even when all "White" data sent to the screen.				
		Adjacent dot	Adjacent dot defect is defined as two or more bright dot defects or black dot defects.				
			R     G     B     R     G     B       R     G     B     R     G     B       R     G     B     R     G     B       R     G     B     R     G     B       R     G     B     R     G     B       R     G     B     R     G     B       R     G     B     R     G     B   dot defect				
	External	Bubble, Scratch,	Visible operating (all pixels "Black" or "White") and non				
	inspection	Foreign particle (Polarizer, Cell, Backlight)	operating.				
		Appearance inspection	Does not satisfy the value at the spec.				
	Others	LED wire	Damaged to the LED wires, connector, pin, functional failure or appearance failure.				
	Definition	Definition of	**				
	of size	<b>a</b> d = (a +					

# Visuals specification



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#### 2) Standard

2) Standard Classification Inspection item			Judgement standard				
Defect	*		Acceptable number : 4				
(in LCD	defect	Dright dot dolot					n or more
glass)		Black dot defect		Acceptable number : 5			
8		Diack dot delect		•			n or more
		2 dot join Bright dot defect		Acceptable number	: 2		
		Black dot defect		Acceptable number		: 3	
		3 or more of	dots join	Acceptable number		: 0	
		Total dot d	efects	Acceptable number		÷5 Ma	X
	Others	White dot,	Dark dot				
		(Circle)		Size (mn	n)	Ac	ceptable number
				d	0.2		(Neglected)
				0.2 < d	0.4		5
				0.4 < d	0.5		3
				0.5 < d			0
Extornal	inspection	Polarizer (	Seretah)				
(Defect on	-			Width (mm)	Longth (	mm)	Acceptable number
Polarizer				Width (IIIII) W 0.1	Length (mm)		(Neglected)
				0.1 < W = 0.3	L 5.0		(Neglected)
	between Polarizer				5.0 < L		0
and LCD glass)				0.3 < W	-		0
-		Polarizer (Bubble)					
		1 Ular izer (	Dubble/	Size (mm	<u></u>	1	contable number
				Size (mm) d 0.2		Acceptable number (Neglected)	
				0.2 < d	0.3		5
				0.3 < d 0.5		3	
				0.5 < d		0	
		Foreign pa	rticle			•	
		( Circular shape )		Size (mm)		Acceptable number	
		( oneular shape )		d 0.2		(Neglected)	
				0.2 < d 0.4			
				0.4 < d 0.5		3	
				0.5 < d			0
Foreign particle		rticle					
		( Linear shape )		Width (mm) Lengt		h (mm) Acceptable number	
		(Linear snape) Scratch		Width (mm) Length W 0.03 -		h (mm) Acceptable number - (Neglected)	
		Scratch		0.03 < W 0.1	$\begin{array}{c c} & L & 2.0 \\ \hline 2.0 < L & 4.0 \end{array}$		(Neglected)
							3
					4.0 < L		0
				0.1 < W	-		(According to
							circular shape)
					•		

