

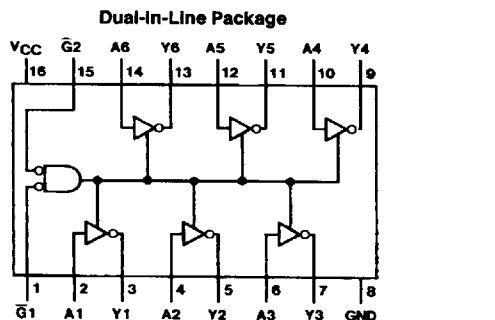
DM54LS366A/DM74LS366A Hex TRI-STATE® Inverting Buffer

General Description

This device contains six independent gates each of which performs an inverting buffer function. The outputs have the TRI-STATE feature. When enabled, the outputs exhibit the low impedance characteristics of a standard LS output with additional drive capability to permit the driving of bus lines without external resistors. When disabled, both the output

transistors are turned off presenting a high-impedance state to the bus line. Thus the output will act neither as a significant load nor as a driver. To minimize the possibility that two outputs will attempt to take a common bus to opposite logic levels, the disable time is shorter than the enable time of the outputs.

Connection Diagram



Order Number DM54LS366AJ, DM54LS366AW,
DM54LS366AE, DM74LS366AM or DM74LS366AN
See NS Package Number E20A, J16A, M16A, N16E or W16A

Function Table

$Y = \bar{A}$

Inputs			Output
$\bar{G}1$	$\bar{G}2$	A	Y
H	X	X	Hi-Z
X	H	X	Hi-Z
L	L	L	H
L	L	H	L

H = High Logic Level

L = Low Logic Level

X = Either Low or High Logic Level

Hi-Z = TRI-STATE (Outputs are disabled)

TRI-STATE® is a registered trademark of National Semiconductor Corporation.

Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage	7V
Input Voltage	7V
Operating Free Air Temperature Range	–55°C to +125°C
DM54LS	0°C to +70°C
DM74LS	–65°C to +150°C

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	DM54LS366A			DM74LS366A			Units
		Min	Nom	Max	Min	Nom	Max	
V _{CC}	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
V _{IH}	High Level Input Voltage	2			2			V
V _{IL}	Low Level Input Voltage			0.7			0.8	V
I _{OH}	High Level Output Current			–1			–2.6	mA
I _{OL}	Low Level Output Current			12			24	mA
T _A	Free Air Operating Temperature	–55		125	0		70	°C

Electrical Characteristics over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 1)	Max	Units
V _I	Input Clamp Voltage	V _{CC} = Min, I _I = –18 mA			–1.5	V
V _{OH}	High Level Output Voltage	V _{CC} = Min, I _{OH} = Max V _{IL} = Max, V _{IH} = Min	2.4	3.4		V
V _{OL}	Low Level Output Voltage	V _{CC} = Min, I _{OL} = Max		0.25	0.4	V
		V _{IL} = Max, V _{IH} = Min		0.35	0.5	
		I _{OL} = 12 mA, V _{CC} = Min		0.25	0.4	
I _I	Input Current @ Max Input Voltage	V _{CC} = Max, V _I = 7V	DM74		0.1	mA
		V _{CC} = Max, V _I = 10.0V	DM54			
I _{IH}	High Level Input Current	V _{CC} = Max, V _I = 2.7V			20	μA
I _{IL}	Low Level Input Current	V _{CC} = Max, V _I = 0.5V (Note 4)	A Input		–20	μA
		V _{CC} = Max, V _I = 0.4V (Note 5)	A Input		–0.4	mA
		V _{CC} = Max, V _I = 0.4V	$\bar{G}$ Input		–0.4	
I _{OZH}	Off-State Output Current with High Level Output Voltage Applied	V _{CC} = Max, V _O = 2.4V V _{IH} = Min, V _{IL} = Max			20	μA
I _{OZL}	Off-State Output Current with Low Level Output Voltage Applied	V _{CC} = Max, V _O = 0.4V V _{IH} = Min, V _{IL} = Max			–20	μA
I _{OS}	Short Circuit Output Current	V _{CC} = Max (Note 2)	DM54	–30	–130	mA
			DM74	–20	–100	
I _{CC}	Supply Current	V _{CC} = Max (Note 3)		12	21	mA

Note 1: All typicals are at V_{CC} = 5V, T_A = 25°C.

Note 2: Not more than one output should be shorted at a time, and the duration should not exceed one second.

Note 3: I_{CC} is measured with the DATA inputs grounded and the OUTPUT CONTROLS at 4.5V.

Note 4: Both $\bar{G}$ inputs are at 2V.

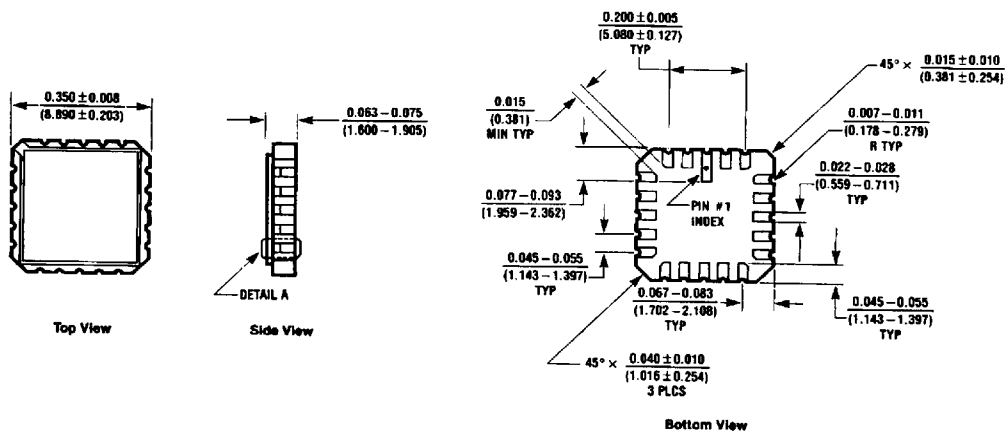
Note 5: Both $\bar{G}$ inputs at 0.4V.

Switching Characteristics:

Symbol	Parameter	DM54LS		DM74LS				Units
		C _L = 50 pF		R _L = 667Ω				
				C _L = 50 pF		C _L = 150 pF		
		Min	Max	Min	Max	Min	Max	
t _{PLH}	Propagation Delay Time Low to High Level Output		12		15		25	ns
t _{PHL}	Propagation Delay Time High to Low Level Output		22		16		25	ns
t _{PZH}	Output Enable Time to High Level Output		24		30		35	ns
t _{PZL}	Output Enable Time to Low Level Output		30		30		40	ns
t _{PHZ}	Output Disable Time from High Level Output (Note 6)		25		20			ns
t _{PLZ}	Output Disable Time from Low Level Output (Note 6)		20		20			ns

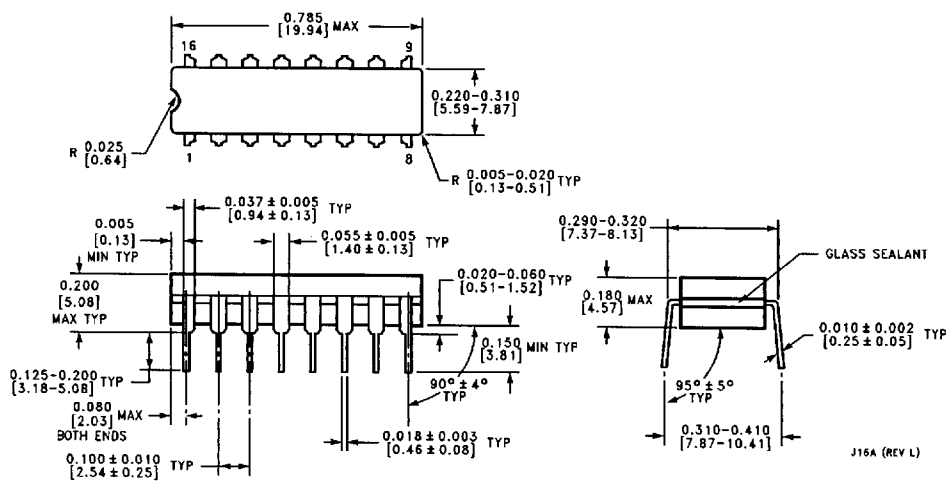
Note 6: $C_L = 5 \text{ pF}$.

Physical Dimensions inches (millimeters)



Ceramic Leadless Chip Carrier Package (E)
Order Number DM54LS366AE
NS Package Number E20A

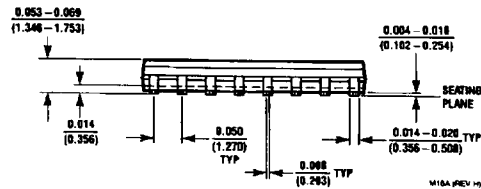
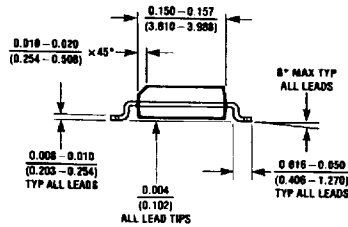
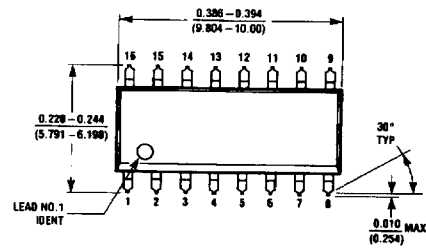
E20A (REV D)



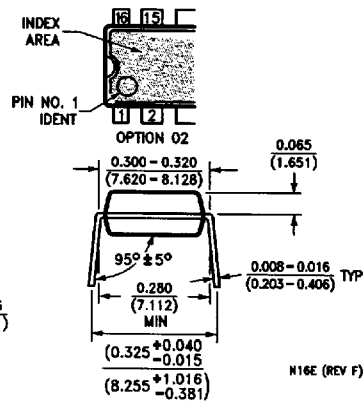
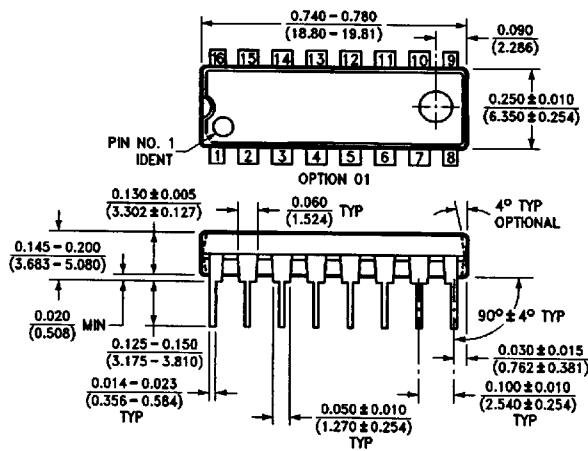
16-Lead Ceramic Dual-In-Line Package (J)
Order Number DM54LS366AJ
NS Package Number J16A

J16A (REV L)

Physical Dimensions inches (millimeters) (Continued)

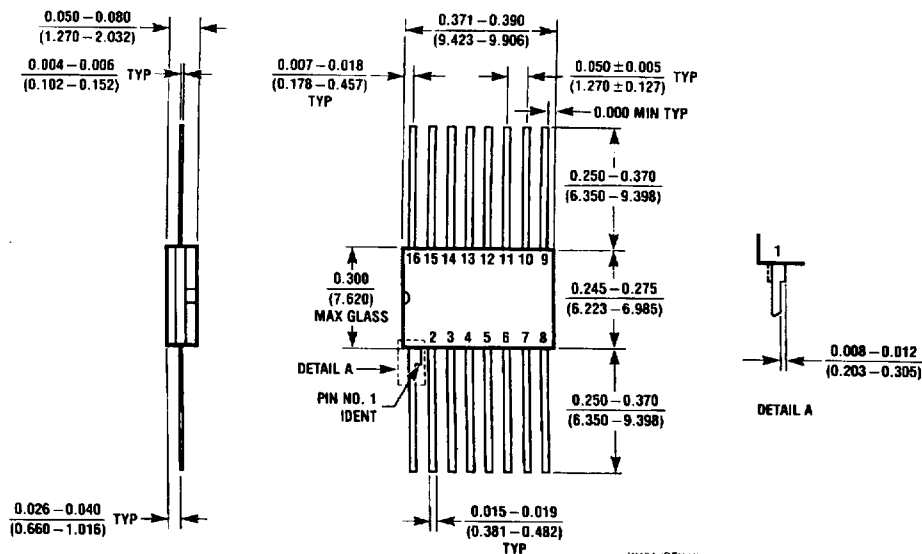


16-Lead Small Outline Molded Package (M)
Order Number DM74LS366AM
NS Package Number M16A



16-Lead Molded Dual-In-Line Package (N)
Order Number DM74LS366AN
NS Package Number N16E

Physical Dimensions inches (millimeters) (Continued)



16-Lead Ceramic Flat Package (W)
Order Number DM54LS366AW
NS Package Number W16A

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2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.



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