

Lynx 386SX non-cache Chipset Specifications

Process technology used	1.0μ CMOS	22cm x 22cm half baby AT board	Yes
CPU's supported	Intel 386SX AMD Am386SX Cyrix 486SLC TI 486SLC IBM 386SLC 486 SLC2	Hidden refresh Shadow RAM range maximum size/block size	Yes 256KB/64KB
CPU speed supported	20/25/33/40 MHz	Programmable wait states	Yes
Coprocessors supported (No external PALS required)	WEITEK 387SX	Local Bus support	Yes
		PQFP package	160
AT clock generation	Async	External TTL component	Less than 9
Hardware/Software turbo switching	Yes	DRAM speed	80ns
Page Mode DRAM control	Yes	Fast gate A20	Yes
DRAM type supported	256K/1M/4M	Fast reset	Yes
Max physical memory	16 MB		
Mixing DRAMS	Yes		

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