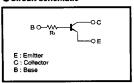
# **Digital transistor (built-in resistors)**

### DTB114TK

### **●**Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors.
- The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input, and parasitic effects are almost completely eliminated.
- Only the on/off conditions need to be set for operation, making device design easy.
- 4) Higher mounting densities can be achieved.

### Circuit schematic



### ●Absolute maximum ratings (Ta=25℃)

Parameter	Symbol	Limits	Unit
Collector-base voltage	Vсво	-50	V
Collector-emitter voltage	VCEO	40	٧
Emitter-base voltage	VEBO	-5	٧
Collector current	1c	-500	mA
Collector power dissipation	Pc	200	mW
Junction temperature	Τj	150	r
Storage temperature	Tstg	-55~150	°C

### ●Package, marking, and packaging specifications

Type	DTB114TK
Package	SMT3
Marking	E94
Code	T146
Basic ordering unit (pleces)	3000

### ●Electrical characteristics (Ta=25℃)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	-50		_	٧	Ic=-50 μA
Collector-emitter breakdown voltage	BVcso	40		-	٧	Ic=-1mA
Emitter-base breakdown voltage	BVEBO	-5		_	V	l∈= -50 μ A
Collector cutoff current	Iceo	T -		-0.5	μΑ	Vce=-50V
Emitter cutoff current	lebo	_	_	-0.5	μА	VEB=-4V
Collector-emitter saturation voltage	VCE(sat)	T -	_	0.3	V	Ic/Is=-50mA/-2.5mA
DC current gain	hre	100	250	600		Ic=-50mA , VcE=-5V
Input resistance	R <sub>1</sub>	7	10	13	kΩ	_
Transition frequency	fr	_	200	_	MHz	Vce=-10V , IE=5mA , f=100MHz

<sup>\*</sup> Transition frequency of mounted transistor.

(96-294-B114T)

# Digital transistor (built-in resistors), driver (60V, 1A)

### Features

- 1) High DC current gain. (Min. 300 at Vo/lo=2V/0.5A)
- 2) Low output voltage. (Typ. 0.4V at lo/li=500/50mA)
- 3) Built-in zener diode gives strong protection against reverse

### ●Absolute maximum ratings (Ta=25℃)

Parameter	Symbol	Limits	Unit		
Supply voltage	Voc	60±10	٧		
Input voltage	1/ 40				
	Vin	-6	V		
	lo 1		A		
Collector current	ÎC(Mex.)	2	A (Pulse)	*1	
Power dissipation	Pd	1.5	W	*2	
Junction temperature	Tj	150	Ç		
Storage temperature	Tstg	Tstg			

DTDS23YP

E02

T100

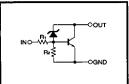
●Package, marking, and packaging specifications

Type Package Marking

Code

Basic ordering unit (pleces)

### ●Circuit schematic



Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
1	Vi(off)	T -	_	0.3	V	Vcc≃5V , lo=100 μA	
Input voltage	VI(on)	2	_	l –	٧ ا	Vo=0.4V , lo=100mA	
Output voltage	VO(on)	1 -	-	0.4	٧	lo/li=500mA/5mA	
Input current	li li	T -	_	3.6	mA	Vi=5V	
Output current	lo(off)	_	-	0.5	μA	Vcc=40V , Vi=0V	
DC current gain	Gr	300	_	_	_	Vo=2V , Io=500mA	
Transition frequency	fτ	T	80	T -	MHz	VcE=5V , IE=-0.1A , f=30MHz	
Input resistance	R <sub>1</sub>	1.54	2.2	2.86	kΩ	_	
Emitter-base resistance		7	10	13	kΩ	_	

<sup>\*</sup> Transition frequency of mounted transistor.

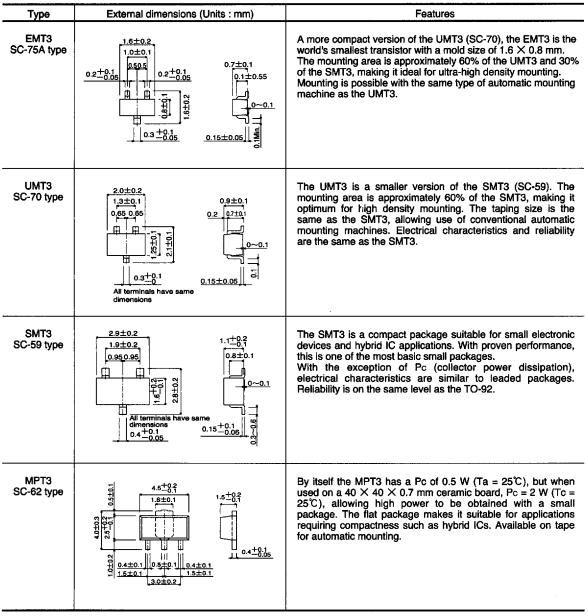
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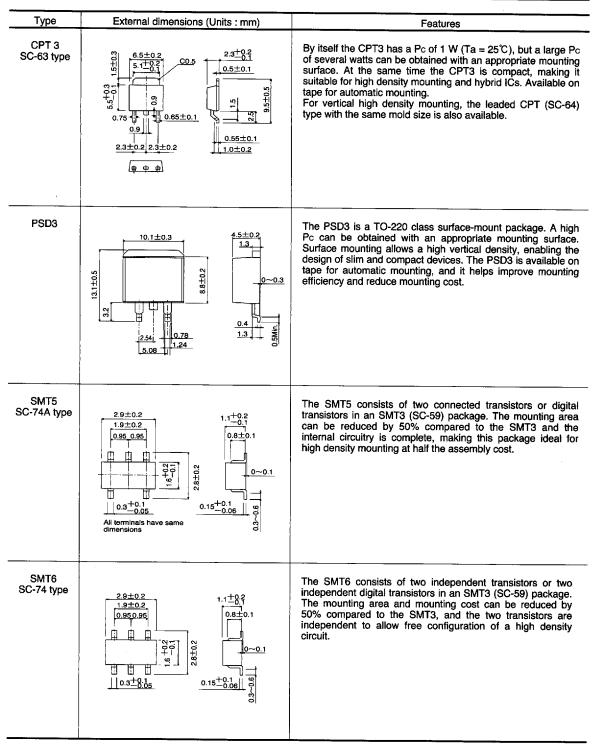
(96-378-DS23YP)

## **Packages**

ROHM has been manufacturing transistors since 1975. In the development of products, we constantly strive to anticipate the needs of our customers. Regarding packages, the demands of the market for compactness, low power consumption, low power dissipation and automatic mounting support are becoming ever greater, and we are strengthening our product development system to meet these needs.

Types and features of surface-mount packages

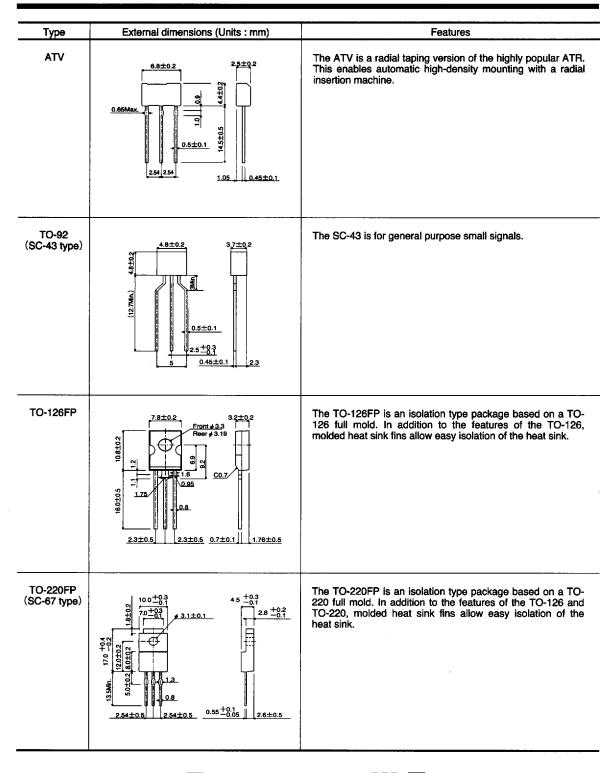




Туре	External dimensions (Units : mm)	Features
UMT5 SC-88A type	2.0±0.2 1,3±0.1 0,65 0,95 1,3±0.1 0,7 1,3±0.1 0,7 1,3±0.1 0,7 1,3±0.1 0,7 1,3±0.1 0,7 1,3±0.1 1,3±0.1 1,0,0,0,0,0,0 1,0,0,0,0,0 1,0,0,0,0,0 1,0,0,0,0,0 1,0,0,0,0,0 1,0,0,0,0,0 1,0,0,0,0 1,0,0,0,0 1,0,0,0,0 1,0,0,0,0 1,0,0,0,0 1,0,0,0,0 1,0,0,0,0 1,0,0,0,0 1,0,0 1,0 1	The UMT5 consists of two connected transistors or digital transistors in a UMT3 (SC-70) package. The mounting area can be reduced by 50% compared to the UMT3 and the internal circuitry is completed, making this package ideal for high density mounting at half the assembly cost.
UMT6 SC-88 type	2.0±0.2 1.3±0.1 0.65 0.65 0.7 0.7 0.7 0.7 0.7 0.7 0.7 0.7	The UMT6 consists of two independent transistors or two independent digital transistors in a UMT (SC-70) package. The mounting area and mounting cost can be reduced by 50% compared to the UMT3, and the two transistors are independent to allow free configuration of a high density circuit.

### ●Types and features of leaded packages

Туре	External dimensions (Units : mm)	Features
SPT (SC-72 type)	2±0,2 0.45±0.15 0.45±0.15 0.5 0.45±0.15 0.5 0.45±0.15	The SPT is a smaller version of the conventional TO-92 type. The body size (3×4×2 mm³) has been reduced to 1/4 that of the TO-92 (5×5×4 mm³). The SPT is available on tape for automatic insertion, and less space is occupied on the printed circuit board than the TO-92. Reliability is the same as the TO-92.
FTR	0.65±0.1 2.4±0.2 0.55±0.1 0.55±0.1 0.45±0.1 0.45±0.1	SIL type with a height of 3.4 mm and a lead pitch of 2.54 mm.
FTL	0.65Max 2.4±0.2 0.65Max 2.4±0.2 0.5±0.1 0.5±0.1	The FTL is a radial taping version of the highly popular FTR. This enables automatic high-density mounting with a radial insertion machine.
ATR (SC-71 type)	0.65Max	SC-71type with a height of 4.4 mm and a Pc=1W type.



Type	External dimensions (Units : mm)	Features
TO-220FN	\$3.2±0.2	The TO-220FN features the same performance as the TO-220FP with approximately 2 mm less height, allowing the design of slimmer devices. Furthermore, the elimination of support pins in the fin (collector electrode) solves short-circuiting problems with neighboring components and the chassis.  To make the height to the installation hole the same as the TO-220FP, it can be replaced as is from the TO-220FP.