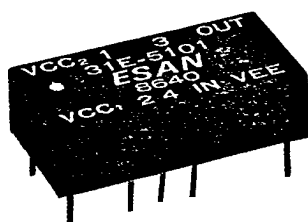


- ULTRA-Fast Rise Time
- 16 Pin Dip Package
- 5 Tap Delay



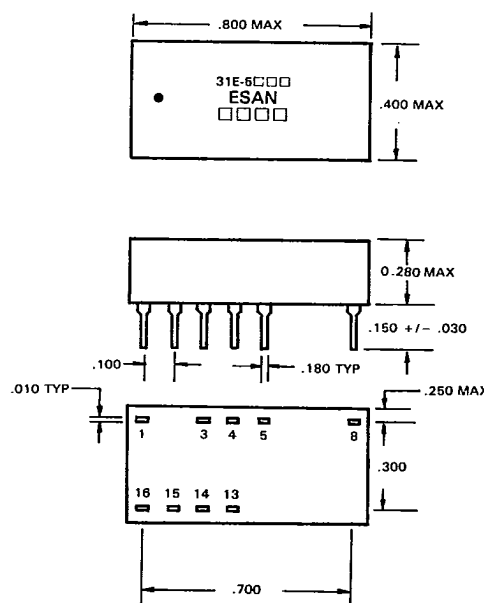
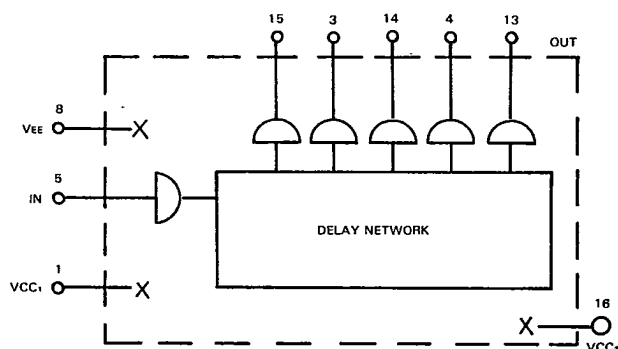
Specifications:

- Delay tolerance : $\pm 5\%$ or 1ns
- Rise time : 3ns max.
- Operating Temp. : -0°C to 70°C
- Supply Voltage : $-5.2\text{V} \pm 5\%$
- Logic 1 Output : -0.96V min
- Logic 0 Output : -1.65 max.
- Logic 1 Input : -0.98V min
- Logic 0 Input : -1.63V max
- Power Dissipation : 200mW typ

Test Conditions:

- Input pulse Amplitude : -1.0V (-0.75V to -1.75V)
- Pulse width : 3 * max Td
- Pulse Spacing : 10 * max Td
- Time delay measured : @ -1.3V level
- Output connected through an external pull down resistor of 100Ω to 2.0VDC
- Input pulse provide by standard open emitter ECL 10K gate.

MODEL NO.	TOTAL DELAY (ns)	TAP DELAY TAP (ns)
31E-5100	10	$2 \pm .5$
31E-5150	15	$3 \pm .5$
31E-5200	20	$4 \pm .5$
31E-5250	25	5 ± 1.0
31E-5300	30	6 ± 1.0
31E-5400	40	8 ± 1.0
31E-5500	50	10 ± 1.0
31E-5600	60	12 ± 1.0
31E-5800	80	16 ± 1.5
31E-5101	100	20 ± 2.0



Unit: Inch