

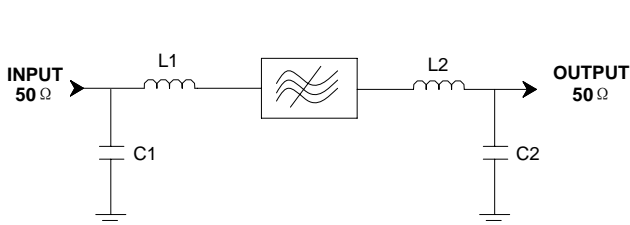
### Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	149.9	150	150.1
Insertion Loss	dB	-	23	25
3 dB Bandwidth	MHz	4.5	4.61	-
40 dB Bandwidth	MHz	-	6.58	6.9
Phase Linearity	deg	-	1.5	-
Passband Variation	dB	-	0.4	1
Ultimate Rejection	dB	50	52	-
Absolute Delay	usec	-	1.658	-
Substrate Material		Quartz		
Ambient Temperature	°C	25		
Package Size		DIP2212 (22.2x12.8x4.7mm <sup>3</sup> )		

#### Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance shown

### Matching Configuration



L1=100nH

L2=120nH

C1=33pF

C2=56pF

**Source/Load Impedance=50 ohm**

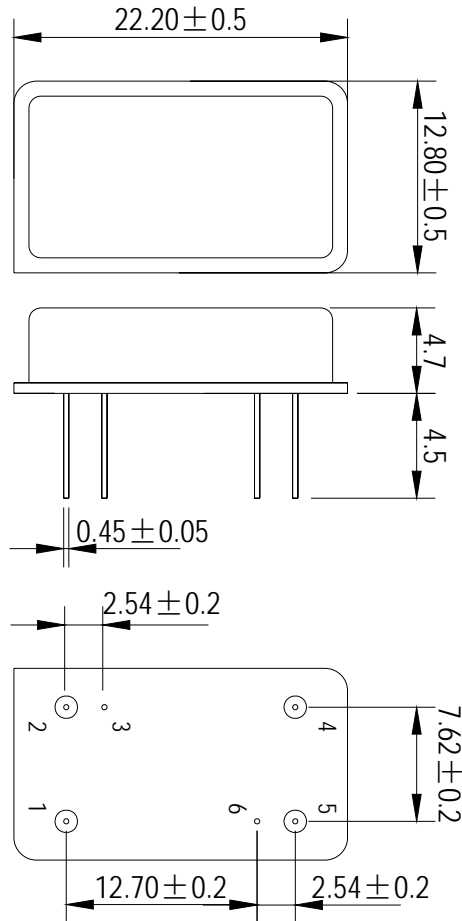
Notes - Component values may change depending on board layout.



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*Package Dimension*

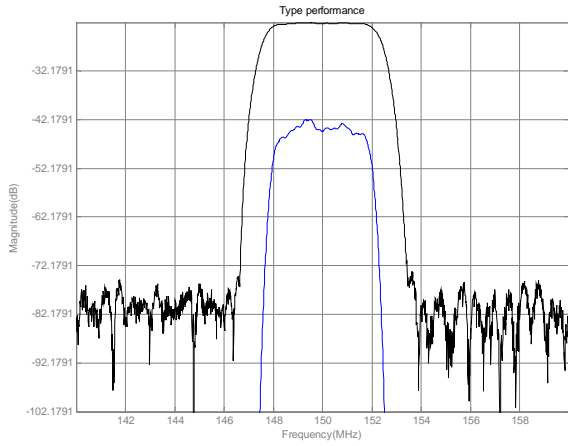


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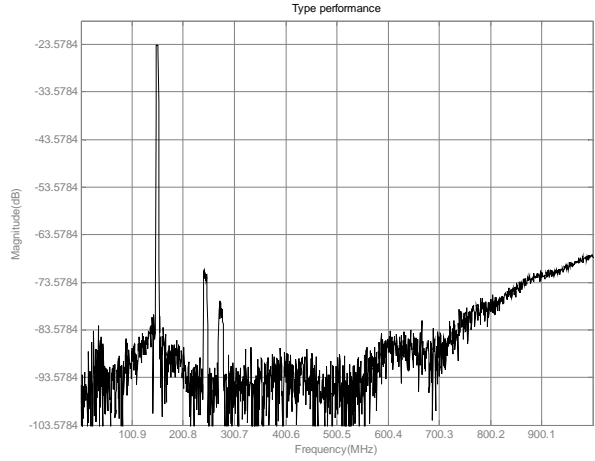
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*Typical Performance*

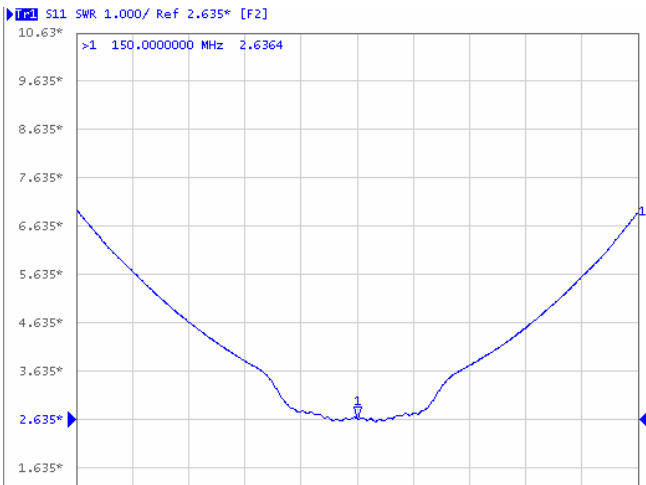
Frequency Respond



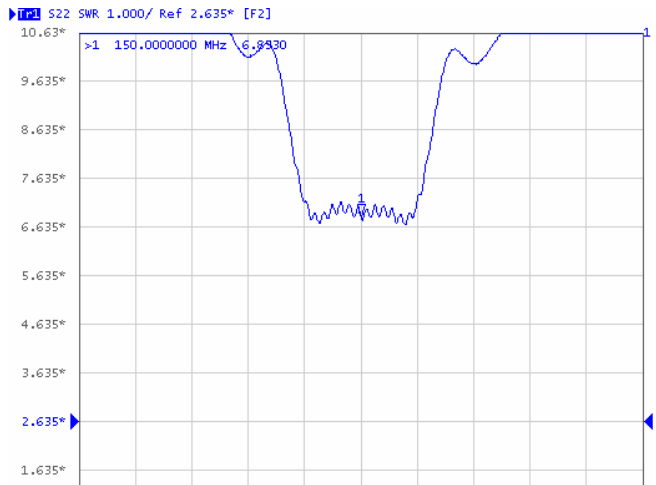
Wideband Respond



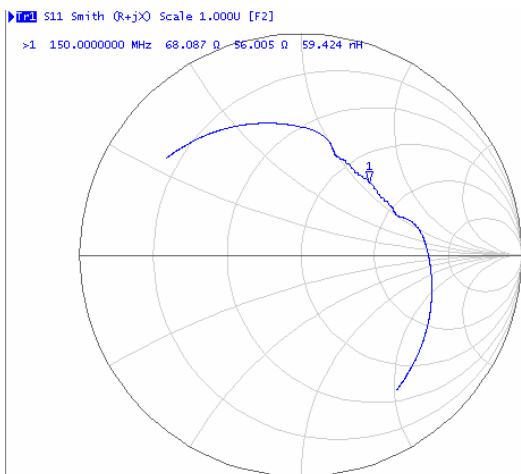
SWR(S11)



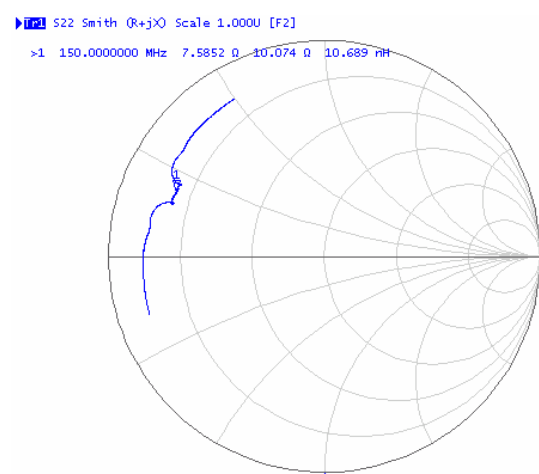
SWR(S22)



Simth Chart S11



Simth Chart S22



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*Marking*



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