

Description

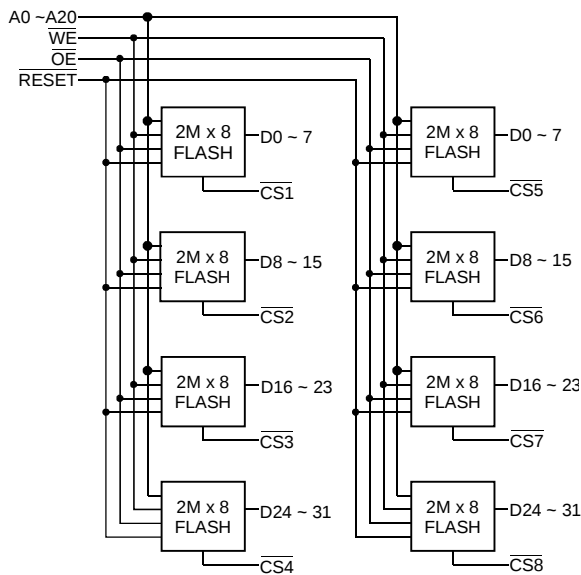
The PUMA F128006 is a 128Mbit FLASH (5V) Module organised as 4M x 32 in a JEDEC 84 pin surface mount J-leded PLCC, available with access times of 90, 120, and 150ns. The output width is user configurable as 8, 16 or 32 bits using eight Chip Selects (CS1~8).

The PUMA 84F128006 offers a dramatic space saving advantage over eight standard 2Mx8 FLASH devices. 5V Program / Erase Flash technology is utilised to simplify the in circuit programming procedure.

Features

- Access times of 90/120/150 ns.
- Very High Density Package
- JEDEC 84 'J' leded plastic Surface Mount Package.
- Single 5.0 V $\pm$ 10% Power supply.
- User Configurable as 8/16/32 bit wide output.
- Operating Power (32-BIT) 1.375 W (max)
Low Power Standby CMOS 66 mW (max)
- Flexible Sector (64K Byte) Architecture.
- Embedded Algorithms.
- Multiple ground pins for maximum noise immunity.

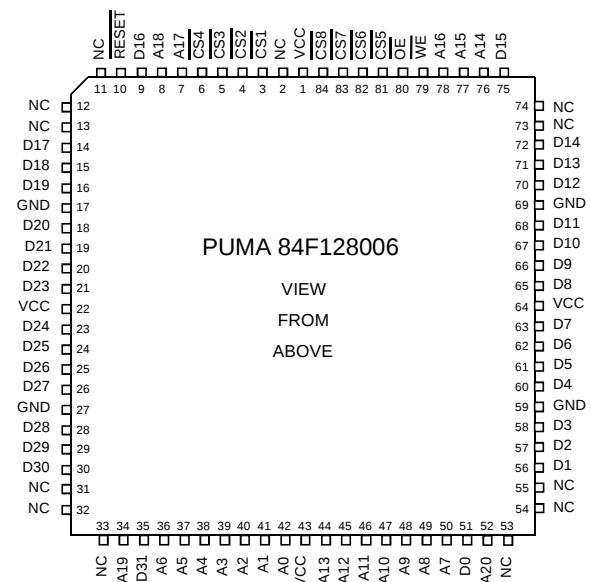
Block Diagram



Pin Functions

Address Inputs	A0 ~ A20
Data Input/Output	D0 ~ D31
Chip Select	CS1 ~ 8
Write Enable	WE
Output Enable	OE
Reset	RESET
No Connect	NC
Power (+5V)	V_{cc}
Ground	GND

Pin Definition

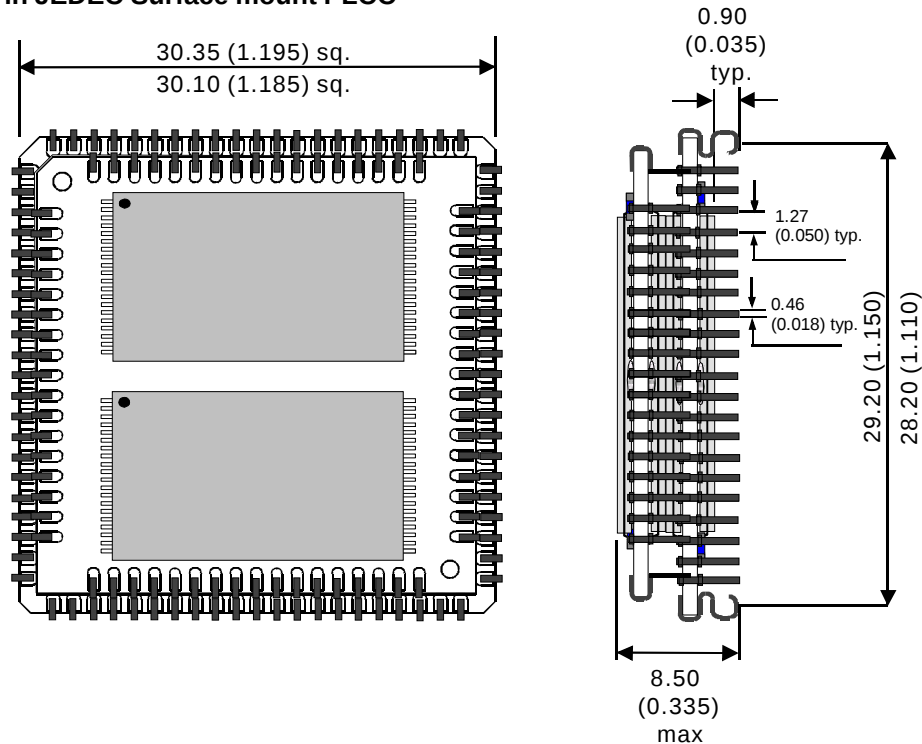


Package Details

Plastic 84 J-Leaded JEDEC PLCC

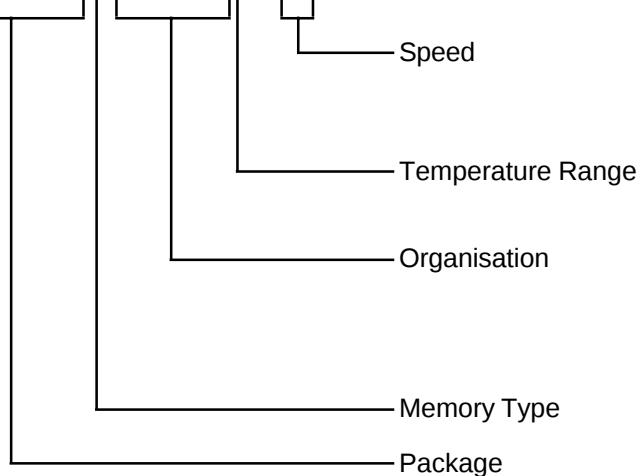
Package Information Dimensions in mm(inches)

Plastic 84 Pin JEDEC Surface mount PLCC



Ordering Information

PUMA 84F128006I - 90



90	=	90 ns
12	=	120 ns
15	=	150 ns
Blank	=	Commercial Temperature
I	=	Industrial Temperature
128006	=	4M x 32 FLASH configurable as 8M x 16 and 16M x 8, 5V PROG/ERASE
F	=	FLASH
PUMA 84	=	Memory Stack 84 pin 'J' Leaded

Note :

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