

# M5M82C55AFP-5,-2

6249828 MITSUBISHI (MICMPTR/MIPRC)

91D 12011 D

CMOS PROGRAMMABLE PERIPHERAL INTERFACE

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## DESCRIPTION

This is a family of general-purpose programmable input/output devices designed for use with the 8/16-bit parallel CPU as input/output ports.

This device is fabricated using silicon-gate CMOS technology for a single supply voltage. This LSI is a simple input and output interface for TTL circuits, having 24 input/output pins which correspond to three 8-bit input/output ports.

M5M82C55AFP-5,-2's specifications are compatible with M5M82C55AP-5,-2. Only package outline is different. M5M82C55AFP-5,-2 is mounted in the SOP (Small Outline Package).

Refer to M5M82C55AP-5,-2 for detail information.

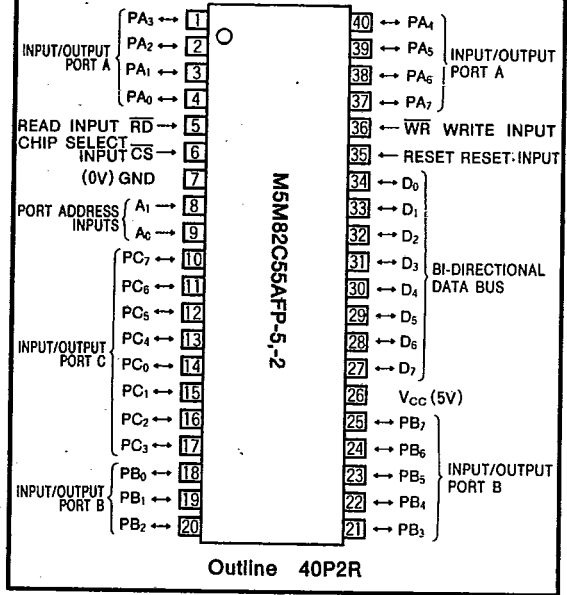
## FEATURES

- 120nsec access time (M5M82C55AFP-2)
- Having internal anti-noise circuit on RESET, ACK and STB pins
- Single 5 V supply voltage
- TTL compatible
- Improved DC driving capability
- Improved timing characteristics
- 24 programmable I/O pins
- Direct bit set/reset capability
- Packaged in flat small outline package

## APPLICATION

Input/output ports for MELPS85, 8086, 8088 microprocessor

## PIN CONFIGURATION (TOP VIEW)



## BLOCK DIAGRAM

