

series 268C300



FEATURES

- 2" X 2" Module outline
- 12-Bit position output
- Fast tracking to 50 RPS
- Excitation frequencies 50 Hz to 1200 Hz
- Velocity output
- Inter-LSB output
- Direction output
- Ripple Clock output
- 150 MW power dissipation @ ±12V

APPLICATIONS

Ordinance Control — Radar Tracking Systems — Navigation Systems — Collision Avoidance Systems — Process Control — Battery Operated Equipment

GENERAL DATA

The Series 268C300 is a low power synchro/resolver-to-digital converter packaged in a 2.0" \times 2.0" \times 0.395" module, weighing only 1.25 ounces. This new, small size rivals that of hybrid converters, yet still maintains the flexibility and fast deliveries of modular converters.

The converter is a continuously tracking type and can follow synchro or resolver shaft speeds of up to 50 RPS without additional error. Operation can be specified over a frequency range of 50 Hz to 1.2 KHz and an input signal range of 2.5 to 115 volts rms. The converter uses a ratiometric conversion technique which insures high noise immunity and tolerance

of lead length when located at a distance from the synchro or resolver source.

The output is a 12-bit parallel, natural binary angle, positive logic. An INHIBIT input and CONVERTER BUSY output insures easy data transfer. The DIRECTION and RIPPLE CLOCK outputs allow simple extention of the counters for multiturn applications.

CONNECTING THE CONVERTER

The power lines, which must not be reversed, should be connected to the +15V, -15V, and +5V pins with the common connection to the GND pin.

ELECTRICAL SPECIFICATIONS Parameter Value Resolutions 12 bits Accuracy (1) ±8.5 minutes Input Rates 60 Hz 400Hz Tracking Rate 20 RPS **50 RPS** Acceleration 2000 Sec-2 12,000 Sec-2 Constant Settling Time 500 ms 150 ms (179° step) Frequency Response 15 Hz 34 Hz Power Supplies (2) 6mA max. (5mA typ.) + 15V 8mA max. (7mA typ.) - 15V + 5V 3mA max. (2mA typ.) Reference Input 2.5 to 115 Vrms Voltage Frequency 50 Hz to 1.2 kHz Signal Input (3) 2.5 V rms 11.8 V rms Voltage 90V rms Impedance 26 Kohms 100 Kohms 800 Kohms Inhibit Input (INH) Logic '0' inhibits 0.5 LSTTL Load Parallel Angle (1-12) Natural binary angle positive logic Max. load 2 TTI Converter Busy (CB) Logic '1' when busy Maximum load 2 ŤTL 1-2 microseconds Pulse width **Direction Output** Logic '0' when counting up (DIR) Maximum load 1 LSTTL Ripple Clock (RC) Low-level output pulse when an overflow or underflow condition exists Maximum load **Velocity Voltage** (VEL) (4) Full Scale Voltage ± 10 Vdc Full Scale Accuracy +20% max. Polarity Positive for increasing angle Reversal Error ± 10% max. (included in Full Scale Accuracy) 2% for 0-20% of max. velocity Linearity 6% for 0-50% of max. velocity 15% for 0-100% of max. velocity Zero Offset ±8 mV max. 5 mVrms Ripple Inter LSB Voltage (E) (4) Polarity Negative for increasing angle Output Voltage ± 4.5 V/LSB DC Error Voltage (D) (4) Polarity Negative for increasing angle

± 15 mV/LSB

1.25 oz

0° to 70° C

-55° to 125° C

2.0" X 2.0" X 0.395"

Output Voltage

Temperature Ranges

Dimensions

Operating Storage

Weight

NOTES:

- 1. Accuracy applies for:
 - (a) $\pm 10\%$ specified signal amplitude variation
 - (b) over entire reference voltage range
 - (c) 25% signal and reference harmonic distortion
 - (d) over power supply range
 - (e) over operating temperature range
- Although specified at ± 15V all units can operate on voltages between ± 12V to ± 16.5V with no degradation in performance. The tolerance on the +5V supply is ±5%.
- Other signal voltages available on special order, consult factory.
- 4. These outputs should be connected via buffers to prevent the possibility of oscillation.

The digital output is taken from pins 1 thru 12 where pin 1 is the MSB, which equals 180°.

Connecting the converter to a synchro is as follows:

SYNCHRO	CONVERTER		
R1	то	RH	
R2	TO	RL	
S1	TO	S1	
S2	TO	S2	
S3	TO	S3	

There are two conventions when connecting resolvers, depending on which rotor windings are excited. The converter/resolver connections for the two conventions are as follows:

R2-I	R4 EXCI	TED	R1-R3 EXCITED			
RESOLVE	R CO	NVERTER	RESOLVE	R CO	NVERTER	í
R2	TO	RH	R1	TO	RH	
R4	TO	RL	R3	TO	RL	
S1	TO	S1	S1	TO	S4	
S2	TO	S2	S2	TO	S1	
S3	TO	S3	S3	TO	S2	
S4	TO	S4	S4	TO	S3	

OPERATION OF THE CONVERTER

The 268C300 series is a tracking converter, therefore the output follows the input for speeds up to the specified tracking rate. No convert command is necessary — a conversion is initiated by each LSB increment of the input. Each increment generates a 1-microsecond CB pulse which in turn causes the digital output to increment one LSB.

The direction of rotation of the synchro or resolver is indicated by the DIR output. This direction data is always valid in advance of a CB pulse.

The RC output indicates when the synchro or resolver crosses electrical zero. Its relationship to CB and DIR can be seen in the timing diagram.

The CB output or INH input can be used to interface to a computer. The converter will ignore an inhibit command applied during an increment command. There are two methods of inter-

facing with a computer: (1) Synchronous and (2) asynchronous. A simple method of synchronous loading is to: (a) set INH to logic '0', (b) wait 2 microseconds, (c) transfer the digital angle, and (d) reset INH to logic '1'. Asynchronous loading is accomplished by transferring data on the negative going edge of the CB pulse.

VELOCITY VOLTAGE

The Velocity output (VEL) is a dc voltage proportional to the angular velocity of the synchro or resolver shaft. Voltage polarity is positive for an increasing angle. This output can be used in many applications to provide loop stabilization and velocity feed back data. Refer to the electrical specifications for performance characteristics.

INTER LSB VOLTAGE

The Inter LSB (E) voltage is a dc analog voltage representing the synchro or resolver shaft position within the least significant bit of the digital angle output.

DC ERROR VOLTAGE

The DC Error Voltage (D) is the signal at the output of the phase sensitive demodulator and is proportional to the error between the analog input angle and digital output angle. This is an unfiltered output and will increase if the output angle fails to track in the input for any reason.

DYNAMIC PERFORMANCE

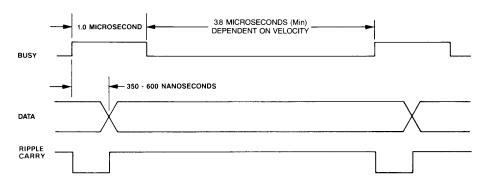
The 268C300 series employs a type II servo loop ($K_v = \infty$) with very high acceleration constants. The loop dynamics are completely independent of power supply variations within their specific ranges. As long as the maximum tracking rate is not exceeded there will be no velocity lag and only minor acceleration lag in the converter output. Acceleration lag (in degrees) can be computed from the following equation:

$$E_a = \frac{\text{Acceleration (°/sec}^2)}{K_a}$$

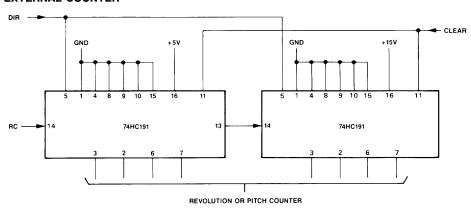
The nominal open loop transfer functions are given by:

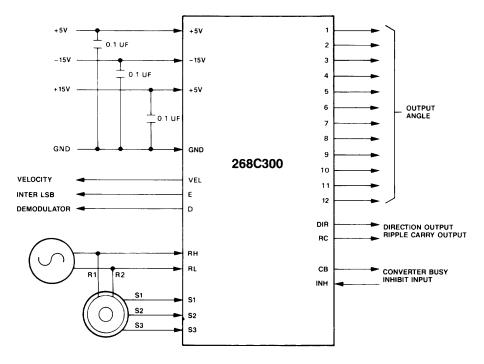
$$G_{60HZ} = \frac{45^2 \left(\frac{S}{35} + 1\right)}{S^2 \left(\frac{S}{139} + 1\right)} \qquad \qquad G_{400HZ} = \frac{100^2 \left(\frac{S}{75} + 1\right)}{S^2 \left(\frac{S}{296} + 1\right)}$$

TIMING DIAGRAM



HCMOS EXTERNAL COUNTER





ORDERING INFORMATION

268C SUFFIX	INPUT TYPE	L-L VOLTAGE	REF FREQ
300	SYNC	11.8V	50-1200Hz
301	SYNC	90V	50-1200Hz
302	RSVR	11.8V	50-1200Hz
303	RSVR	90V	50-1200Hz
304	SYNC	11.8V	360-1200Hz
305	SYNC	90V	360-1200Hz
306	RSVR	11.8V	360-1200Hz
307	RSVR	90V	360-1200Hz

NOTES

- S4 pin appears on multiple and input and resolver models only
- 2. Rigid .025 diameter pins suitable for solder-in or plug-in applications.
- 3. Non-cumulative.
- 4. Dimensions are in inches.

MECHANICAL OUTLINE

