

ELMO

T-46-23-14

SRAM SELECTION GUIDE**SRAM MODULES**

PART NUMBER	ORG.	TECH.	SPEED (ns)	TEMP. RANGE	MAX. DC CURRENT			PACKAGE		
					ICC (mA)	ISB1 (mA)	ISB2 (mA)	STYLE	PINS	DIMENSIONS LxWxH (In.)
1 MB DENSITY										
EMC01MS08M02-055D	128Kx8	CMOS	55	MIL	150	10	30	DIP	32	1.600x.600x.210
EMC01MS08M02-070D	128Kx8	CMOS	70	MIL	150	10	30	DIP	32	1.600x.600x.210
EMC01MS08M02-100D	128Kx8	CMOS	100	MIL	150	10	30	DIP	32	1.600x.600x.210
EMC01MS08M03-055C	128Kx8	CMOS	55	COM	150	10	30	PDIP	32	1.600x.600x.240
EMC01MS08M03-070C	128Kx8	CMOS	70	COM	150	10	30	PDIP	32	1.600x.600x.240
EMC01MS08M03-100C	128Kx8	CMOS	100	COM	150	10	30	PDIP	32	1.600x.600x.240
2 MB DENSITY										
EMC02MS08M02-045D	256Kx8	CMOS	45	MIL	130	4	30	DIP	32	1.600x.600x.210
EMC02MS08M02-055D	256Kx8	CMOS	55	MIL	130	4	30	DIP	32	1.600x.600x.210
EMC02MS08M02-070D	256Kx8	CMOS	70	MIL	130	4	30	DIP	32	1.600x.600x.210
EMC02MS08M02-100D	256Kx8	CMOS	100	MIL	130	4	30	DIP	32	1.600x.600x.210
EMC02MS08M03-045C	256Kx8	CMOS	45	COM	130	4	30	PDIP	32	1.600x.600x.240
EMC02MS08M03-055C	256Kx8	CMOS	55	COM	130	4	30	PDIP	32	1.600x.600x.240
EMC02MS08M03-070C	256Kx8	CMOS	70	COM	130	4	30	PDIP	32	1.600x.600x.240
EMC02MS08M03-100C	256Kx8	CMOS	100	COM	130	4	30	PDIP	32	1.600x.600x.240
EMM02MS08M01-055D	256Kx8	CMOS	55	MIL	150	20	30	DIP	32	1.600x.600x.290
EMM02MS08M01-070D	256Kx8	CMOS	70	MIL	150	20	30	DIP	32	1.600x.600x.290
EMM02MS08M01-100D	256Kx8	CMOS	100	MIL	150	20	30	DIP	32	1.600x.600x.290
EMM02MS08M03-055C	256Kx8	CMOS	55	COM	150	20	30	PDIP	32	1.600x.600x.240
EMM02MS08M03-070C	256Kx8	CMOS	70	COM	150	20	30	PDIP	32	1.600x.600x.240
EMM02MS08M03-100C	256Kx8	CMOS	100	COM	150	20	30	PDIP	32	1.600x.600x.240
4 MB DENSITY										
EMC04MS08M01-045D	512Kx8	CMOS	45	MIL	140	5	40	DIP	32	1.600x.600x.290
EMC04MS08M01-055D	512Kx8	CMOS	55	MIL	140	5	40	DIP	32	1.600x.600x.290
EMC04MS08M01-070D	512Kx8	CMOS	70	MIL	140	5	40	DIP	32	1.600x.600x.290
EMC04MS08M01-100D	512Kx8	CMOS	100	MIL	140	5	40	DIP	32	1.600x.600x.290
EMC04MS08M03-045C	512Kx8	CMOS	45	COM	140	5	40	PDIP	32	1.600x.600x.240
EMC04MS08M03-055C	512Kx8	CMOS	55	COM	140	5	40	PDIP	32	1.600x.600x.240
EMC04MS08M03-070C	512Kx8	CMOS	70	COM	140	5	40	PDIP	32	1.600x.600x.240
EMC04MS08M03-100C	512Kx8	CMOS	100	COM	140	5	40	PDIP	32	1.600x.600x.240

9000-2626

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SRAM SELECTION GUIDE**MONOLITHIC SRAMs**

PART NUMBER	ORG.	TECH.	SPEED (ns)	TEMP. RANGE	MAX. DC CURRENT			PACKAGE		
					ICC (mA)	ISB1 (mA)	ISB2 (mA)	STYLE	PINS	DIMENSIONS LxWxH (In.)
256K DENSITY										
EIC256S08C01-035D	32Kx8	CMOS	35	MIL	60	0.10	6.0	LCC	32	.550x.450x.090
EIC256S08C01-045D	32Kx8	CMOS	45	MIL	60	0.10	6.0	LCC	32	.550x.450x.090
EIC256S08C01-055D	32Kx8	CMOS	55	MIL	60	0.10	6.0	LCC	32	.550x.450x.090
EIC256S08C01-085D	32Kx8	CMOS	85	MIL	45	0.10	3.0	LCC	32	.550x.450x.090
EIC256S08C01-100D	32Kx8	CMOS	100	MIL	45	0.10	3.0	LCC	32	.550x.450x.090
EIC256S08D01-035D	32Kx8	CMOS	35	MIL	60	0.10	6.0	DIP	28	1.400x.600x.150
EIC256S08D01-045D	32Kx8	CMOS	45	MIL	60	0.10	6.0	DIP	28	1.400x.600x.150
EIC256S08D01-055D	32Kx8	CMOS	55	MIL	60	0.10	6.0	DIP	28	1.400x.600x.150
EIC256S08D01-085D	32Kx8	CMOS	85	MIL	45	0.10	3.0	DIP	28	1.400x.600x.150
EIC256S08D01-100D	32Kx8	CMOS	100	MIL	45	0.10	3.0	DIP	28	1.400x.600x.150
1 MB DENSITY										
EIC01MS08C02-035D	128Kx8	CMOS	35	MIL	120	2.00	15.0	LCC	32	.700x.450x.080
EIC01MS08C02-045D	128Kx8	CMOS	45	MIL	120	2.00	15.0	LCC	32	.700x.450x.080
EIC01MS08C02-055D	128Kx8	CMOS	55	MIL	120	2.00	15.0	LCC	32	.700x.450x.080
EIC01MS08D02-035D	128Kx8	CMOS	35	MIL	120	2.00	15.0	DIP	32	1.600x.600x.190
EIC01MS08D02-045D	128Kx8	CMOS	45	MIL	120	2.00	15.0	DIP	32	1.600x.600x.190
EIC01MS08D02-055D	128Kx8	CMOS	55	MIL	120	2.00	15.0	DIP	32	1.600x.600x.190

NOTES:**Temp. Range**

COM. = 0 to +70°C

MIL. = -55 to +125°C

Package Styles

LCC = Ceramic Leadless Chip Carrier

DIP = Sidebraced Dual-In-Line

PDIP = Plastic Dual-In-Line

DC Current Conditions

ICC = Operating, Min Cycle, No Load

ISB1 = Standby, CMOS Input Levels

ISB2 = Standby, TTL Input Levels

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