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LTR	DESCRIPTION	DATE (YR-MO-DA)	APPROVED																								
A	Paragraph 1.3 absolute maximum ratings, correct input voltage range. Editorial changes throughout.	1990 AUG 14	<i>W. Beckman</i>																								

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	SHEET	1	2	3	4	5	6	7	8	9	10	11	12	13															

PMIC N/A STANDARDIZED MILITARY DRAWING THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE AMSC N/A	PREPARED BY <i>Charles A. Rame</i> CHECKED BY <i>Jim Noh</i> APPROVED BY <i>William K. Beckman</i> DRAWING APPROVAL DATE 8 MARCH 1988 REVISION LEVEL A	DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444 MICROCIRCUITS, DIGITAL BIPOLAR HIGH PERFORMANCE 10-BIT BUFFERS, MONOLITHIC SILICON <table style="width: 100%;"> <tr> <td style="width: 15%;">SIZE A</td> <td style="width: 25%;">CAGE CODE 67268</td> <td style="width: 60%;">5962-88605</td> </tr> <tr> <td colspan="2">SHEET</td> <td style="text-align: right;">1</td> </tr> </table>	SIZE A	CAGE CODE 67268	5962-88605	SHEET		1
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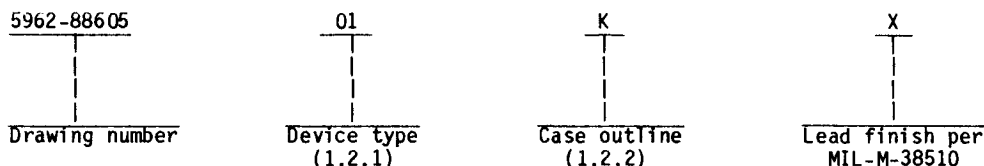
5962-E1740

DISTRIBUTION STATEMENT A. Approved for public release; distribution is unlimited.

1. SCOPE

1.1 Scope. This drawing describes device requirements for class B microcircuits in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices".

1.2 Part number. The complete part number shall be as shown in the following example:



1.2.1 Device types. The device types shall identify the circuit function as follows:

Device type	Generic number	Circuit function
01	29827A	High performance 10-bit buffers, three-state
02	29828A	High performance 10-bit buffers, three-state

1.2.2 Case outlines. The case outlines shall be as designated in appendix C of MIL-M-38510, and as follows:

Outline letter	Case outline
K	F-6 (24-lead, .640" x .420" x .090"), flat package
L	D-9 (24-lead, 1.280" x .310" x .200"), dual-in-line package
3	C-4 (28-terminal, .460" x .460" x .100"), square chip carrier package

1.3 Absolute maximum ratings.

Supply voltage range - - - - -	-0.5 V dc to +7.0 V dc
Input voltage range - - - - -	-1.5 V dc to +6.0 V dc
Storage temperature range - - - - -	-65°C to +150°C
Maximum power dissipation (P_D) - - - - -	1.82 W ^{1/}
Lead temperature (soldering, 10 seconds) - - - - -	+300°C
Thermal resistance, junction-to-case (θ_{JC}):	
Cases K, L, and 3 - - - - -	See MIL-M-38510, appendix C
Junction temperature (T_J) - - - - -	+150°C
DC output voltage - - - - -	-0.5 V dc to +5.5 V dc
DC output current into output - - - - -	+100 mA
DC output current out of output - - - - -	-50 mA
DC input current into input - - - - -	-30 mA to +5.0 mA

1.4 Recommended operating conditions.

Supply voltage (V_{CC}) - - - - -	+4.5 V dc to +5.5 V dc
Minimum high level input voltage (V_{IH}) - - - - -	2.0 V dc
Maximum low level input voltage (V_{IL}) - - - - -	0.7 V dc
Case operating temperature range (T_C) - - - - -	-55°C to +125°C

^{1/} Must withstand the added P_D due to short circuit test; e.g., I_{OS} .

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2. APPLICABLE DOCUMENTS

2.1 Government specification, standard, and bulletin. Unless otherwise specified, the following specification, standard, and bulletin of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATION

MILITARY

MIL-M-38510 - Microcircuits, General Specification for.

STANDARD

MILITARY

MIL-STD-883 - Test Methods and Procedures for Microelectronics.

BULLETIN

MILITARY

MIL-BUL-103 - List of Standardization Military Drawings (SMD's).

(Copies of the specification, standard, and bulletin standard required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices" and as specified herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.

3.2.1 Terminal connections. The terminal connections shall be as specified on figure 1.

3.2.2 Truth tables. The truth tables shall be as specified on figure 2.

3.2.3 Logic diagrams. The logic diagrams shall be as specified on figure 3.

3.2.4 Case outlines. The case outlines shall be in accordance with 1.2.2 herein.

3.3 Electrical performance characteristics. Unless otherwise specified, the electrical performance characteristics are as specified in table I and apply over the full case operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table I.

3.5 Marking. Marking shall be in accordance with MIL-STD-883 (see 3.1 herein). The part shall be marked with the part number listed in 1.2 herein. In addition, the manufacturer's part number may also be marked as listed in MIL-BUL-103 (see 6.6 herein).

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TABLE 1. Electrical performance characteristics.

Test	Symbol	Conditions -55°C < T _C < +125°C 4.5 V < V _{CC} < 5.5 V unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Output high voltage	V _{OH}	V _{CC} = 4.5 V V _{IN} = V _{IL} or V _{IH}	1, 2, 3	A11	2.4		V
					2.0		
Output low voltage	V _{OL}	V _{CC} = 4.5 V V _{IN} = V _{IH} or V _{IL}	1, 2, 3	A11		0.5	V
Input clamp voltage	V _{IC}	V _{CC} = 4.5 V I _{IN} = -18 mA	1, 2, 3	A11		-1.2	V
Input hysteresis	V _{HYST}		1, 2, 3	A11	200		mV
Input low current	I _{IL}	V _{CC} = 5.5 V V _{IN} = 0.4 V	1, 2, 3	A11		-0.5	mA
Input high current	I _{IH}	V _{CC} = 5.5 V V _{IN} = 2.7 V	1, 2, 3	A11		50	μA
Input high current	I _I	V _{CC} = 5.5 V V _{IN} = 5.5 V	1, 2, 3	A11		100	μA
Output off-state high impedance output current	I _{OZH}	V _{CC} = 5.5 V	1, 2, 3	A11		50	μA
	I _{OZL}					-50	
Output short circuit current	I _{OS}	V _{CC} = 5.5 V V _{OUT} = 0 V 1/	1, 2, 3	A11	-75	-250	mA
Bus leakage current	I _{OFF}	V _{CC} = 0 V V _{OUT} = 2.9 V	1, 2, 3	A11		100	μA

See footnote at end of table.

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TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions -55°C < T _C < +125°C 4.5 V < V _{CC} < 5.5 V unless otherwise specified		Group A subgroups	Device type	Limits		Unit		
						Min	Max			
Supply current	I _{CC}	V _{CC} = 5.5 V Outputs unloaded	Outputs logic low	1, 2, 3	A11		80	mA		
			Outputs logic high				55			
			Outputs logic Hi-Z				70			
Propagation delay from data D _i to output Y _i	t _{PLH}	C _L = 50 pF See figure 4	R ₁ = 500Ω R ₂ = 500Ω	9,10,11	01		9.0	ns		
					02		8.0			
Propagation delay from data D _i to output Y _i	t _{PHL}			9,10,11	01		9.0	ns		
					02		10.0			
Output enable time OE to Y _i	t _{PZH}			9,10,11	A11		12.0	ns		
Output enable time OE to Y _i	t _{PZL}			9,10,11	A11		13.0	ns		
Output disable time OE to Y _i	t _{PHZ}			9,10,11	A11		10.0	ns		
Output disable time OE to Y _i	t _{PLZ}			9,10,11	A11		10.0	ns		

1/ Not more than one output should be shorted at a time. Duration should not exceed 1 second.

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Device types 01 and 02

Cases K and L

Case 3

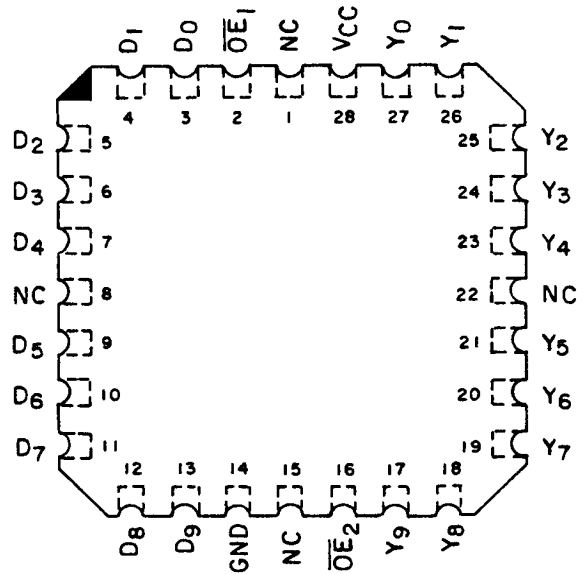
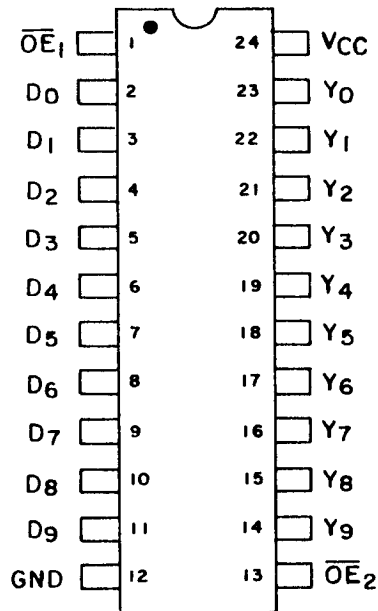


FIGURE 1. Terminal connections.

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Device type 01

Inputs			Outputs	Function
\overline{OE}_1	\overline{OE}_2	D_1	Y_1	
L	L	H	H	Transparent
L	L	L	L	Transparent
X	H	X	Z	Hi-Z
H	X	X	Z	Hi-Z

Device type 02

Inputs			Outputs	Function
\overline{OE}_1	\overline{OE}_2	D_1	Y_1	
L	L	H	L	Transparent
L	L	L	H	Transparent
X	H	X	Z	Hi-Z
H	X	X	Z	Hi-Z

H = High
 L = Low
 X = Don't care
 Z = High impedance

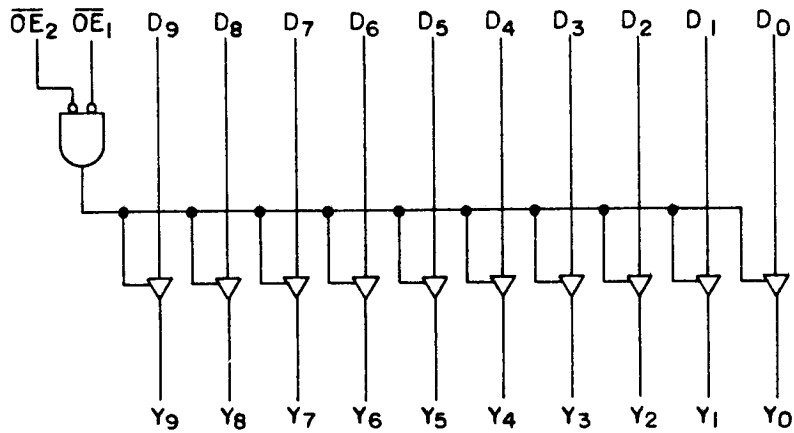
FIGURE 2. Truth tables.

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Device type 01



Device type 02

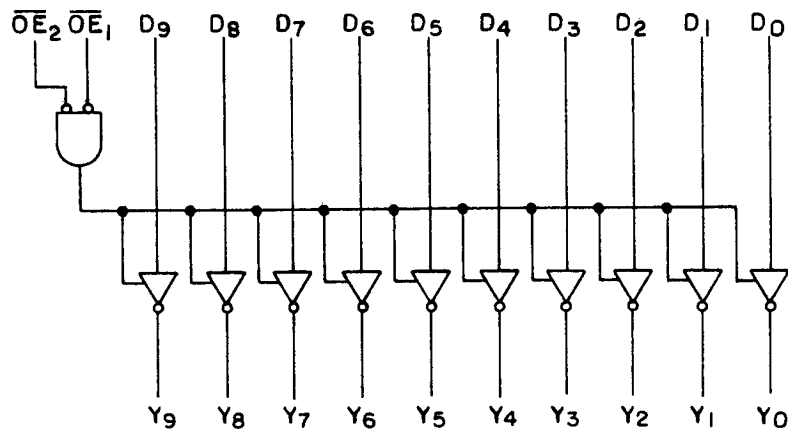


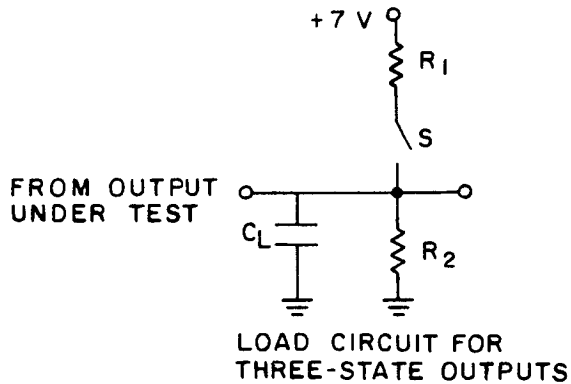
FIGURE 3. Logic diagrams.

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SWITCHING TEST CIRCUITS



Parameter	S Position
t_{PLH}	Open
t_{PHL}	Open
t_{PHZ}	Open
t_{PZH}	Open
t_{PLZ}	Closed
t_{PZL}	Closed

Switch positions for parameter testing

ENABLE AND DISABLE TIMES

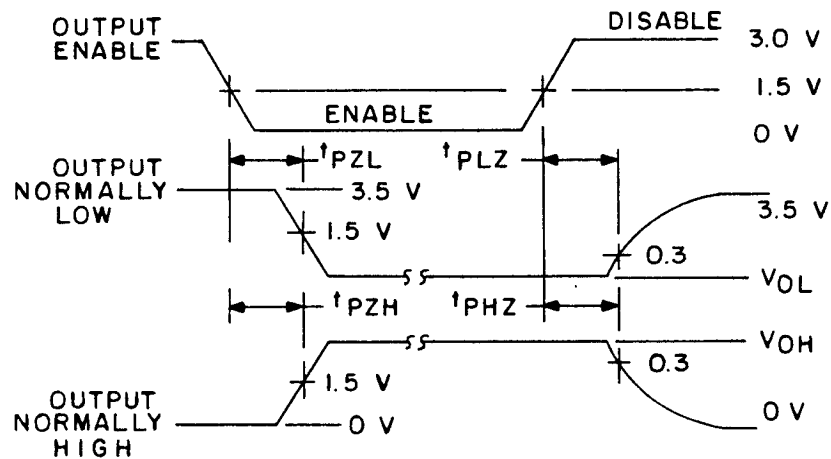


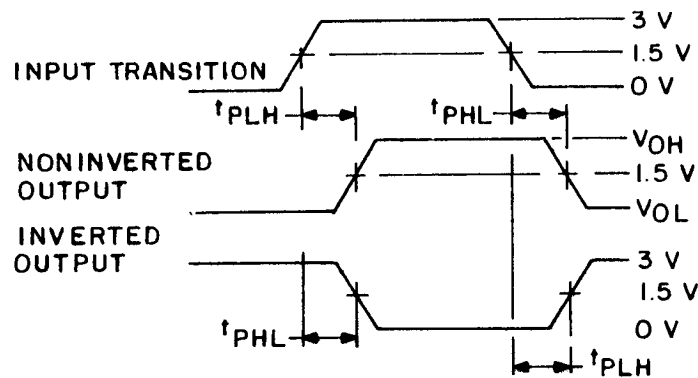
FIGURE 4. Switching waveforms and test circuit.

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PROPAGATION DELAY



NOTE: Pulse generator for all pulses, rate ≤ 1.0 MHz; $Z_0 = 50\Omega$; $t_r \leq 2.5$ ns; $t_f \leq 2.5$ ns.

FIGURE 4. Switching waveforms and test circuit - Continued.

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3.6 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in MIL-BUL-103 (see 6.6 herein). The certificate of compliance submitted to DESC-ECC prior to listing as an approved source of supply shall affirm that the manufacturer's product meets the requirements of MIL-STD-883 (see 3.1 herein) and the requirements herein.

3.7 Certificate of conformance. A certificate of conformance as required in MIL-STD-883 (see 3.1 herein) shall be provided with each lot of microcircuits delivered to this drawing.

3.8 Notification of change. Notification of change to DESC-ECC shall be required in accordance with MIL-STD-883 (see 3.1 herein).

3.9 Verification and review. DESC, DESC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with section 4 of MIL-M-38510 to the extent specified in MIL-STD-883 (see 3.1 herein).

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

a. Burn-in test, method 1015 of MIL-STD-883.

(1) Test condition A using the circuit submitted with the certificate of compliance (see 3.6 herein).

(2) $T_A = +125^{\circ}\text{C}$, minimum.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

4.3.1 Group A inspection.

a. Tests shall be as specified in table II herein.

b. Subgroups 4, 5, and 6 in table I, method 5005 of MIL-STD-883 shall be omitted.

c. Subgroups 7 and 8 testing shall consist of verification of the truth tables.

4.3.2 Groups C and D inspections.

a. End-point electrical parameters shall be as specified in table II herein.

b. Steady-state life test, method 1005 of MIL-STD-883.

(1) Test condition A using the circuit submitted with the certificate of compliance (see 3.6 herein).

(2) $T_A = +125^{\circ}\text{C}$, minimum.

(3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

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TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (per method 5005, table I)
Interim electrical parameters (method 5004)	---
Final electrical test parameters (method 5004)	1*,2,3,7*,8, 9,10,11
Group A test requirements (method 5005)	1,2,3,7,8, 9,10,11
Groups C and D end-point electrical parameters (method 5005)	1,2,3

* PDA applies to subgroups 1 and 7.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use when military specifications do not exist and qualified military devices that will perform the required function are not available for OEM application. When a military specification exists and the product covered by this drawing has been qualified for listing on QPL-38510, the device specified herein will be inactivated and will not be used for new design. The QPL-38510 product shall be the preferred item for all applications.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished in accordance with MIL-STD-481 using DD Form 1693, Engineering Change Proposal (Short Form).

6.4 Record of users. Military and industrial users shall inform Defense Electronics Supply Center when a system application requires configuration control and the applicable SMD. DESC will maintain record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronics devices (FSC 5962) should contact DESC-ECC, telephone (513) 296-6022.

6.5 Comments. Comments on this drawing should be directed to DESC-ECC, Dayton, Ohio 45444, or telephone (513) 296-8526.

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6.6 Approved source of supply. An approved source of supply is listed in MIL-BUL-103. Additional sources will be added as they become available. The vendors listed in MIL-BUL-103 have agreed to this drawing and a certificate of compliance (see 3.6 herein) has been submitted to and accepted by DESC-ECC. The approved source of supply listed below is for information purposes only and is current only to the date of the last action of this document.

Military drawing part number	Vendor CAGE number	Vendor similar part number <u>1/</u>
5962-8860501KX	34335	AM29827A/BKA
5962-8860501LX	34335	AM29827A/BLA
5962-88605013X	34335	AM29827A/B3A
5962-8860502KX	34335	AM29828A/BKA
5962-8860502LX	34335	AM29828A/BLA
5962-88605023X	34335	AM29828A/B3A

1/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE
number

34335

Vendor name
and address

Advanced Micro Devices, Incorporated
901 Thompson Place
P.O. Box 3453
Sunnyvale, CA 94088

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