

## Dual Channel FM Sound Demodulator for TV Systems

### Description

The U2860B is a dual channel FM sound demodulator realized with TELEFUNKEN's advanced bipolar process. All TV FM standards, from 4.5 up to 6.5 MHz (standard M, B/G, I, D/K), can be processed with high

performance. The circuit is alignment-free and has a minimum number of external components. With 5 V supply voltage the U2860B is suitable for TV, VCR and Multimedia applications.

### Features

- Two alignment-free PLL FM demodulators, automatic lock in on the received sound carrier frequency
- Mono and dual channel application
- Sound IF inputs provided for ceramic filters
- Automatic mute for 2nd sound channel (squelch)
- Mute function for both sound channels
- 5 V supply voltage, low power consumption
- Few external components

**Package:** 14 pin dual-inline plastic (DIP14)

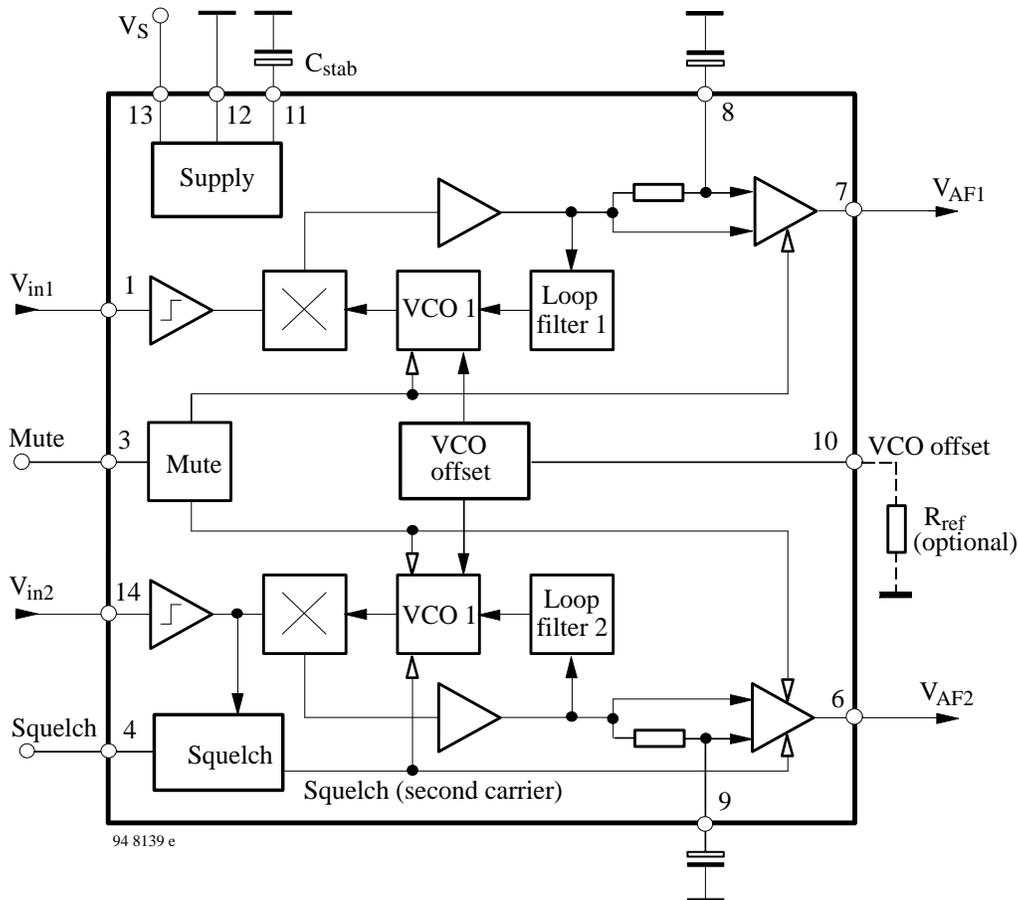


Figure 1. Block diagram

## U2860B-A

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### Circuit Description

The U2860B includes two identical sound IF channels. Each consists of a limiter amplifier, PLL FM demodulator and AF amplifier. Additionally this circuit contains a squelch function, mute switch and internal voltage regulation.

### Limiter Amplifiers

The intercarrier signals are fed through external ceramic bandpass filters to a 7 stage limiter amplifier. This guarantees high input sensitivity and excellent AM suppression.

### PLL FM demodulators

The alignment-free "Phase Locked Loop" (PLL) demodulator cover a wide frequency range of 4.5 MHz up to 6.5 MHz with low noise performance. The linear voltage to frequency characteristic results in low harmonic distortion. The free running frequency of the internal VCO circuit is about 5.5 MHz. For this frequency the input sensitivity and VCO locking is optimal. An additional external resistor at pin 10 allows frequency shift of  $\pm 1$  MHz via an internal offset current. With this option it is possible to shift the optimum conditions to the upper frequency (6.5 MHz) or to the lower frequency (4.5 MHz). The offset current acts simultaneously on both VCO circuits. If no resistor is connected the offset current is disabled.

### Audio Amplifiers

The demodulated signals are amplified to 500 mVrms with low output impedance at the audio outputs (Pin 6 and Pin 7). AC decoupling at Pin 8 and Pin 9 of the audio amplifiers leads to high common mode rejection.

### Squelch Function

For channel 2 the audio output amplifier and VCO2 is muted automatically (squelch) when the second sound carrier is not present. This avoids a wrong identification for stereo and dual sound in the stereo decoder. Therefore, with mono sound, there is no output signal at Pin 6. The automatic squelch function can be disabled by switching Pin 4 to ground.

### Mute Switch

Simultaneously muting of both circuits is possible with switching Pin 3 to ground.

### Internal Voltage Stabilizer

The internal bandgap reference ensures constant performance independent of supply voltage and temperature.

## Pin Description

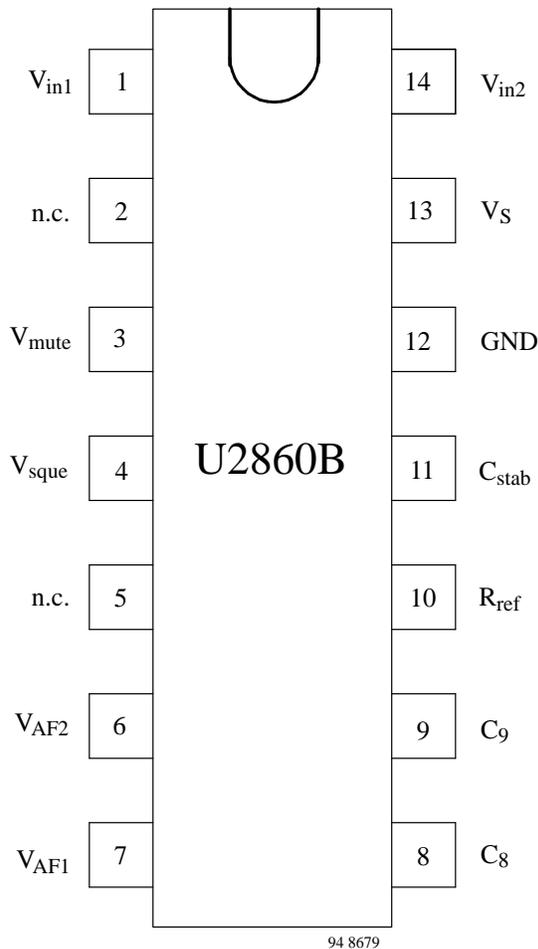


Figure 2. Connection diagram

Pin	Symbol	Function
1	$V_{in1}$	Inter-carrier input of sound channel 1 (5.5 MHz)
2	n.c.	Not connected
3	$V_{mute}$	Mute for sound channel 1+2 "on/off"
4	$V_{sque}$	Automatic mute for 2nd sound channel (squelch) "on/off"
5	n.c.	Not connected
6	$V_{AF2}$	Audio output AF2 of sound channel 2
7	$V_{AF1}$	Audio output AF1 of sound channel 1
8	$C_8$	Decoupling capacitor for sound channel 1
9	$C_9$	Decoupling capacitor for sound channel 2
10	$R_{ref}$	VCO offset of the free running frequency
11	$C_{stab}$	Internal supply voltage stabilization
12	GND	Ground
13	$V_S$	Supply voltage
14	$V_{in2}$	Inter-carrier input of sound channel 2 (5.74 MHz)

## Absolute Maximum Ratings

Reference point Pin 12, unless otherwise specified.

Parameters	Symbol	Value	Unit
Supply voltage	Pin 13	$V_S$	9.0 V
Supply current	Pin 13	$I_S$	33 mA
Power dissipation	$V_S = +9 V$	$P$	300 mW
Output currents	Pin 6, 7	$I_{out}$	$\pm 1.5$ mA
External voltages	Pin 1, 14	$V_{ext}$	2.0 V
	Pin 3, 4	$V_{ext}$	$V_S$ V
	Pin 6, 7, 8, 9, 10, 11	$V_{ext}$	4.5 V V
Junction temperature	$T_j$	+125	$^{\circ}C$
Storage temperature	$T_{stg}$	-25 to +125	$^{\circ}C$
Electrostatic handling *) all pins	$V_{ESD}$	$\pm 200$	V

\*) Machine model in accordance with ESD S5.2 standard

## Operating Range

Parameters	Symbol	Value	Unit
Supply voltage range Pin 13	$V_S$	4.5 to 9.0	V
Ambient temperature	$T_{amb}$	0 to +85	°C

## Thermal Resistance

Parameters	Symbol	Value	Unit
Junction-ambient when soldered to PCB	$R_{thJA}$	90	K/W

## Electrical Characteristics

$V_S = 5\text{ V}$ ,  $T_{amb} = 25^\circ\text{C}$ , reference point pin 12, unless otherwise specified

Parameters	Test Conditions / Pins	Symbol	Min.	Typ.	Max.	Unit
<b>DC-supply Pin 13</b>						
Supply voltage range		$V_S$	4.5	5.0	9.0	V
Supply current		$I_S$		27	33	mA
<b>Intercarrier input 1 Pin 1</b>						
DC input voltage		$V_{DC}$		1.75		V
Input resistance	see note 1	$R_{in}$		680	750	$\Omega$
Input limiting voltage	input signal $v_{in}$ : $f = 5.5\text{ MHz}$ output signal AF1: $v_{AF1} \Rightarrow -3\text{ dB}$	$v_{lim}$			150	$\mu\text{V}$
<b>Intercarrier input 2 Pin 14</b>						
DC input voltage		$V_{DC}$		1.75		V
Input resistance	see note 1	$R_{in}$		680	750	$\Omega$
Input limiting voltage	input signal $v_{in}$ : $f = 5.74\text{ MHz}$ output signal AF2: $v_{AF2} \Rightarrow -3\text{ dB}$	$v_{lim}$			150	$\mu\text{V}$
Input signal for automatic second sound carrier "mute off" (squelch)	audio output AF2 active	$v_{in}$	> 0.7	1.0	< 1.5	mV
<b>FM demodulators, internal VCO's Pin 10</b>						
Free-running frequency		$f_{VCO}$		5.5		MHz
Oscillator drift (free running) as function of temperature	$\Delta T = 55^\circ\text{C}$	$\Delta f_{VCO}$		500		kHz
Oscillator shift (free running) as function of supply voltage	$4.5\text{ V} < V_S < 5.5\text{ V}$	$\Delta f_{VCO}$		200		kHz
Adjustment range of free running frequencies	by external resistor $R_{ref}$ at Pin 10	$\Delta f_{adj}$	$\pm 1$			MHz
Adjustment resistance for free running frequencies		$R_{ref}$	15	22	30	k $\Omega$

Parameters	Test Conditions / Pins	Symbol	Min.	Typ.	Max.	Unit
<b>FM demodulators, internal VCO's Pin 10</b>						
Steppness of free running frequency adjustment	resistor $R_{ref}$ at Pin 10	S		200		kHz/k $\Omega$
Capture range of PLL's		$\Delta f_{cap}$	$\pm 1.4$	$\pm 1.9$		MHz
Holding range of PLL's		$\Delta f_{hold}$	$\pm 2.0$	$\pm 3.0$		MHz
<b>Audio outputs, AF1 (Pin 7) and AF2 (Pin 6)</b>						
DC output voltage		$V_{DC}$		2.2		V
DC output current		$I_{DC}$		-1.8	-2.0	mA
Output resistance	see note 1	$R_{out}$		150		$\Omega$
AC output peak current		$i_{AC}$			$\pm 1.5$	mA
AF output voltage, RMS value	$v_{in} = 10$ mV, $f = 5.5$ MHz, FM-dev. = 50 kHz, $f_{mod} = 1$ kHz	$v_{AF}$		500		mV
Difference between the output signals		$\Delta v_{AF}$			$\pm 1$	dB
Total harmonic distortion	$v_{in} = 10$ mV, $f = 5.5$ MHz, FM-dev. = 50 kHz, $f_{mod} = 1$ kHz	THD		0.1	0.5	%
AM suppression	$v_{in} = 10$ mV, $f = 5.5$ MHz, $f_{mod} = 1$ kHz, reference signal: FM-dev. = 50 kHz test signal: $m = 30\%$	$\alpha_{AM}$	46	66		dB
Crosstalk attenuation between the AF outputs	$f = 50$ Hz to 12.5 kHz	$\alpha_{att}$		70		dB
Supply voltage ripple rejection	$V_{RR} < 200$ mV, $f = 70$ Hz	RR		24		dB
<b>Mute switch Pin 3</b>						
Control voltage - muteoff - mute on	AF outputs active AF outputs not active	$V_{mute}$	2.0 0		$V_S$ 0.8	V V
Control current		$I_{mute}$		150		$\mu A$
<b>Squelch function Pin 4</b>						
Control voltage for automatic mute-2nd carrier "off" automatic mute-2nd carrier "on"		$V_{sque}$	0 2.0		0.8 $V_S$	V V
Control current		$I_{sque}$		150		$\mu A$

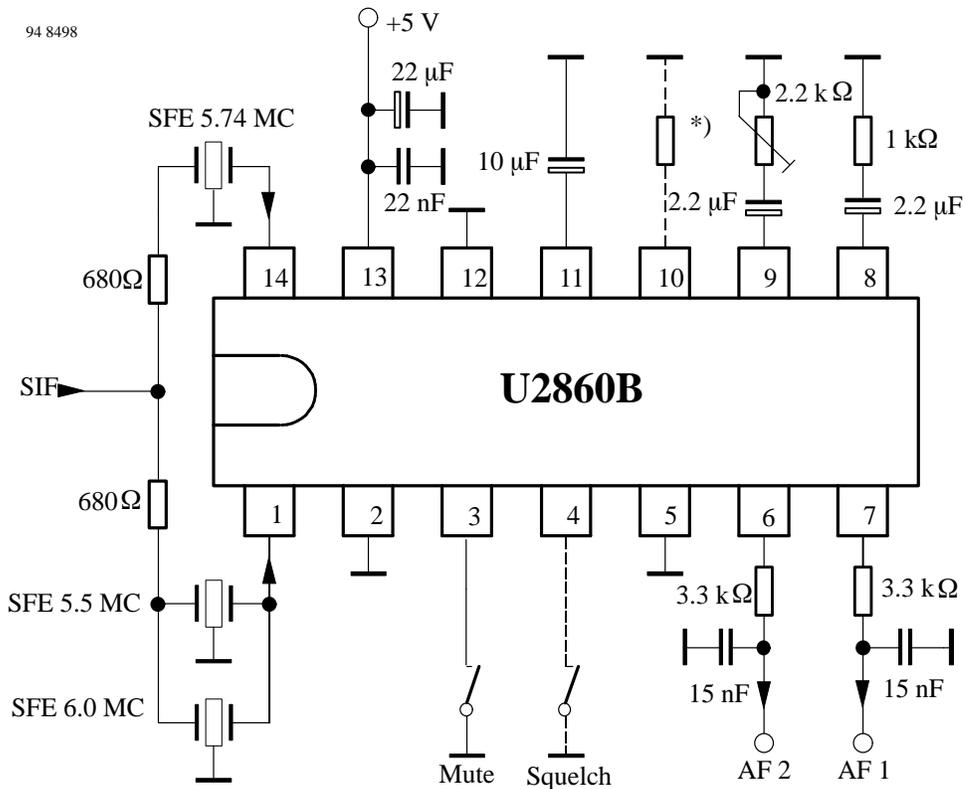
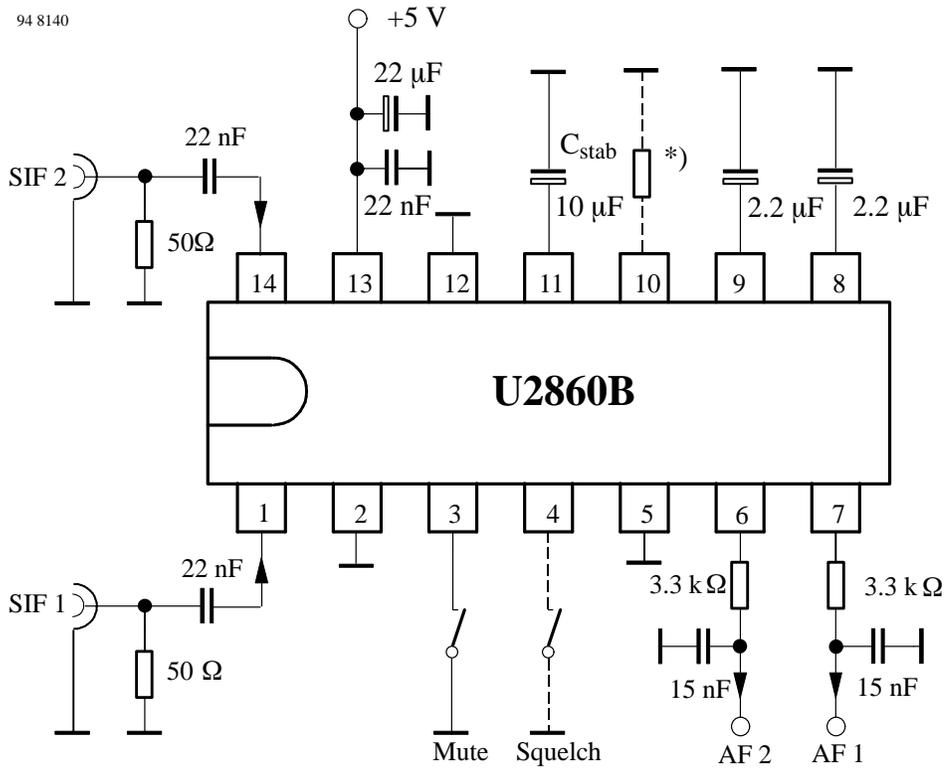
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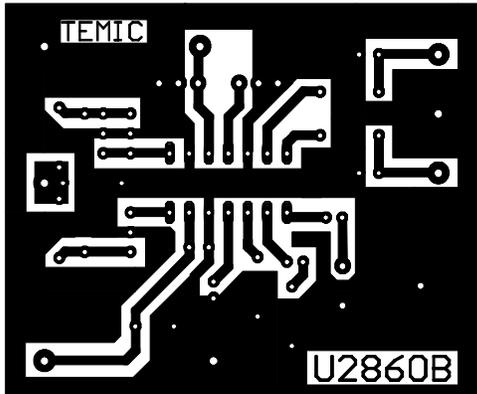
1. This parameter is given as an application information and not measured during final testing.

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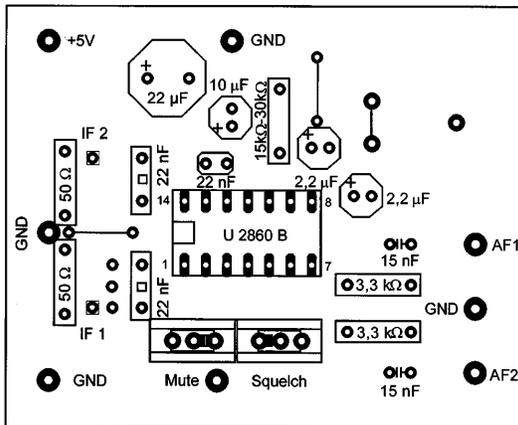
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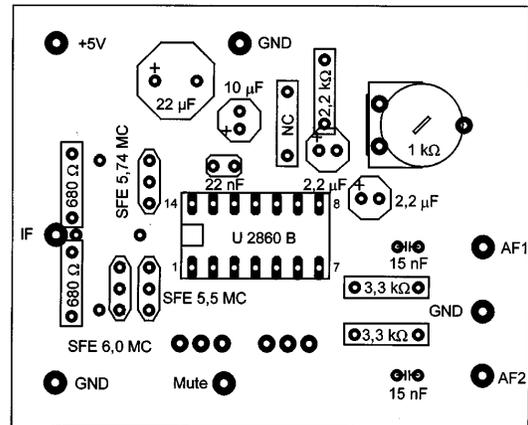
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Figure 5. PCB layout (test/application circuit)



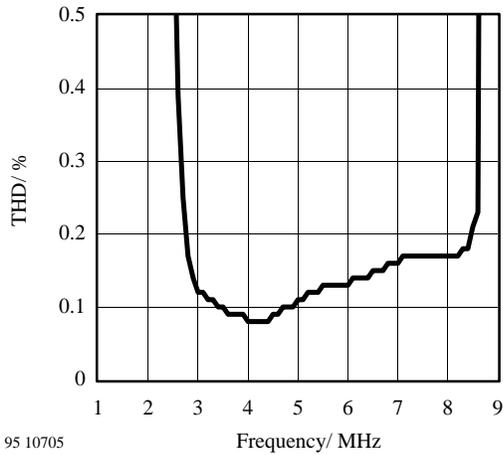
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Figure 6. Component layout (test circuit)



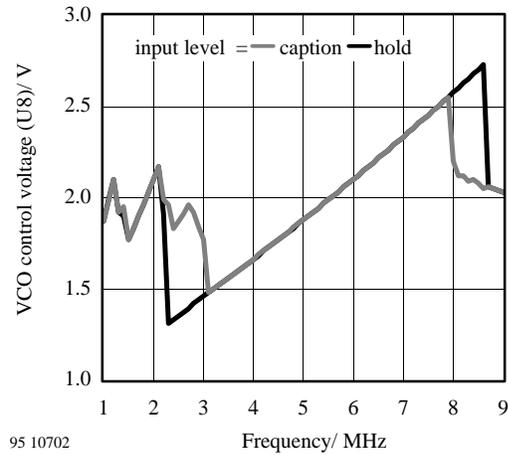
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Figure 7. Component layout (application circuit)



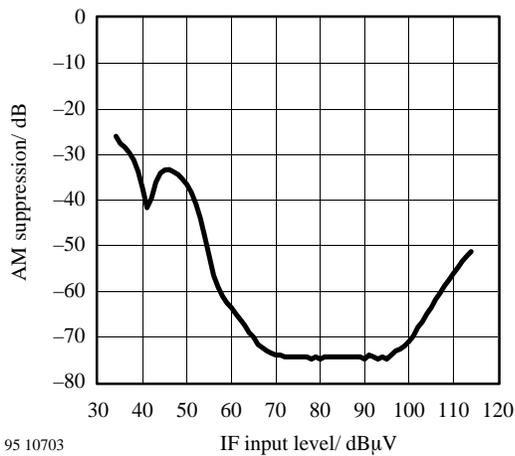
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Figure 8. Figure 9 AM suppression



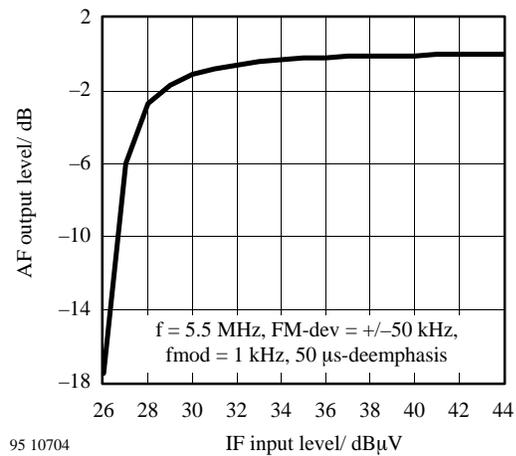
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Figure 11. Capture and hold range



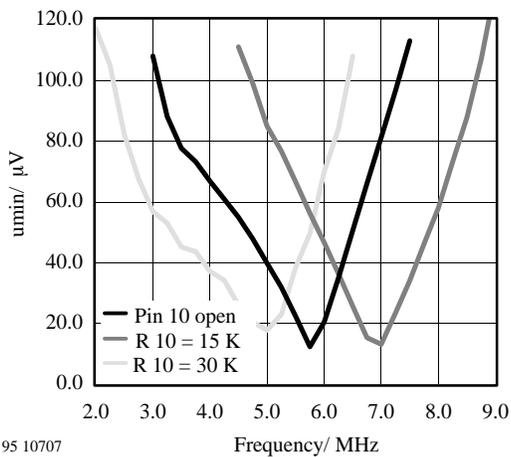
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Figure 9. VCO free running frequency



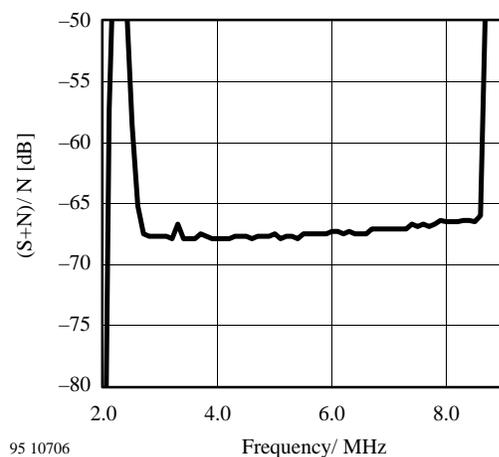
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Figure 12. Limiter characteristics



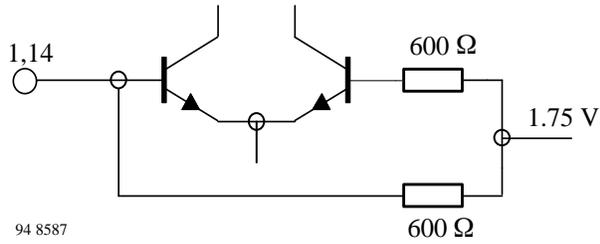
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Figure 10. Total harmonic distortion



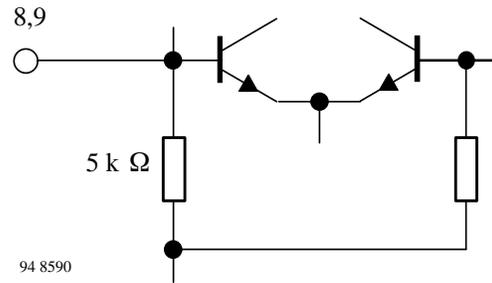
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Figure 13. Signal to noise ratio



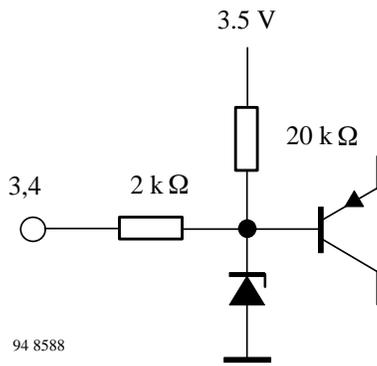
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Figure 14. SIF inputs



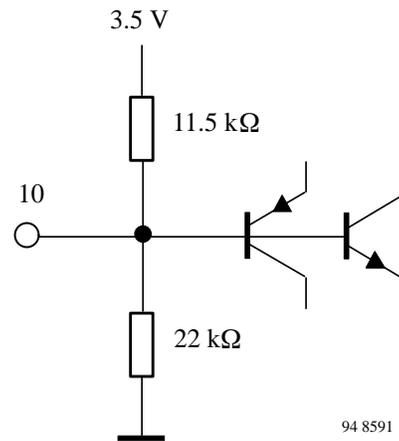
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Figure 17. Decoupling capacitor



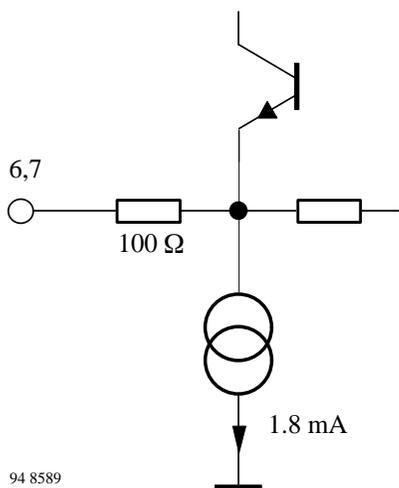
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Figure 15. Mute switch/squelch switch



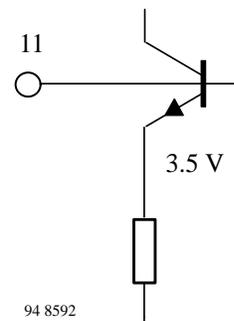
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Figure 18. VCO offset (reference resistor)



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Figure 16. Audio outputs



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Figure 19. Internal supply voltage stabilization

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## Dimensions in mm

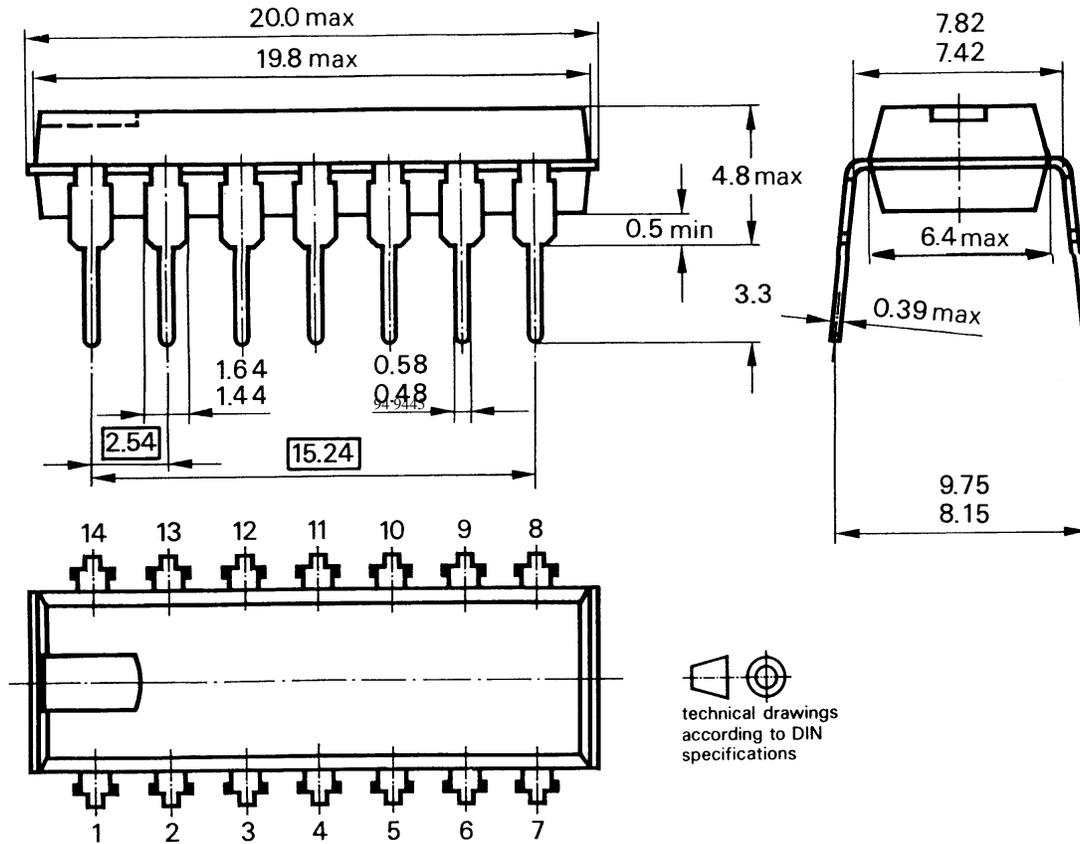


Figure 20. 14 pin dual-inline-plastic (DIP 14)

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1. Meet all present and future national and international statutory requirements.
2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

**TEMIC TELEFUNKEN microelectronic GmbH** semiconductor division has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

**TEMIC** can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

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