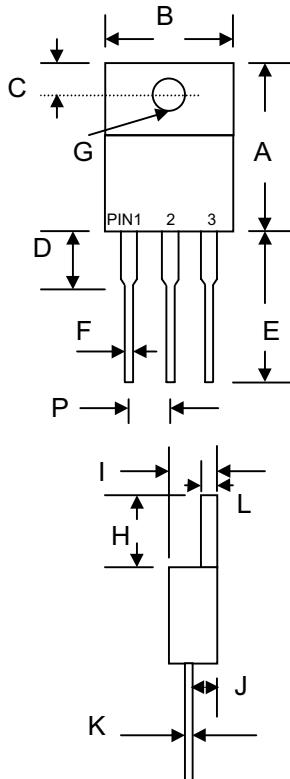


Features

- Glass Passivated Die Construction
- Ultra-Fast Switching
- High Current Capability
- Low Reverse Leakage Current
- High Surge Current Capability
- Plastic Material has UL Flammability Classification 94V-O

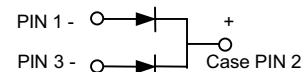
Mechanical Data

- Case: Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: See Diagram
- Weight: 2.24 grams (approx.)
- Mounting Position: Any
- Marking: Type Number



TO-220		
Dim	Min	Max
A	14.9	15.1
B	—	10.5
C	2.62	2.87
D	3.56	4.06
E	13.46	14.22
F	0.68	0.94
G	3.74 Ø	3.91 Ø
H	5.84	6.86
I	4.44	4.70
J	2.54	2.79
K	0.35	0.64
L	1.14	1.40
P	2.41	2.67

All Dimensions in mm



Maximum Ratings and Electrical Characteristics @T_A=25°C unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

Characteristic	Symbol	UF 1600CT	UF 1601CT	UF 1602CT	UF 1603CT	UF 1604CT	UF 1606CT	UF 1608CT	Unit
Peak Repetitive Reverse Voltage	V _{RRM}								
Working Peak Reverse Voltage	V _{RWM}	50	100	200	300	400	600	800	V
DC Blocking Voltage	V _R								
RMS Reverse Voltage	V _{R(RMS)}	35	70	140	210	280	420	560	V
Average Rectified Output Current @T _c = 105°C	I _o				16				A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}				125				A
Forward Voltage @I _F = 8.0A	V _{FM}		1.0		1.3		1.7		V
Peak Reverse Current @T _A = 25°C At Rated DC Blocking Voltage @T _A = 125°C	I _{RM}			10	500				µA
Reverse Recovery Time (Note 1)	t _{rr}		50			100			nS
Typical Junction Capacitance (Note 2)	C _j		80			50			pF
Operating and Storage Temperature Range	T _j , T _{STG}			-65 to +150					°C

Note: 1. Measured with IF = 0.5A, IR = 1.0A, IRR = 0.25A. See figure 5.

2. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.

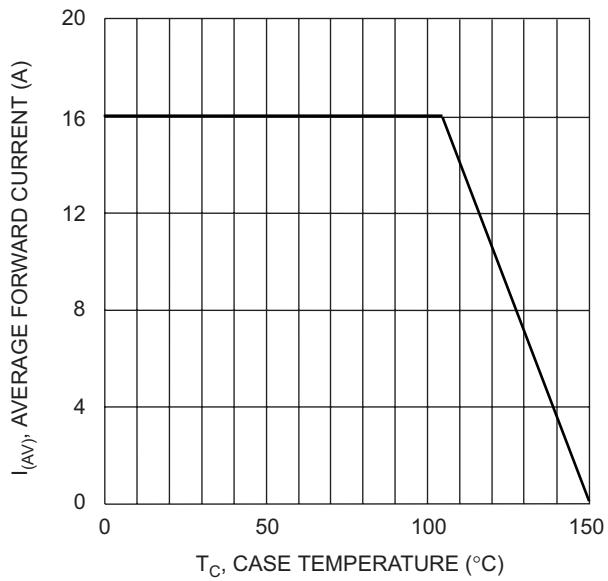


Fig. 1 Forward Current Derating Curve

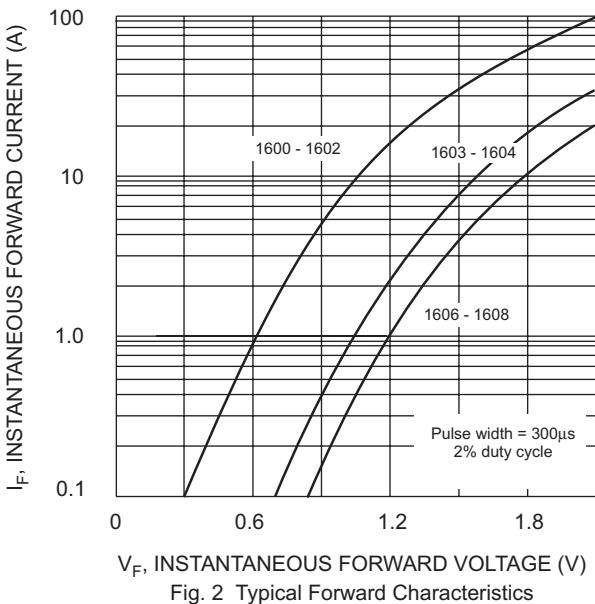


Fig. 2 Typical Forward Characteristics

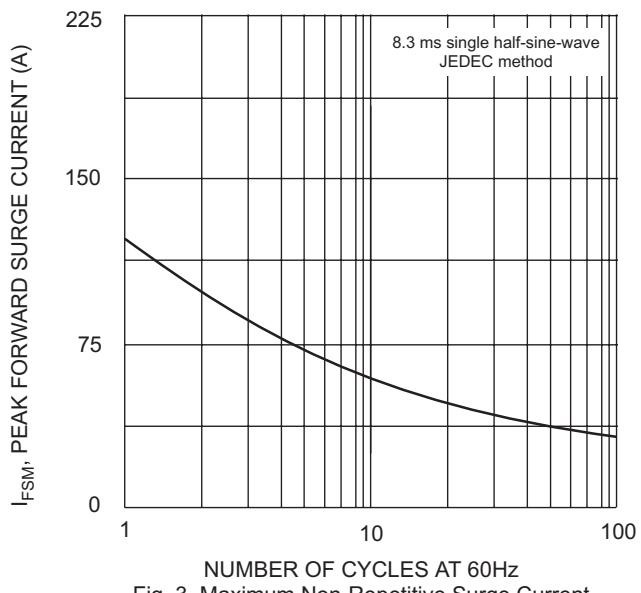


Fig. 3 Maximum Non-Repetitive Surge Current

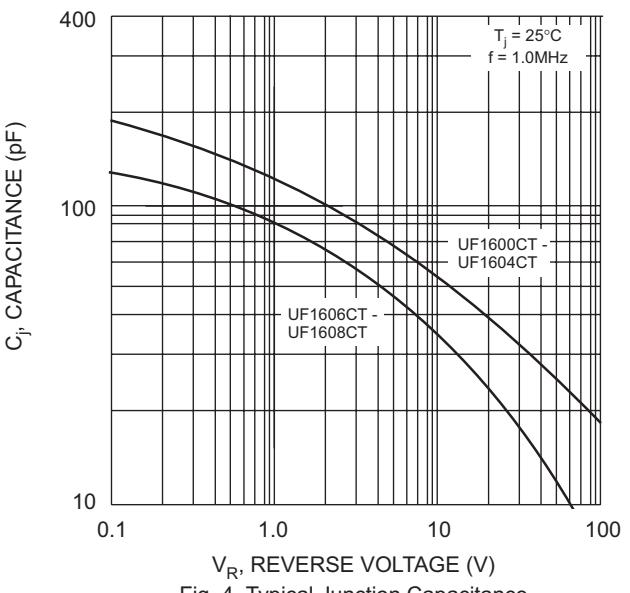


Fig. 4 Typical Junction Capacitance

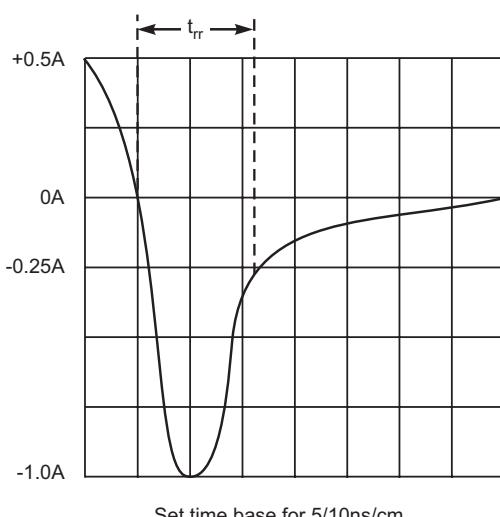
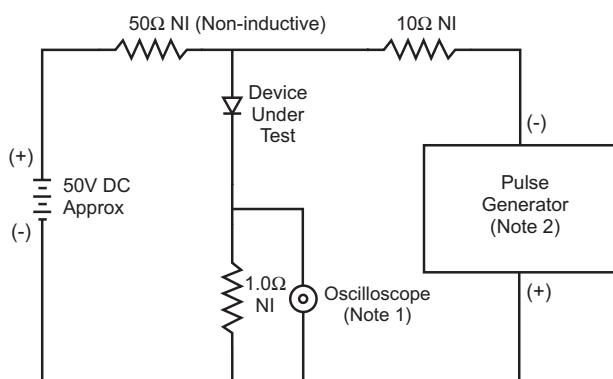


Fig. 5 Reverse Recovery Time Characteristic and Test Circuit