

# NPN Silicon High-Voltage Power Transistors

... designed for use in line-operated equipment requiring high fT.

• High DC Current Gain

 $h_{FE} = 40-160 @ I_{C}$ = 20 mAdc

• Current Gain Bandwidth Product —

 $f_T = 15 \text{ MHz (Min)} @ I_C$ = 10 mAdc

• Low Output Capacitance

 $C_{ob} = 10 \text{ pF (Max) } @ \text{ f}$ = 1.0 MHz

#### **MAXIMUM RATINGS**

Rating	Symbol Value		Unit
Collector–Emitter Voltage	VCEO	350	Vdc
Collector–Base Voltage	VCB	450	Vdc
Emitter–Base Voltage	V <sub>EB</sub>	5.0	Vdc
Collector Current — Continuous	IC	0.3	Adc
Base Current	ΙΒ	150	mAdc
Total Power Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	P <sub>D</sub>	15 0.12	Watts W/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>Stg</sub>	-65 to +150	°C

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$\theta$ JC	8.33	°C/W

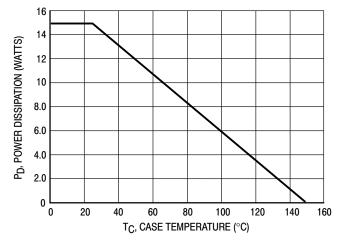
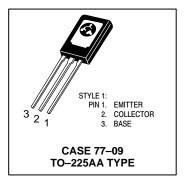


Figure 1. Power-Temperature Derating Curve

## **MJE3439**

0.3 AMPERE
POWER TRANSISTOR
NPN SILICON
350 VOLTS
15 WATTS



#### **MJE3439**

#### **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> = 25°C unless otherwise noted)

Symbol	Min	Max	Unit
,		•	•
VCEO(sus)	350	_	Vdc
ICEO	_	20	μAdc
ICEX	_	500	μAdc
ІСВО	_	20	μAdc
lEBO	_	20	μAdc
<u> </u>			
hFE	30 15	 200	_
VCE(sat)	_	0.5	Vdc
VBE(sat)	_	1.3	Vdc
VBE(on)	_	0.8	Vdc
·			
fT	15	_	MHz
C <sub>ob</sub>	_	10	pF
h <sub>fe</sub>	25	_	_
	VCEO(sus)  ICEO ICEX ICBO IEBO  VEE(sat) VBE(sat) VBE(on)	VCEO(sus)   350	VCEO(sus)         350         —           ICEO         —         20           ICEX         —         500           ICBO         —         20           IEBO         —         20           VCE(sat)         —         0.5           VBE(sat)         —         1.3           VBE(on)         —         0.8

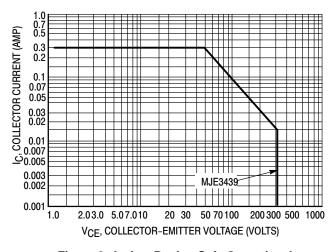


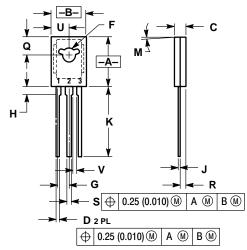
Figure 2. Active-Region Safe Operating Area

The Safe Operating Area Curves indicate  $I_C - V_{CE}$  limits below which the device will not enter secondary breakdown. Collector load lines for specific circuits must fall within the applicable Safe Area to avoid causing a catastrophic failure. To insure operation below the maximum  $T_J$ , power–temperature derating must be observed for both steady state and pulse power conditions.

### **MJE3439**

### **PACKAGE DIMENSIONS**

#### TO-225AA **CASE 77-09 ISSUE W**



STYLE 1:
PIN 1. EMITTER
2. COLLECTOR
3. BASE

- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.

	INCHES		MILLIN	MILLIMETERS	
DIM	MIN	MAX	MIN	MAX	
Α	0.425	0.435	10.80	11.04	
В	0.295	0.305	7.50	7.74	
С	0.095	0.105	2.42	2.66	
D	0.020	0.026	0.51	0.66	
F	0.115	0.130	2.93	3.30	
G	0.094 BSC		2.39 BSC		
Н	0.050	0.095	1.27	2.41	
J	0.015	0.025	0.39	0.63	
K	0.575	0.655	14.61	16.63	
M	5°	TYP	5°	5° TYP	
Q	0.148	0.158	3.76	4.01	
R	0.045	0.065	1.15	1.65	
S	0.025	0.035	0.64	0.88	
U	0.145	0.155	3.69	3.93	
٧	0.040		1.02		

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