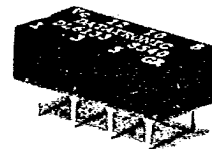


SERIES DL6130 TRIPLE DIGITAL DELAY MODULES

- 14 Pin Package
- Three Independent Equal Delays
- TTL and DTL Compatible

Specifications

- Supply Voltage : 4.75 to 5.25VDC
- Logic 1 Input Current : 50µA Max
- Logic 0 Input Current : -2mA Max
- Logic 1 Volt Out : 2.5v Min
- Logic 0 Volt Out : 0.5v Max
- Logic 1 Fan-Out : 20/Tap Max
- Logic 0 Fan-Out : 10/Tap Max
- Power Dissipation : 240mW Total
- Operating Temp Range : 0°C to 70°C
- Temp Coefficient : 100PPM/°C



Input Test Conditions

- Input Pulse Voltage : 3.2v
- Supply Current : I_{cch} 90mA Max
I_{ccl} 115mA Max
- Input Pulse-width : Min 100% of Total Delay

Electrical Specifications at 25°C (measured with no loads on Taps)

Part Number	Delay Time NSEC (1)	Tolerance NSEC	Rise Time NSEC Max (2)
DL6130	5	±1.5	3
DL6131	10	±1.5	3
DL6132	15	±1.5	3
DL6133	20	±1.5	3
DL6134	25	±1.5	3
DL6135	30	±2.0	3
DL6136	40	±2.5	3
DL6137	50	±2.5	3
DL6138	60	±3.0	4
DL6139	70	±3.0	4
DL6140	75	±4.0	4
DL6141	80	±4.0	4
DL6142	90	±4.0	4
DL6143	100	±5.0	4
DL6144	125	±6.0	4
DL6145	150	±7.0	4
DL6146	154	±7.0	4
DL6147	175	±8.0	4
DL6148	200	±10.0	4
DL6149	225	±11.0	4
DL6150	250	±12.0	4
DL6151	300	±15.0	5
DL6152	350	±17.0	5
DL6153	400	±20.0	5
DL6154	450	±22.0	5
DL6155	500	±25.0	5

Note: (1) Measured at 1.5v level leading edge.
(2) Measured from 0.75v to 2.4v.

