

STAP1011-180

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RF power transistor the LdmoST family, N-channel enhancement-mode lateral MOSFETs

Features

- Excellent thermal stability
- Common source configuration push-pull
- P_{OUT} = 180W with 10 dB gain @ 1030 MHz 36 V/100 µsec - 10%
- Plastic package
- In compliance with the 2002/95/EC european directive

Description

The STAP1011-180 is a common source N-channel enhancement-mode lateral field-effect RF power transistor. It is designed for 1030-1090 MHz avionics applications.

STAP1011-180 is mounted in STAP ST advanced PowerSO-10RF package.

The STAP package was designed to offer high reliability and power capability. It has been specially optimized for RF needs and offers excellent RF performances and ease of assembly.



Figure 1. Pin connection

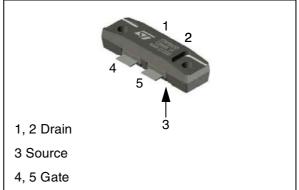


Table 1. Device summary

Order code	Package	Packaging
STAP1011-180	STAP2	Tube

1 Electrical data

1.1 Maximum ratings

T_{CASE} = 25 °C

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V _{(BR)DSS}	Drain-source voltage	65	V
V _{GS}	Gate-source voltage	±20	V
P _{DISS}	Power dissipation (@ Tc = 70 °C)	800	W
Т _Ј	Max. operating junction temperature	165	°C
T _{STG}	Storage temperature	-65 to + 150	°C

1.2 Thermal data

Table 3. Thermal data (CW)

Symbol	Parameter	Value	Unit
R _{thJC}	Junction - case thermal resistance	0.10	°C/W



2 Electrical characteristics

T_{CASE} = +25 °C

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2.1 Static

Table 4. Static (per side)

Symbol	Test conditions			Min	Тур	Max	Unit
V _{(BR)DSS}	$V_{GS} = 0 V$	I _{DS} = 1 mA		65			V
I _{DSS}	$V_{GS} = 0 V$	V _{DS} = 28 V				1	μA
I _{GSS}	V _{GS} = 20 V	$V_{DS} = 0 V$				1	μA
V _{GS(Q)}	V _{DS} = 28 V	I _D = 100 mA		2.0		4.0	V
V _{DS(ON)}	V _{GS} = 10 V	I _D = 3 A			0.7	0.9	V
G _{FS}	V _{DS} = 10 V	I _D = 3 A			3		S
$\Delta V_{TH}^{(1)}$	I _D = 100 mA					100	mV
C _{ISS}	V _{GS} = 0 V	V _{DS} = 28 V	f = 1 MHz		83		pF
C _{OSS}	V _{GS} = 0 V	V _{DS} = 28 V	f = 1 MHz		58		pF
C _{RSS}	$V_{GS} = 0 V$	V _{DS} = 28 V	f = 1 MHz		3.0		pF

1. Absolute VGS difference between side 1 and side 2 of the device

2.2 Dynamic

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Table 5.	Dynamic				
Symbol	Test conditions	Min	Тур	Max	Unit
P _{OUT}	$V_{DD} = 36 V I_{DQ} = 300 mA$ f = 1030 MHz	180	195	-	W
G _{PS}	V_{DD} = 36 V I _{DQ} = 300 mA P _{OUT} = 180 W f = 1030 MHz	10		-	dB
h _D	$V_{DD} = 36 \text{ V} I_{DQ} = 300 \text{ mA} P_{OUT} = 180 \text{ W} \text{ f} = 1030 \text{ MHz}$	45	50	-	%
Load mismatch	$V_{DD} = 36 \text{ V} I_{DQ} = 300 \text{ mA} P_{OUT} = 180 \text{ W} \text{ f} = 1030 \text{ MHz}$ All phase angles	10:1		-	VSWR
Rise time	$V_{DD} = 36 \text{ V } I_{DQ} = 300 \text{ mA} P_{OUT} = 180 \text{ W} f = 1030 \text{ MHz}$		40	100	nsec
Fall time	$V_{DD} = 36 \text{ V} \text{ I}_{DQ} = 300 \text{ mA} \text{ P}_{OUT} = 180 \text{ W} \text{ f} = 1030 \text{ MHz}$		25	100	nsec
Power drop	$V_{DD} = 36 \text{ V} \text{ I}_{DQ} = 300 \text{ mA} \text{ P}_{OUT} = 180 \text{ W} \text{ f} = 1030 \text{ MHz}$		0.3	0.5	dB

Table 5. Dynamic

3 Impedance

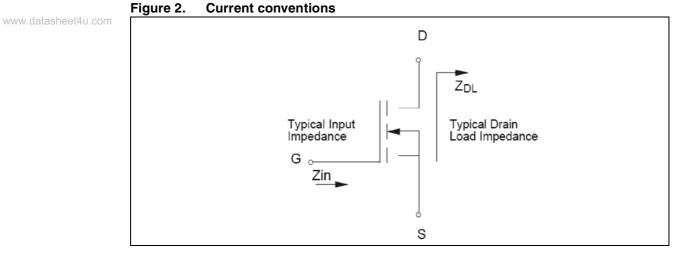


Table 6.Impedance data

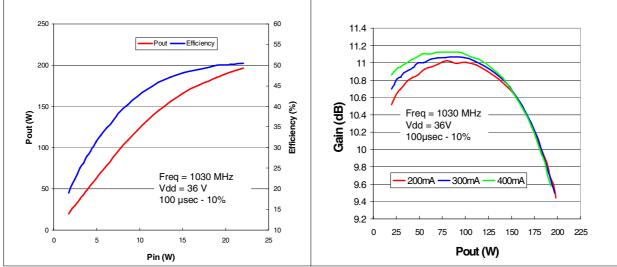
Freq. (MHz)	Z _{IN} (Ω)	Z _{DL} (Ω)
1030 MHz	TBD	TBD

Note: Measured gate to gate and drain to drain respectively.

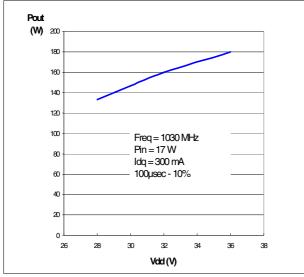


4 Typical performance











5 Package mechanical data

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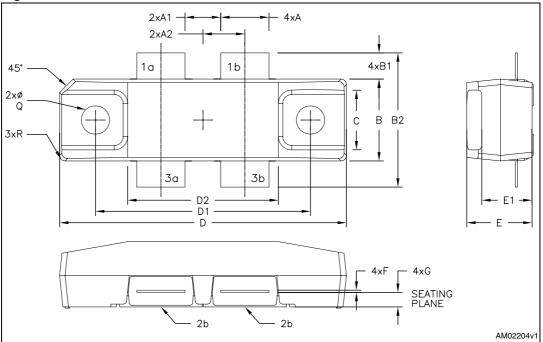


Dim	mm					
DIM	Min	Тур	Max	Min	Тур	Max
A	5.40		5.65	0.212		0.222
A1	3.89		4.29	0.153		0.169
A2	4.70		4.90	0.185		0.193
В	9.27		9.53	0.365		0.375
B1	2.90		3.10	0.114		0.122
B2	15.10		15.65	0.594		0.616
С	6.60		6.99	0.260		0.275
D	32.74		33.05	1.289		1.301
D1	24.51		24.82	0.965		0.977
D2	17.15		17.45	0.675		0.687
E	7.42		7.57	0.292		0.298
E1	5.69		5.84	0.224		0.230
F	0.21		0.31	0.008		0.012
G	1.62		1.68	0.064		0.068
Q	3.15		3.30	0.124		0.130
R		0.64			0.025	

 Table 7.
 STAP2 mechanical data

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Figure 6. STAP2 mechanical data





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6 Revision history

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e 8. Document revision history

Date	Revision	Changes
07-May-2009	1	Initial release



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