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REVISIONS

ZONE	REV	DESCRIPTION	DATE	APPROVED
	0	ORIGINAL RELEASE	04-14-00	KC



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DESIGNED BY:
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KEVIN CHENEY

PROJECT LEAD:
KEVIN CHENEY

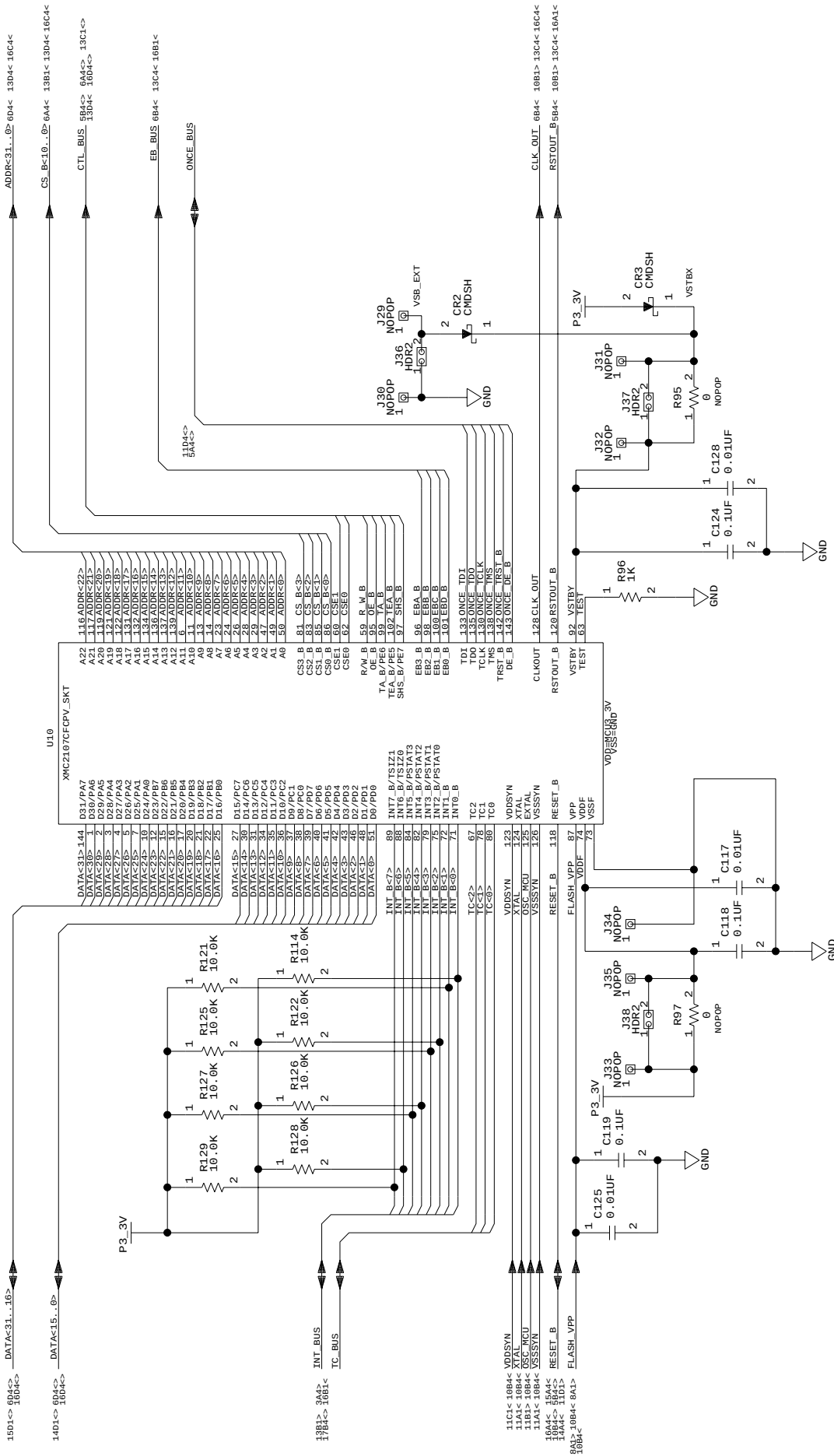
PRODUCT ENGINEER:
CARLOS BLANCO

9801 METRIC BLVD
AUSTIN, TEXAS 78758 USA

TITLE:
MMC2107 CMB/EVB
SCHEMATIC

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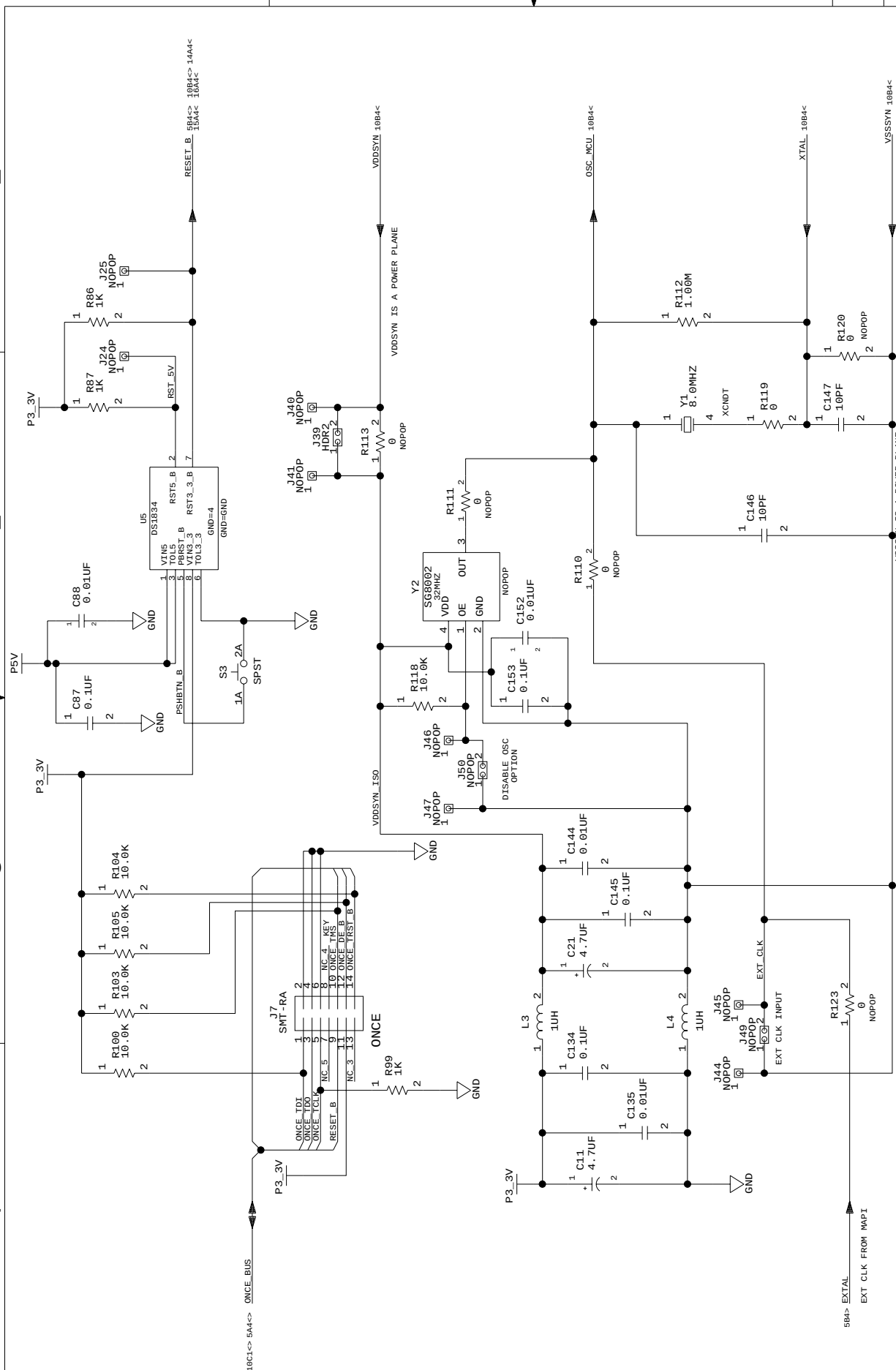
LAST_MODIFIED=Tue Apr 18 09:19:30 2000 SHEET 1 OF 17



MMC2107 MCU: DATA, ADDRESS, CONTROL, & EDGE

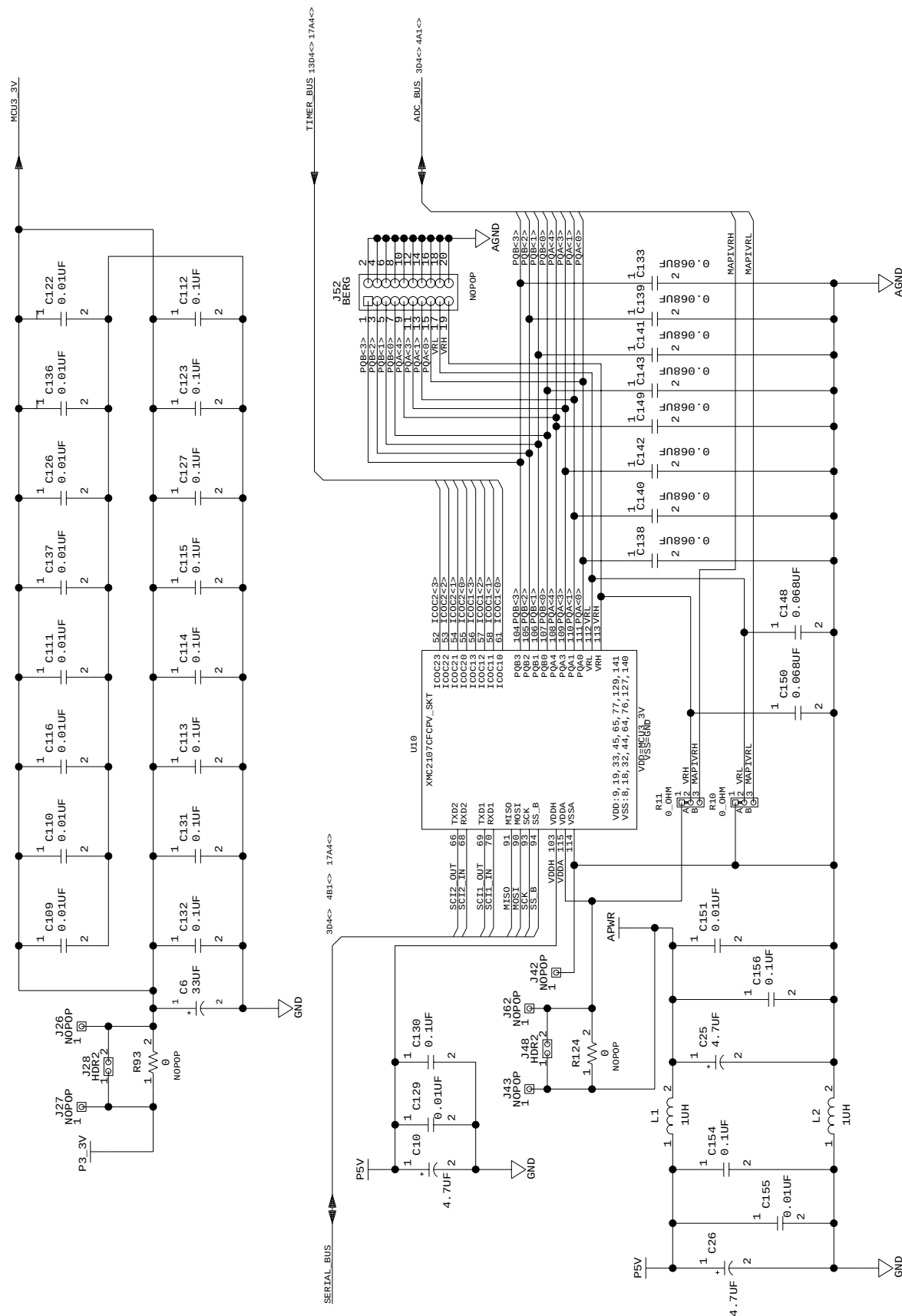
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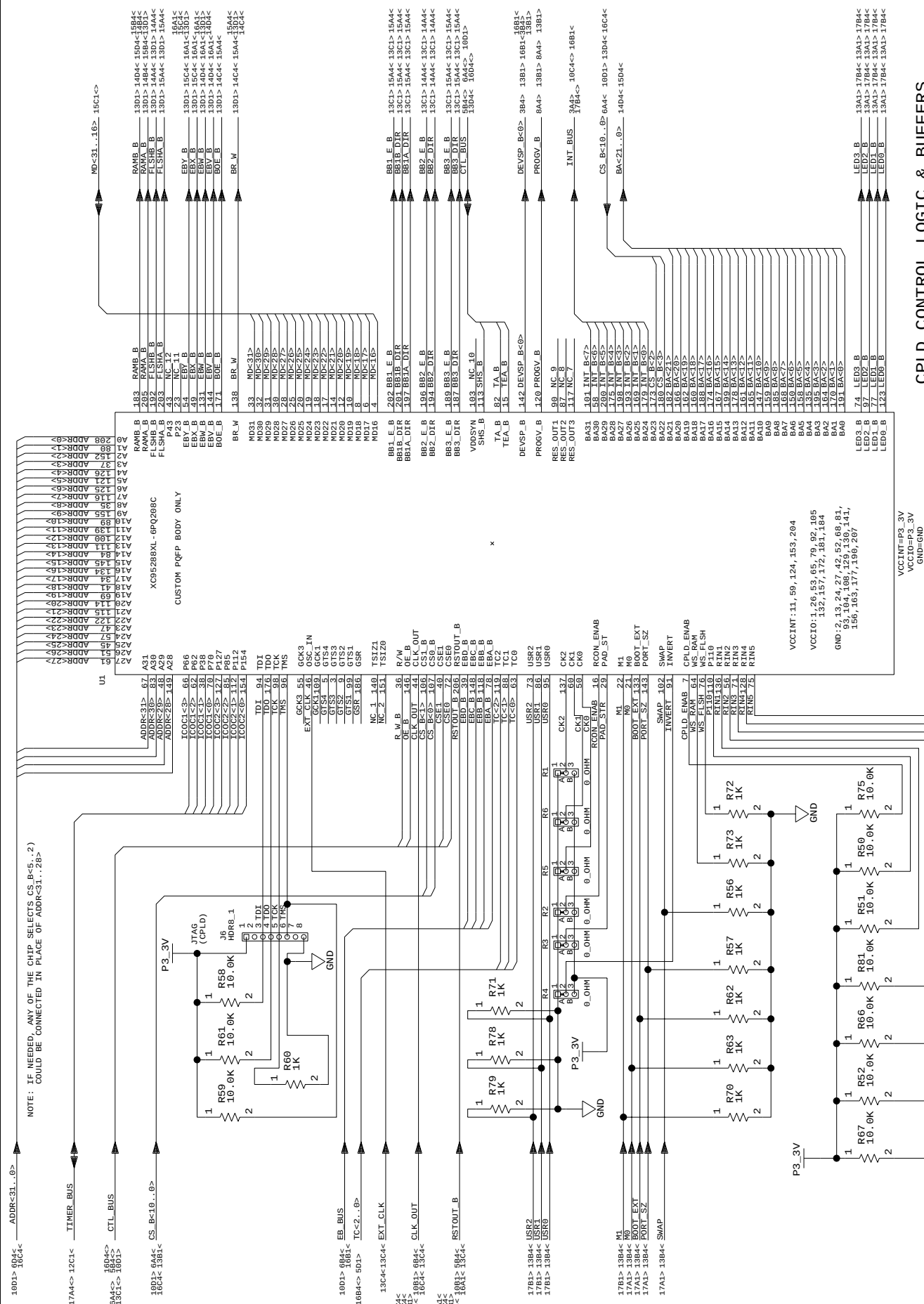
ONCE PORT, CLOCK, AND RESET

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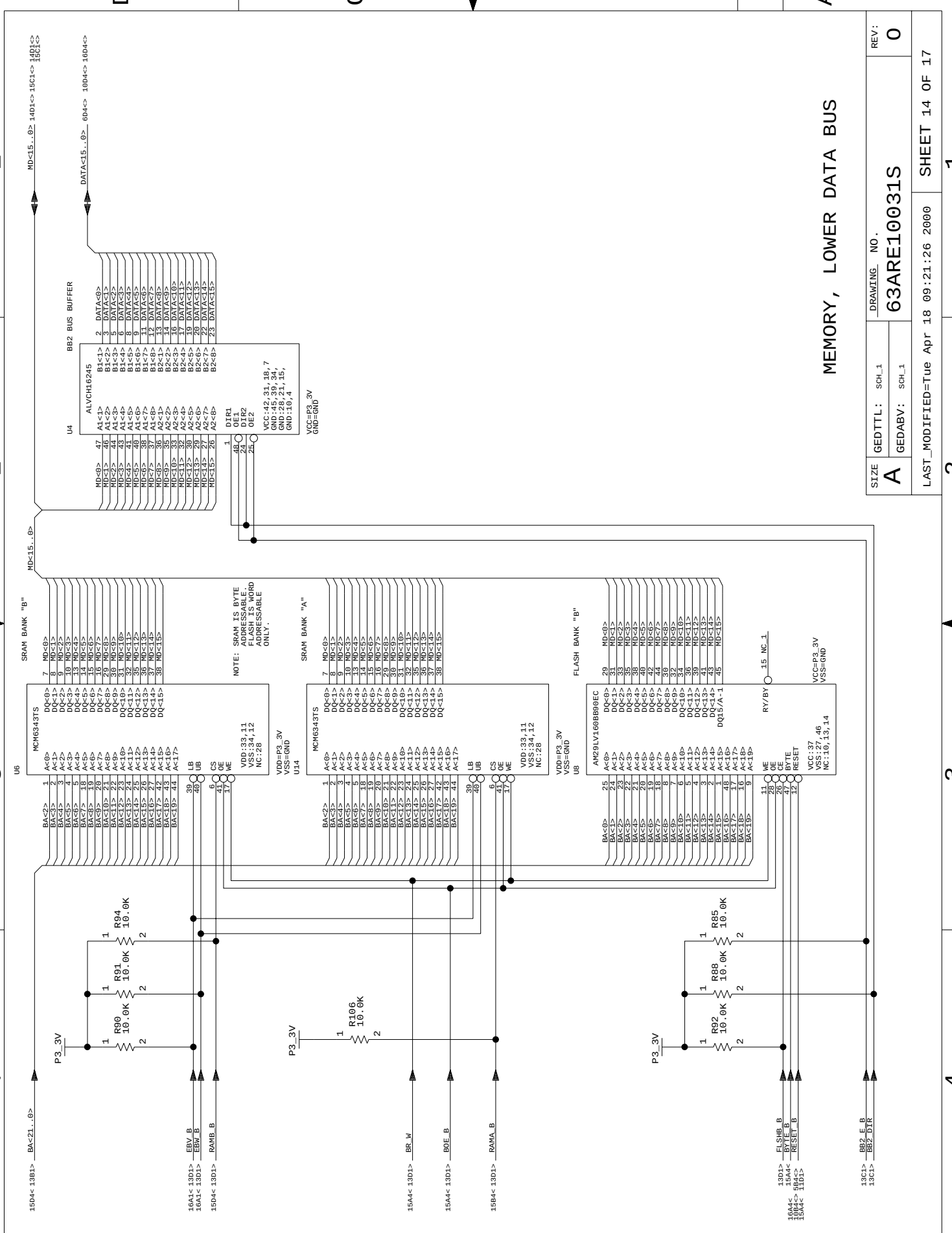
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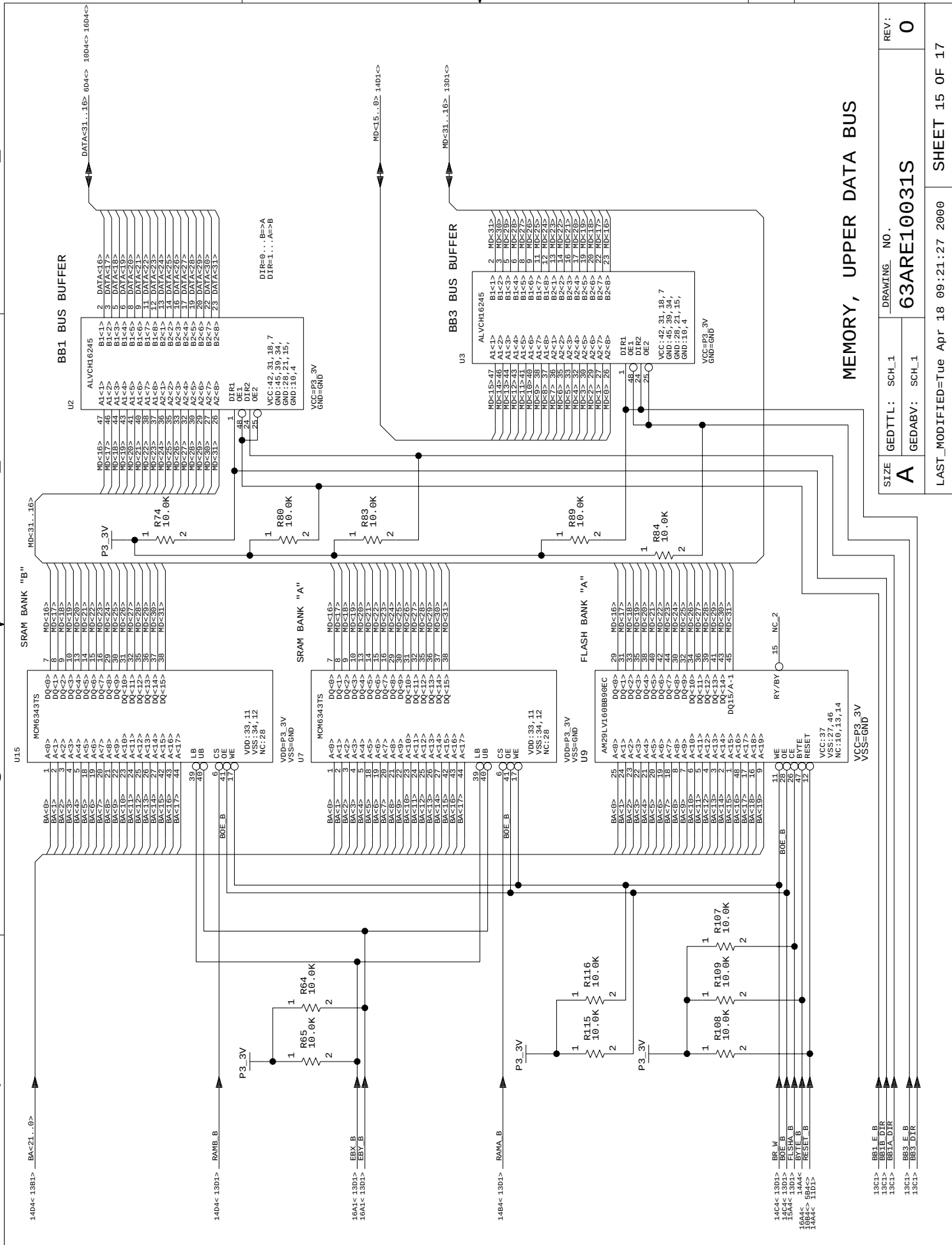
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**For More Information On This Product,
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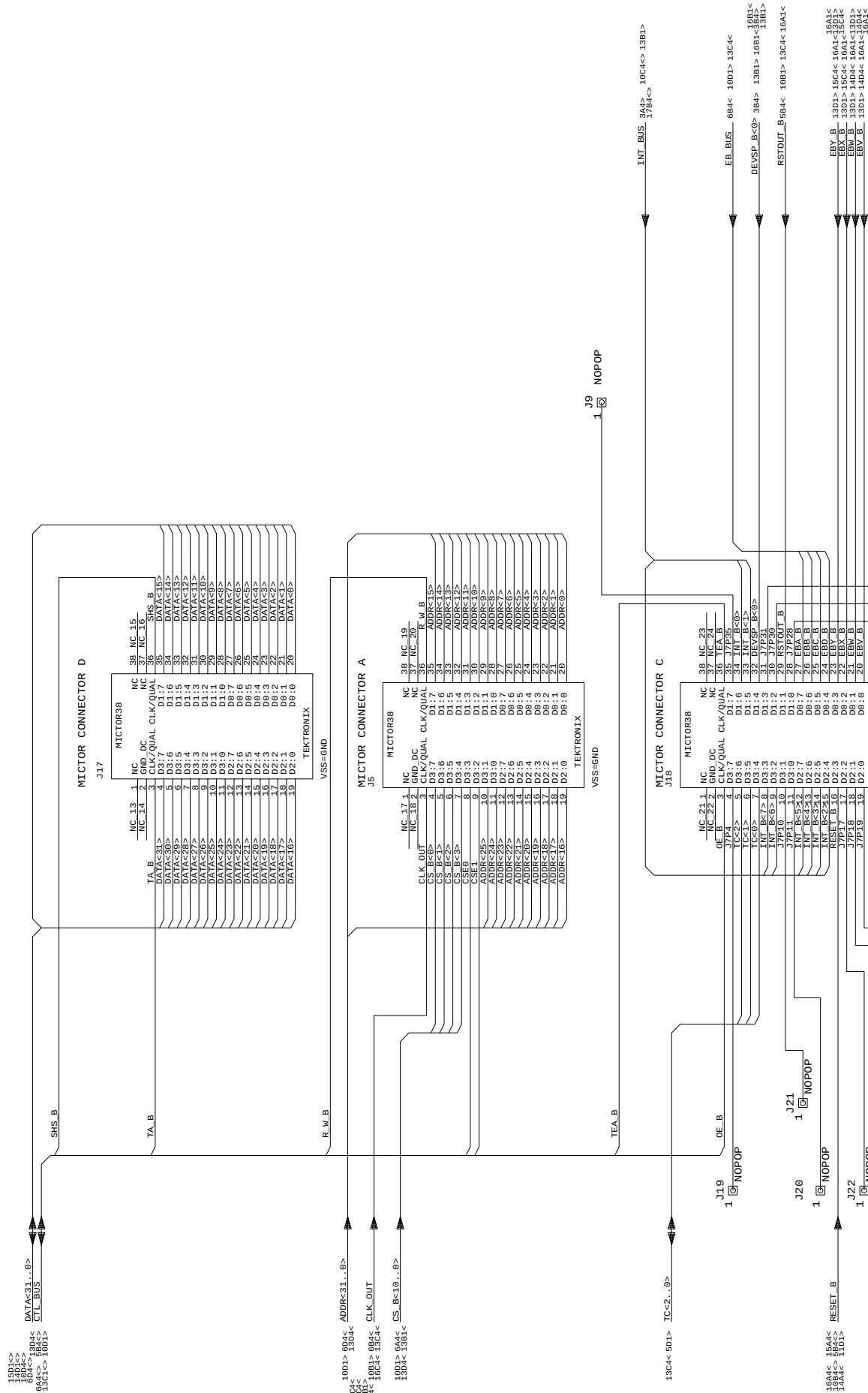
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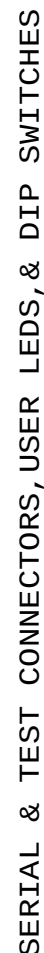


MEMORY, UPPER DATA BUS

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LAST_MODIFIED=Tue Apr 18 09:21:27 2000			
SHEET 15 OF 17			



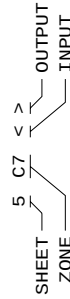
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NOTES

NOTES:

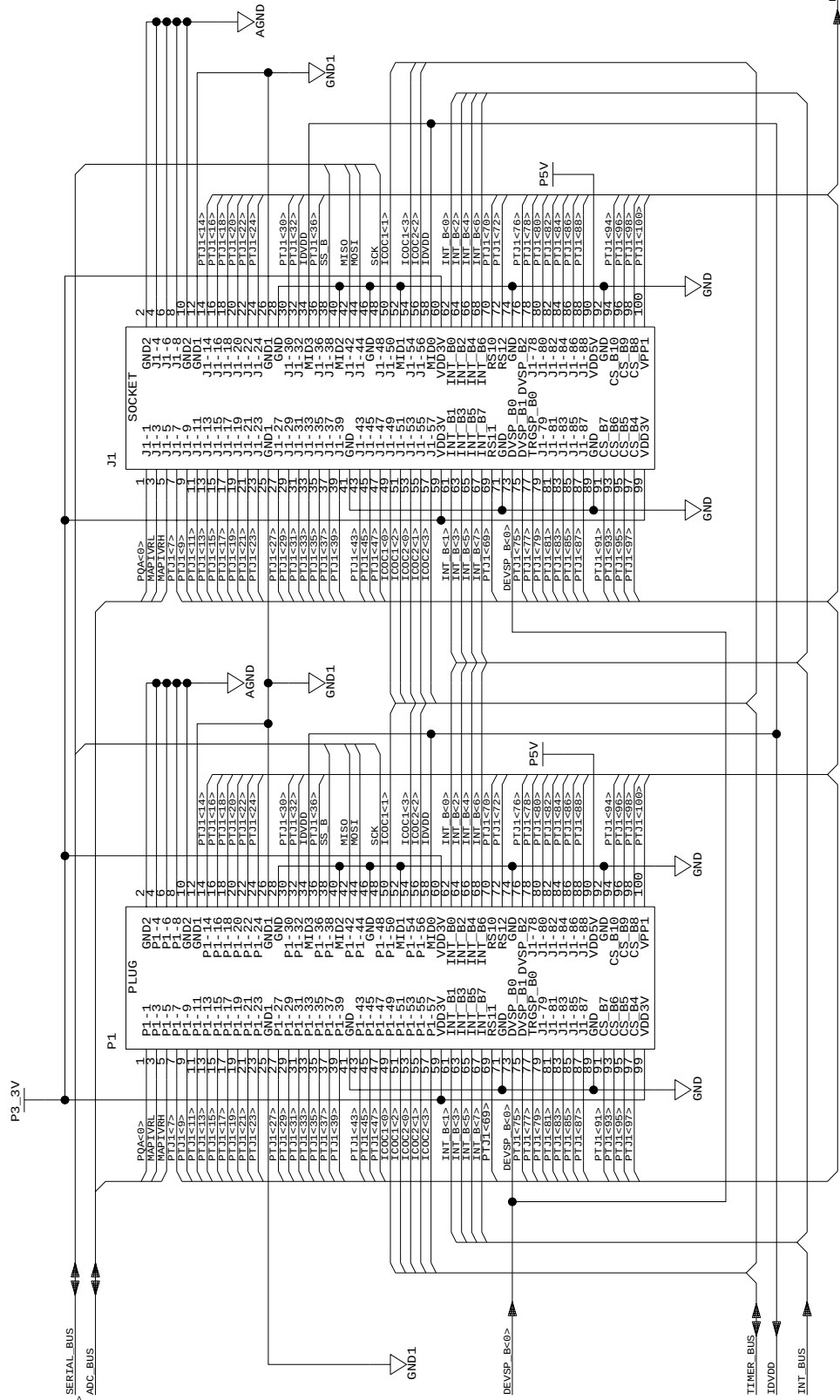
1. UNLESS OTHERWISE SPECIFIED:
ALL RESISTORS ARE IN OHMS, 5%, 1/8 WATT.
ALL CAPACITORS ARE IN UF.
2. ALL VOLTAGES ARE DC.
INTERRUPTED LINES CODED WITH THE
SAME LETTER OR LETTER COMBINATIONS
ARE ELECTRICALLY CONNECTED.
3. DEVICE TYPE NUMBER IS FOR REFERENCE
ONLY. THE NUMBER VARIES WITH THE
MANUFACTURER.
4. SPECIAL SYMBOL USAGE:
-B DENOTES - ACTIVE LOW SIGNAL.
<> DENOTES - VECTORED SIGNALS.
5. INTERPRET DIAGRAM IN ACCORDANCE
WITH AMERICAN NATIONAL STANDARDS
INSTITUTE SPECIFICATIONS, CURRENT
REVISION, WITH THE EXCEPTION OF
LOGIC BLOCK SYMBOLOLOGY.
6. CODE FOR SHEET TO SHEET REFERENCES
IS AS FOLLOWS:



SIZE	GEDTTL: SCH_1	DRAWING NO.	REV:
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LAST_MODIFIED=Tue Apr 18 09:14:39 2000 SHEET 2 OF 17

MAPI P1/J1



SIZE	GEDTTL: SCH_1	DRAWING NO.	REV:
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MAPI P2/J2

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SHEET 4 OF 17				

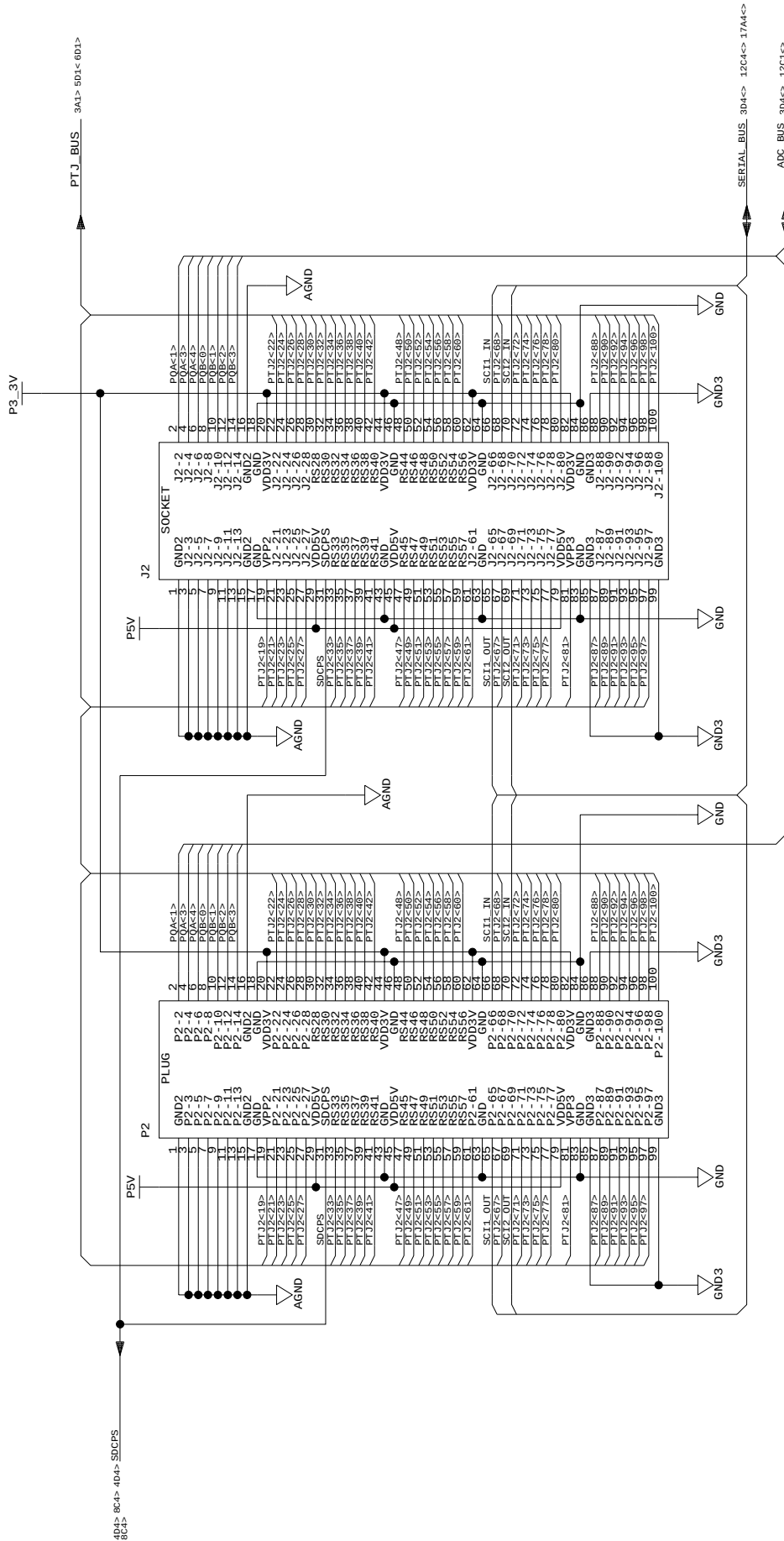
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A

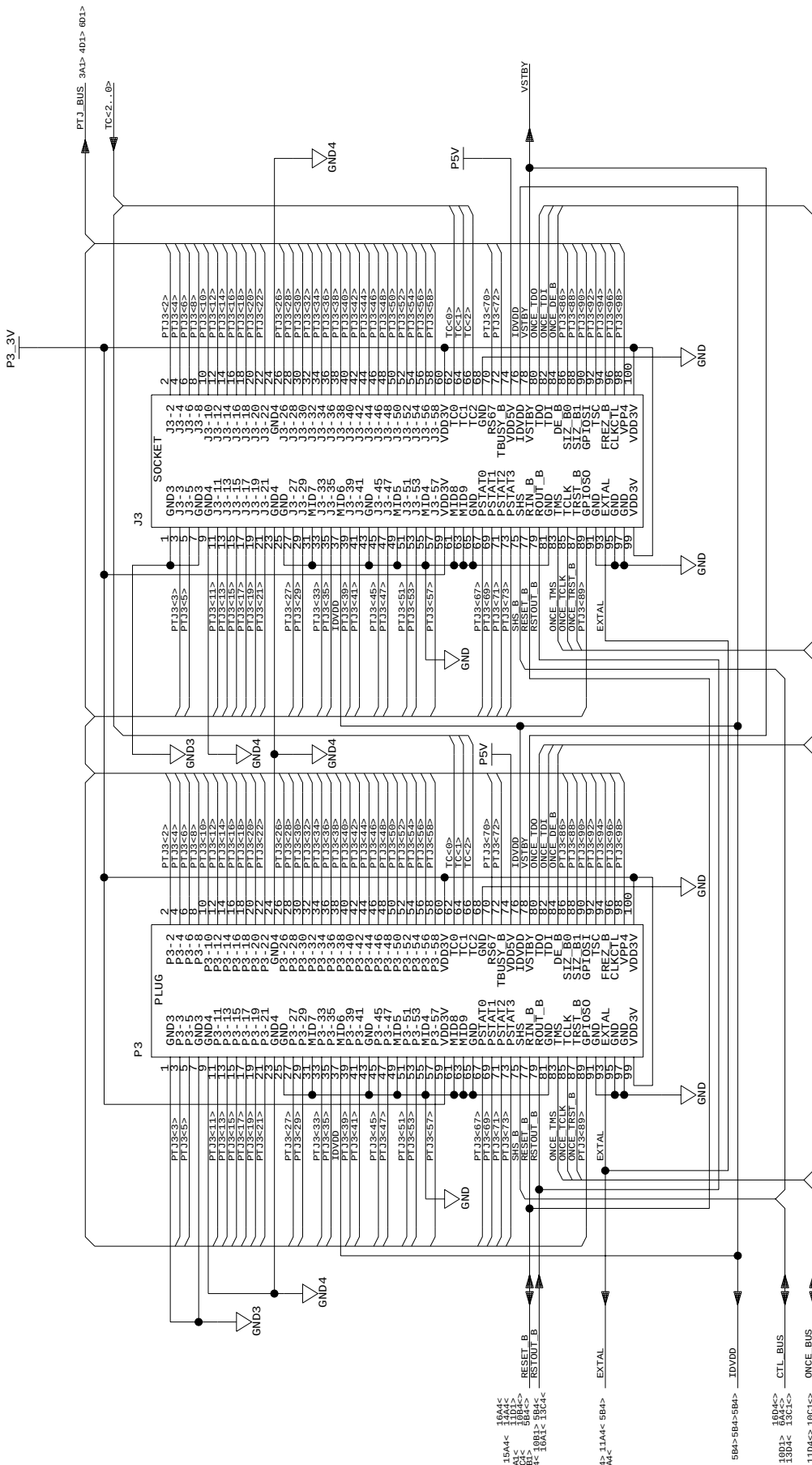


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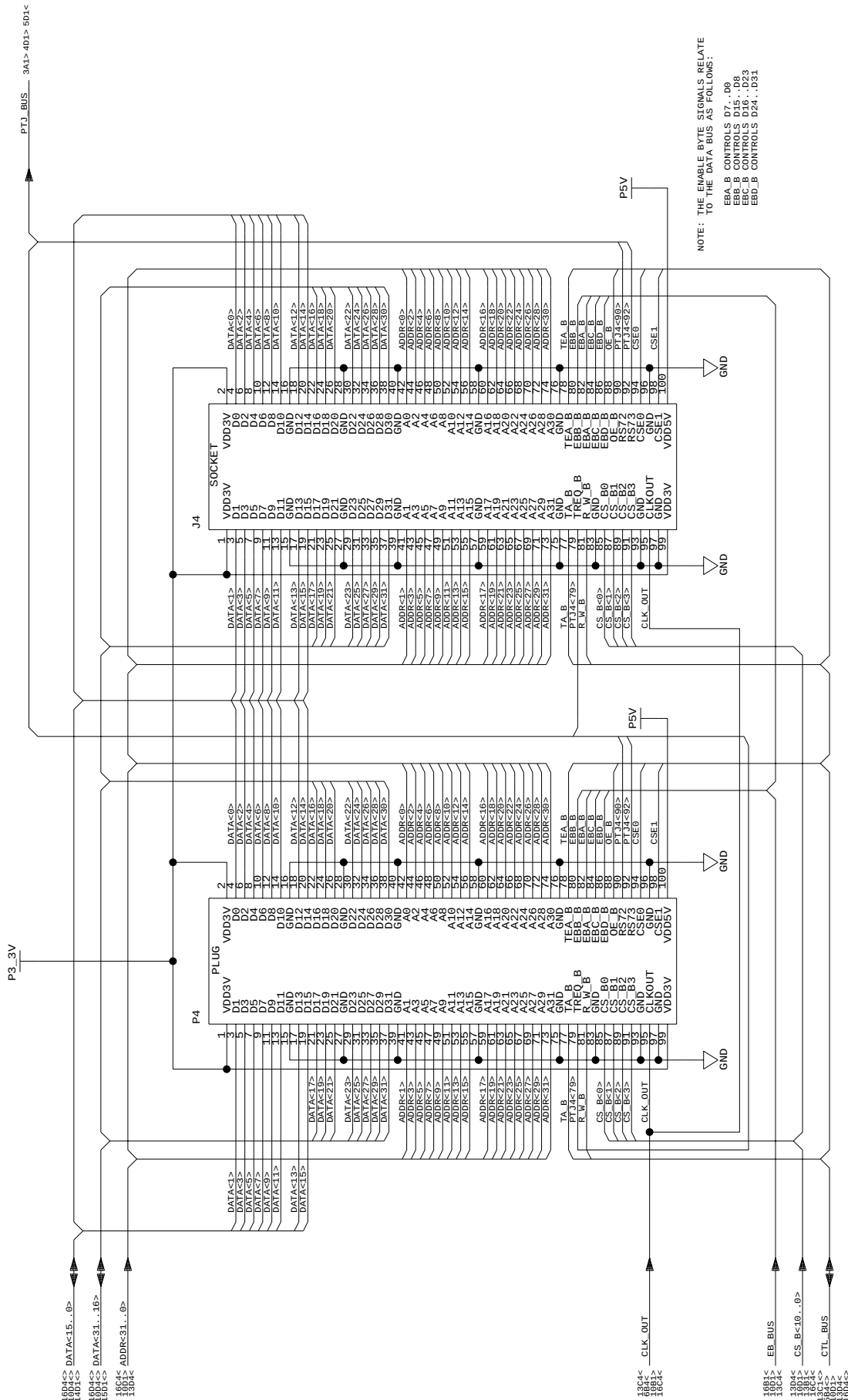


MAPI P3/J3

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LAST_MODIFIED=Tue Apr 18 09:21:12 2000

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NOTE: THE ENABLE BYTE SIGNALS RELATE TO THE DATA BUS AS FOLLOWS:

EBA_B CONTROLS D7..D0
EBB_B CONTROLS D15..D8
EBC_B CONTROLS D16..D23
EBD_B CONTROLS D24..D31

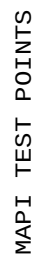
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LAST_MODIFIED= Tue Apr 18 09:21:13 2000 SHEET 6 OF 17

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SIZE	GEDTTL: SCH_1	DRAWING NO.	REV:
A	GEDABV: SCH_1	63ARE10031S	O

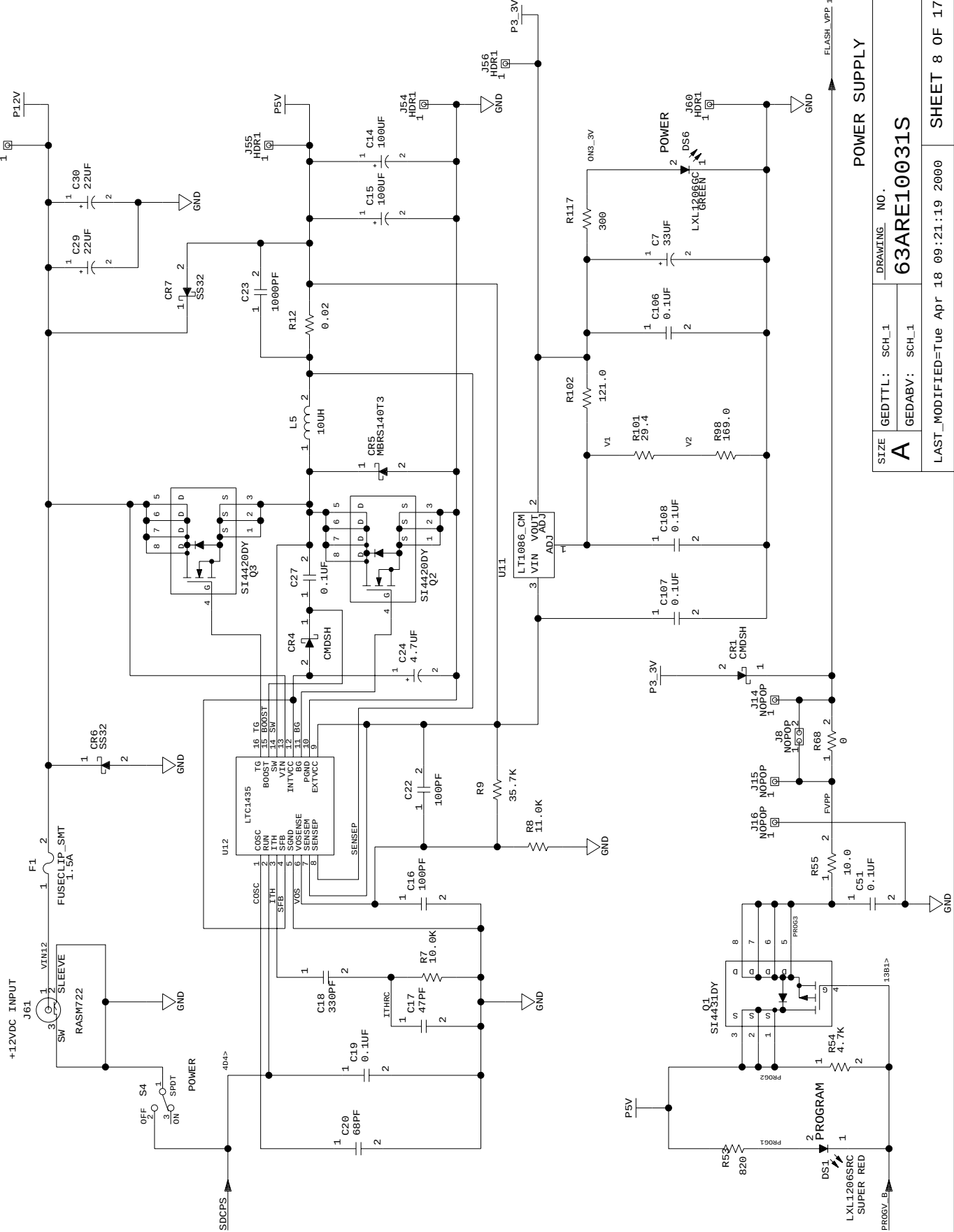
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DECOUPLING

SIZE	GEDTTL:	SCH_1	DRAWING NO.	REV:
A	GEDABV:	SCH_1	63ARE10031S	O

LAST_MODIFIED=Tue Apr 18 09:21:20 2000 SHEET 9 OF 17

