

**PRELIMINARY**  
Notice: This is not a final specification.  
Some parametric limits are subject to change.

MITSUBISHI Nch POWER MOSFET

# FY7BCH-02F

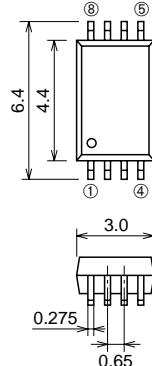
HIGH-SPEED SWITCHING USE

## FY7BCH-02F

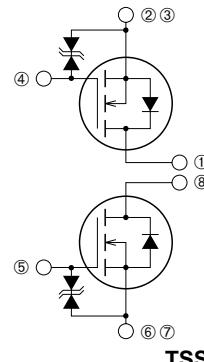
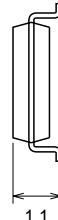


- 2.5V DRIVE
- V<sub>DSS</sub> ..... 20V
- r<sub>Ds</sub> (ON) (MAX) ..... 21mΩ
- I<sub>D</sub> ..... 7A

### OUTLINE DRAWING



Dimensions in mm



②③⑥⑦ SOURCE  
④⑤ GATE  
①⑧ DRAIN

TSSOP8

## APPLICATION

Li - ion battery protection

### MAXIMUM RATINGS ( $T_c = 25^\circ\text{C}$ )

Symbol	Parameter	Conditions	Ratings	Unit
V <sub>DSS</sub>	Drain-source voltage	V <sub>GS</sub> = 0V	20	V
V <sub>GSS</sub>	Gate-source voltage	V <sub>DS</sub> = 0V	±10	V
I <sub>D</sub>	Drain current		7	A
I <sub>DM</sub>	Drain current (Pulsed)		49	A
I <sub>DA</sub>	Avalanche current (Pulsed)	L = 10μH	7	A
I <sub>S</sub>	Source current		1.5	A
I <sub>SM</sub>	Source current (Pulsed)		6.0	A
P <sub>D</sub>	Maximum power dissipation		1.6	W
T <sub>ch</sub>	Channel temperature		-55 ~ +150	°C
T <sub>stg</sub>	Storage temperature		-55 ~ +150	°C
—	Weight	Typical value	0.035	g

Sep. 2000

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**ELECTRICAL CHARACTERISTICS** ( $T_{ch} = 25^{\circ}\text{C}$ )

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
V (BR) DSS	Drain-source breakdown voltage	$Id = 1\text{mA}$ , $V_{GS} = 0\text{V}$	20	—	—	V
V (BR) GSS	Gate-source breakdown voltage	$IG = \pm 100\mu\text{A}$ , $V_{DS} = 0\text{V}$	$\pm 10$	—	—	V
$I_{GSS}$	Gate-source leakage current	$V_{GS} = \pm 10\text{V}$ , $V_{DS} = 0\text{V}$	—	—	$\pm 10$	$\mu\text{A}$
$I_{DSS}$	Drain-source leakage current	$V_{DS} = 20\text{V}$ , $V_{GS} = 0\text{V}$	—	—	0.1	mA
$V_{GS(\text{th})}$	Gate-source threshold voltage	$Id = 1\text{mA}$ , $V_{DS} = 10\text{V}$	0.5	0.9	1.5	V
$r_{DS(\text{ON})}$	Drain-source on-state resistance	$Id = 7\text{A}$ , $V_{GS} = 4\text{V}$	—	17	21	$\text{m}\Omega$
$r_{DS(\text{ON})}$	Drain-source on-state resistance	$Id = 3.5\text{A}$ , $V_{GS} = 2.5\text{V}$	—	21	30	$\text{m}\Omega$
$V_{DS(\text{ON})}$	Drain-source on-state voltage	$Id = 7\text{A}$ , $V_{GS} = 4\text{V}$	—	0.119	0.147	V
$ y_{fs} $	Forward transfer admittance	$Id = 7\text{A}$ , $V_{DS} = 10\text{V}$	—	20	—	S
$C_{iss}$	Input capacitance	$V_{DS} = 10\text{V}$ , $V_{GS} = 0\text{V}$ , $f = 1\text{MHz}$	—	1350	—	pF
$C_{oss}$	Output capacitance		—	—	—	pF
$C_{rss}$	Reverse transfer capacitance		—	—	—	pF
$t_{d(\text{on})}$	Turn-on delay time	$V_{DD} = 10\text{V}$ , $Id = 3.5\text{A}$ , $V_{GS} = 4\text{V}$ , $R_{GEN} = R_{GS} = 50\Omega$	—	—	—	ns
$t_r$	Rise time		—	—	—	ns
$t_{d(\text{off})}$	Turn-off delay time		—	—	—	ns
$t_f$	Fall time		—	—	—	ns
$V_{SD}$	Source-drain voltage	$I_S = 1.5\text{A}$ , $V_{GS} = 0\text{V}$	—	0.85	1.1	V
$R_{th(\text{ch-a})}$	Thermal resistance	Channel to ambient	—	—	78.1	$^{\circ}\text{C}/\text{W}$
$t_{rr}$	Reverse recovery time	$I_S = 1.5\text{A}$ , $dI/dt = -50\text{A}/\mu\text{s}$	—	50	—	ns