

High Slew Rate, Wideband Inverting Amplifier

FEATURES

- HIGH SLEW RATE $\pm 320\text{V}/\mu\text{s}$
- FAST SETTLING TIME 550ns
- WIDE POWER BANDWIDTH 5MHz
- HIGH GAIN BANDWIDTH PRODUCT 70MHz
- LOW OFFSET VOLTAGE 0.8mV
- LOW POWER SUPPLY CURRENT 3.5mA

DESCRIPTION

HA-2530 and HA-2535 are monolithic high speed inverting amplifiers which deliver superior slew rate, bandwidth, and accuracy specifications over any other amplifier in its class. Designs of precision integrated amplifiers utilize the feedforward technique to produce excellent low power compensation at

APPLICATIONS

- PULSE AMPLIFIER
- SIGNAL CONDITIONING
- SIGNAL GENERATORS
- COAXIAL CABLE DRIVER
- INTEGRATORS

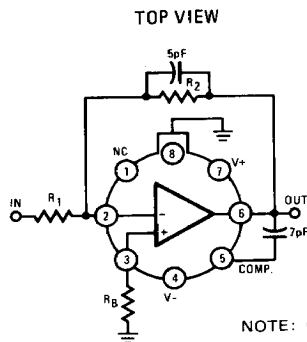
**Product to be Discontinued.
HA-2539 or HA-2540
Suggested for New Designs.**

These amplifiers are ideal components for pulse circuits, and high speed integrators that can take advantage of the $\pm 320\text{V}/\mu\text{s}$ slew rate and 550ns (0.1%) settling time. The 70MHz gain bandwidth product, 5MHz power bandwidth coupled with 0.8mV offset voltage and $\pm 50\text{mA}$ typical output current levels make these amplifiers ideally suited for signal conditioning, signal generation, and coaxial driver applications.

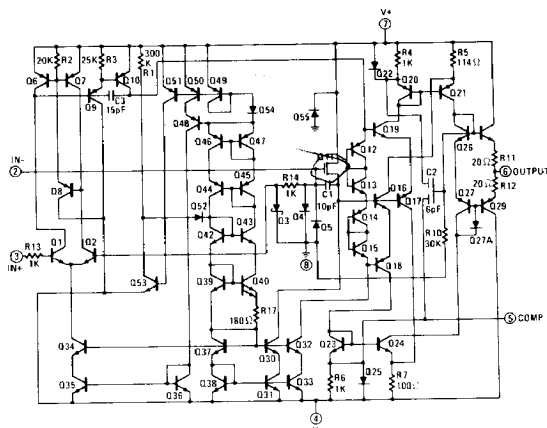
The HA-2530 and HA-2535 are available in metal can (TO-99) packages. HA-2530 is specified over the -55°C to $+125^\circ\text{C}$ range while HA-2535 is specified from 0°C to $+75^\circ\text{C}$.

PINOUT

Section 11 for Packaging



SCHEMATIC



SPECIFICATIONS

ABSOLUTE MAXIMUM RATINGS

Voltage Between V+ and V- Terminals	40V	Internal Power Dissipation (Note 1)	550mW	
Peak Output Current	±100mA	Operating Temperature Range	-55°C ≤ T _A ≤ +125°C	(HA-2530)
		Storage Temperature Range	0°C ≤ T _A ≤ +75°C	(HA-2535)
			-65°C ≤ T _A ≤ +150°C	

ELECTRICAL CHARACTERISTICS

Test Conditions: V_{Supply} = ±15.0V Unless Otherwise Specified.

PARAMETER	TEMP.	HA-2530 -55°C to +125°C			HA-2535 0°C to +75°C			UNITS
		LIMITS			LIMITS			
		MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	
INPUT CHARACTERISTICS								
Offset Voltage	+25°C Full		0.8	3		0.8	5	mV mV
Average Offset Voltage Drift	Full		5			5		μV/°C
Bias Current	+25°C Full		15	100		15	200	nA nA
Offset Current	+25°C Full		5	20		5	20	nA nA
Input Resistance	+25°C		2			2		MΩ
Input Capacitance	+25°C		10			10		pF
TRANSFER CHARACTERISTICS								
Large Signal Voltage Gain (Notes 2,5)	+25°C Full	10 ⁵	2X10 ⁶		10 ⁵	2X10 ⁶		V/V V/V
Common-Mode Rejection Ratio (Note 3)	Full	86	100		80	100		dB
Gain Bandwidth Product (Note 4)	+25°C		70			70		MHz
OUTPUT CHARACTERISTICS								
Output Voltage Swing (Note 2)	Full	±10	±12		±10	±12		V
Output Current (Note 5)	+25°C	±25	±50		±25	±50		mA
Full Power Bandwidth (Note 5)	+25°C	4	5		4	5		MHz
TRANSIENT RESPONSE (NOTES 6&7)								
Rise Time	+25°C		20	40		20	40	ns
Overshoot	+25°C		30	45		30	50	%
Slew Rate	+25°C	±280	±320		±250	±320		V/μs
Settling Time	+25°C		500			500		ns
POWER SUPPLY CHARACTERISTICS								
Supply Current	+25°C		3.5	8		3.5	8	mA
Power Supply Rejection Ratio (Note 8)	Full	86	100		80	100		dB

NOTES: 1. Derate at 5.5mW/°C for Operation at Ambient Temperature Above 75°C.

2. R_L = 2K

3. V_{CM} = ±5.0V

4. A_V > 10

5. V_O = ±10V

6. C_L = 50pF

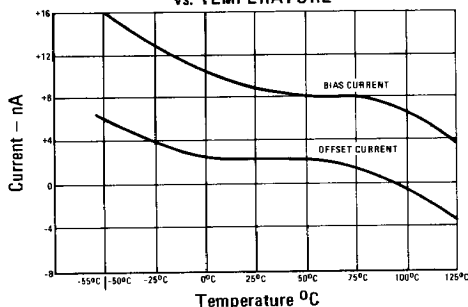
7. See Transient Response Test Circuit and Wave Forms

8. ΔV = ±5.0V

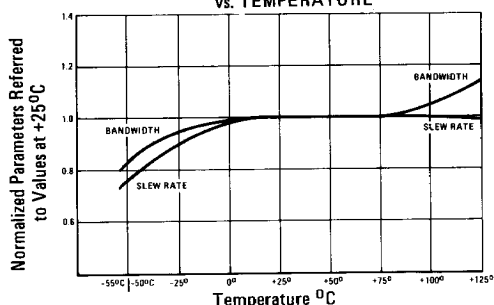
PERFORMANCE CURVES

$V^+ = 15\text{VDC}$, $V^- = 15\text{VDC}$, $T_A = 25^\circ\text{C}$ UNLESS OTHERWISE STATED

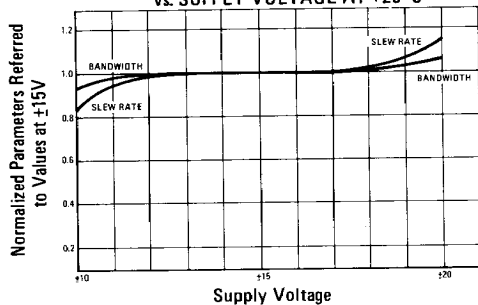
INPUT BIAS AND OFFSET CURRENT
vs. TEMPERATURE



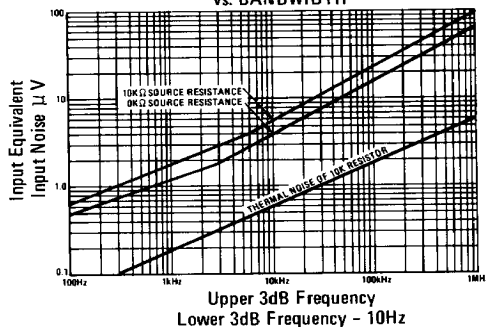
NORMALIZED AC PARAMETERS
vs. TEMPERATURE



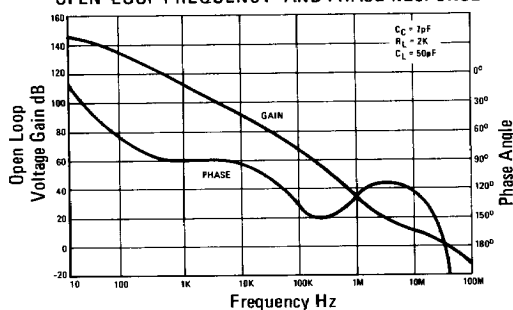
NORMALIZED AC PARAMETERS
vs. SUPPLY VOLTAGE AT $+25^\circ\text{C}$



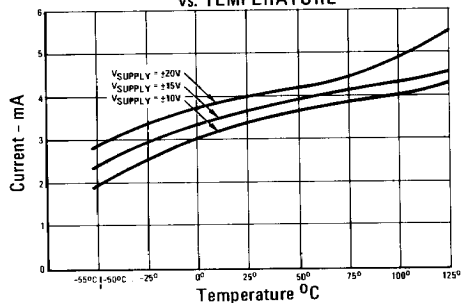
EQUIVALENT INPUT NOISE
vs. BANDWIDTH



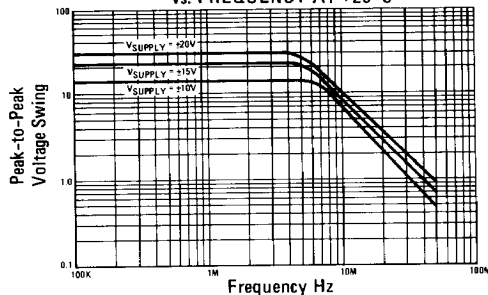
OPEN-LOOP FREQUENCY AND PHASE RESPONSE



POWER SUPPLY CURRENT
vs. TEMPERATURE

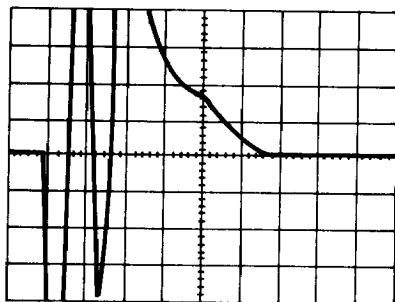


OUTPUT VOLTAGE SWING
vs. FREQUENCY AT $+25^\circ\text{C}$



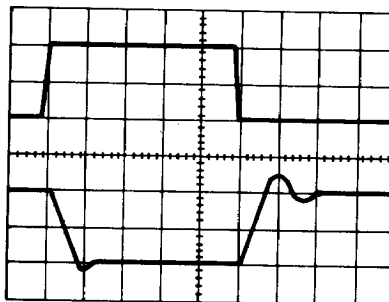
PERFORMANCE CURVES (continued)

SETTLING TIME MEASUREMENT *1



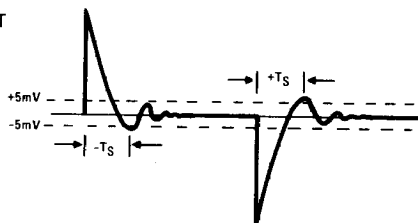
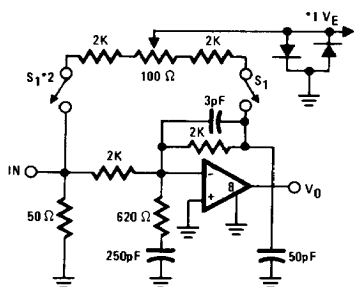
VERTICAL = 5mV/DIV.
HORIZONTAL = 100ns/DIV.
 $T_A = +25^\circ\text{C}$, $V_S = \pm 15\text{V}$

UNITY GAIN PULSE RESPONSE



UPPER TRACE: INPUT VERTICAL = 5V/DIV.
LOWER TRACE: OUTPUT HORIZONTAL = 50ns/DIV.
 $T_A = +25^\circ\text{C}$, $V_S = \pm 15\text{V}$

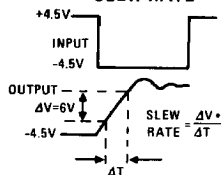
SLEW RATE/SETTLING TIME/TRANSIENT RESPONSE TEST CIRCUIT



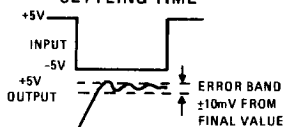
*1 Settling time (T_S) is measured using a high speed high recovery oscilloscope to display the error voltage V_E . When V_E is within $\pm 5\text{mV}$ of final value the output V_O will be within $\pm 10\text{mV}$ (0.1%).

*2 S_1 closed for settling time.

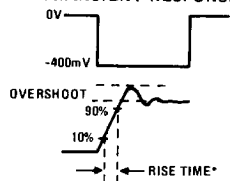
SLEW RATE



SETTLING TIME



TRANSIENT RESPONSE



* MEASURED ON BOTH POSITIVE AND NEGATIVE EXCURSIONS.

5MHz VIDEO AMPLIFIER ($A_V = 10$)

