

IQXO-53, -57

ISSUE 10; 17 JULY 1998

Delivery Options

- Common frequencies are available from stock. Please see p180 for details

Output Compatibility

- HCMOS/LS TTL
- Tri-state (IQXO-57, -57I)
- Non tri-state (IQXO-53, -53I)

Package Outline

- SMD (surface mount device) plastic encapsulated. Available over 0 to 70°C (IQXO-53, -57) or -40 to 85°C (IQXO-53I, -57I)

Standard Frequency Stabilities

- ±50ppm, ±100ppm (inclusive of supply voltage variations over the operating temperature range)

Operating Temperature Range

- 0 to 70°C (IQXO-53, -57)
- 40 to 85°C (IQXO-53I, -57I)

Storage Temperature Range

- 50 to 125°C

Non-Standard Duty Cycle

- Tighter duty cycles are available on request

Tri-state Operation (IQXO-57, -57I)

- Logic '1' to pin 1 enables oscillator output, 2.0V min
- Logic '0' to pin 1 disables oscillator output; when disabled the oscillator output goes to the high impedance state, 0.8V max
- No connection to pin 1 enables oscillator output
- When oscillator is enabled, maximum transition time = 100ns

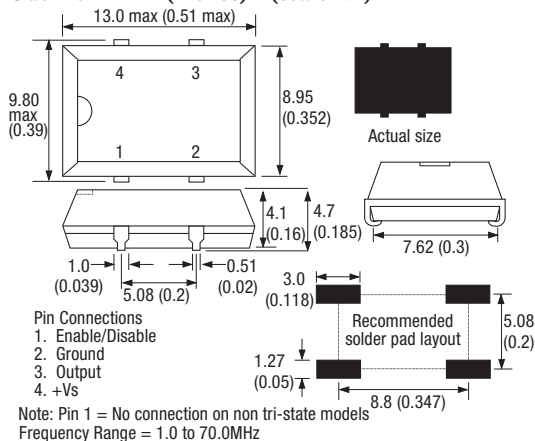
Marking

- Model number (+ Operating Temperature Code; if applicable)
- Frequency Stability Code
- Frequency
- Date Code

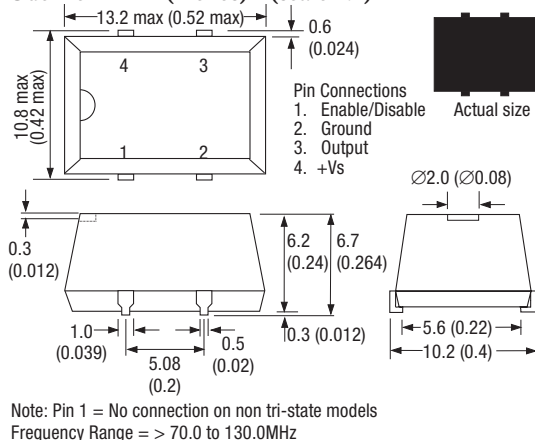
Minimum Order Information Required

- Frequency + Model Number + Operating Temperature Code (if applicable) + Frequency Stability

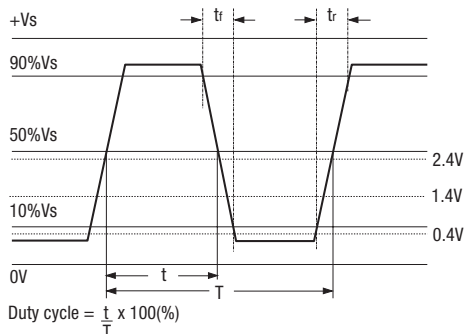
Outline in mm (inches) - (scale 2:1)



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Output Waveform - HCMOS/LS TTL



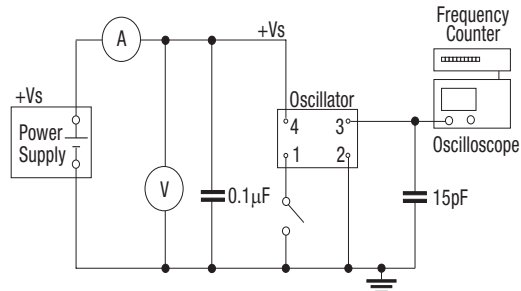
**SURFACE MOUNT
SPXOs**

Please note: TTL models (IQXO-52, -52I, -56, -56I) are available; contact our sales office for further details

24.0MHz IQXO-57I C

Frequency Stability: B = ± 50 ppm; C = ± 100 ppm

Test Circuit - HCMOS



Note: Pin 1 = No connection on non tri-state models

Outline in mm (inches) - Reel (scale 1:8)

