

Memory (monolithic ICs)

4 Mbit CMOS dynamic RAMs

Device	Package		Drawing number	Description	Features
	Type	Number of pins and configuration			
LC324100AJ ^V	SOJ	26	3145A	4 Mbit (4 194 304 × 1) CMOS dynamic RAM	60, 70, 80 ns access time, high-speed page mode
LC324100AZ ^V	ZIP	20	3144A	4 Mbit (4 194 304 × 1) CMOS dynamic RAM	60, 70, 80 ns access time, high-speed page mode
LC324100AJL ^V	SOJ	26	3145A	4 Mbit (4 194 304 × 1) CMOS dynamic RAM	200 µA (max) standby current, high-speed page mode
LC324100AZL ^V	ZIP	20	3144A	4 Mbit (4 194 304 × 1) CMOS dynamic RAM	200 µA (max) standby current, high-speed page mode
LC324400AJ ^V	SOJ	26	3145A	4 Mbit (1 048 576 × 4) CMOS dynamic RAM	60, 70, 80 ns access time, high-speed page mode
LC324400AZ ^V	ZIP	20	3144A	4 Mbit (1 048 576 × 4) CMOS dynamic RAM	60, 70, 80 ns access time, high-speed page mode
LC324400AJL ^V	SOJ	26	3145A	4 Mbit (1 048 576 × 4) CMOS dynamic RAM	200 µA (max) standby current, high-speed page mode
LC324400AZL ^V	ZIP	20	3144A	4 Mbit (1 048 576 × 4) CMOS dynamic RAM	200 µA (max) standby current, high-speed page mode
LC324800AJ ^V	SOJ	28	3145A	4 Mbit (524 288 × 8) CMOS dynamic RAM	60, 70, 80 ns access time, high-speed page mode
LC324800AZ ^V	ZIP	28	—	4 Mbit (524 288 × 8) CMOS dynamic RAM	60, 70, 80 ns access time, high-speed page mode
LC324800AJL ^V	SOJ	28	3145A	4 Mbit (524 288 × 8) CMOS dynamic RAM	200 µA (max) standby current, high-speed page mode
LC324800AZL ^V	ZIP	28	—	4 Mbit (524 288 × 8) CMOS dynamic RAM	200 µA (max) standby current, high-speed page mode

1 Mbit CMOS dynamic RAMs

Device	Package		Drawing number	Description	Features
	Type	Number of pins and configuration			
LC321000P	DIP	18W	3143A	1 Mbit (1 048 576 × 1) CMOS dynamic RAM	80, 100 ns access time, high-speed page mode
LC321000J	SOJ	26	3145A	1 Mbit (1 048 576 × 1) CMOS dynamic RAM	80, 100 ns access time, high-speed page mode
LC321000Z	ZIP	20	3144A	1 Mbit (1 048 576 × 1) CMOS dynamic RAM	80, 100 ns access time, high-speed page mode
LC321000PL	DIP	18W	3143A	1 Mbit (1 048 576 × 1) CMOS dynamic RAM	200 µA (max) standby current, high-speed page mode
LC321000JL	SOJ	26	3145A	1 Mbit (1 048 576 × 1) CMOS dynamic RAM	200 µA (max) standby current, high-speed page mode
LC321000ZL	ZIP	20	3144A	1 Mbit (1 048 576 × 1) CMOS dynamic RAM	200 µA (max) standby current, high-speed page mode
LC321000	DIP	18W	3143A	1 Mbit (1 048 576 × 1) CMOS dynamic RAM	100, 120 ns access time, high-speed page mode
LC321000AP*	DIP	18W	3143A	1 Mbit (1 048 576 × 1) CMOS dynamic RAM	60, 70, 80 ns access time, high-speed page mode
LC321000AJ*	SOJ	26	3145A	1 Mbit (1 048 576 × 1) CMOS dynamic RAM	60, 70, 80 ns access time, high-speed page mode
LC321000AZ*	ZIP	20	3144A	1 Mbit (1 048 576 × 1) CMOS dynamic RAM	60, 70, 80 ns access time, high-speed page mode
LC321000APL*	DIP	18W	3143A	1 Mbit (1 048 576 × 1) CMOS dynamic RAM	200 µA (max) standby current, high-speed page mode
LC321000AJL*	SOJ	26	3145A	1 Mbit (1 048 576 × 1) CMOS dynamic RAM	200 µA (max) standby current, high-speed page mode
LC321000AZL*	ZIP	20	3144A	1 Mbit (1 048 576 × 1) CMOS dynamic RAM	200 µA (max) standby current, high-speed page mode
LC321000BP ^V	DIP	18W	3143A	1 Mbit (1 048 576 × 1) CMOS dynamic RAM	60, 70, 80 ns access time, high-speed page mode
LC321000BJ ^V	SOJ	26	3145A	1 Mbit (1 048 576 × 1) CMOS dynamic RAM	60, 70, 80 ns access time, high-speed page mode
LC321000BZ ^V	ZIP	20	3144A	1 Mbit (1 048 576 × 1) CMOS dynamic RAM	60, 70, 80 ns access time, high-speed page mode
LC321000BPL ^V	DIP	18W	3143A	1 Mbit (1 048 576 × 1) CMOS dynamic RAM	200 µA (max) standby current, high-speed page mode
LC321000BJL ^V	SOJ	26	3145A	1 Mbit (1 048 576 × 1) CMOS dynamic RAM	200 µA (max) standby current, high-speed page mode
LC321000BZL ^V	ZIP	20	3144A	1 Mbit (1 048 576 × 1) CMOS dynamic RAM	200 µA (max) standby current, high-speed page mode
LC321664J ^V	SOJ	40	—	1 Mbit (65 536 × 16) CMOS dynamic RAM	70, 80, 100 ns access time, byte and word function
LC324256P	DIP	20W	3146	1 Mbit (262 144 × 4) CMOS dynamic RAM	80, 100 ns access time, high-speed page mode
LC324256J	SOJ	26	3145A	1 Mbit (262 144 × 4) CMOS dynamic RAM	80, 100 ns access time, high-speed page mode
LC324256Z	ZIP	20	3144A	1 Mbit (262 144 × 4) CMOS dynamic RAM	80, 100 ns access time, high-speed page mode
LC324256PL	DIP	20W	3146	1 Mbit (262 144 × 4) CMOS dynamic RAM	200 µA (max) standby current, high-speed page mode
LC324256JL	SOJ	26	3145A	1 Mbit (262 144 × 4) CMOS dynamic RAM	200 µA (max) standby current, high-speed page mode
LC324256ZL	ZIP	20	3144A	1 Mbit (262 144 × 4) CMOS dynamic RAM	200 µA (max) standby current, high-speed page mode
LC324256	DIP	20W	3146	1 Mbit (262 144 × 4) CMOS dynamic RAM	100, 120 ns access time, high-speed page mode
LC324256AP*	DIP	20W	3146	1 Mbit (262 144 × 4) CMOS dynamic RAM	60, 70, 80 ns access time, high-speed page mode
LC324256AJ*	SOJ	26	3145A	1 Mbit (262 144 × 4) CMOS dynamic RAM	60, 70, 80 ns access time, high-speed page mode
LC324256AZ*	ZIP	20	3144A	1 Mbit (262 144 × 4) CMOS dynamic RAM	60, 70, 80 ns access time, high-speed page mode
LC324256APL*	DIP	20W	3146	1 Mbit (32 768 × 4) CMOS dynamic RAM	200 µA (max) standby current, high-speed page mode
LC324256AJL*	SOJ	26	3145A	1 Mbit (32 768 × 4) CMOS dynamic RAM	200 µA (max) standby current, high-speed page mode
LC324256AZL*	ZIP	20	3144A	1 Mbit (32 768 × 4) CMOS dynamic RAM	200 µA (max) standby current, high-speed page mode
LC324256BP ^V	DIP	20W	3146	1 Mbit (32 768 × 4) CMOS dynamic RAM	60, 70, 80 ns access time, high-speed page mode
LC324256BJ ^V	SOJ	26	3145A	1 Mbit (32 768 × 4) CMOS dynamic RAM	60, 70, 80 ns access time, high-speed page mode
LC324256BZ ^V	ZIP	20	3144A	1 Mbit (32 768 × 4) CMOS dynamic RAM	60, 70, 80 ns access time, high-speed page mode
LC324256BPL ^V	DIP	20W	3146	1 Mbit (32 768 × 4) CMOS dynamic RAM	200 µA (max) standby current, high-speed page mode
LC324256BJL ^V	SOJ	26	3145A	1 Mbit (32 768 × 4) CMOS dynamic RAM	200 µA (max) standby current, high-speed page mode
LC324256BZL ^V	ZIP	20	3144A	1 Mbit (32 768 × 4) CMOS dynamic RAM	200 µA (max) standby current, high-speed page mode