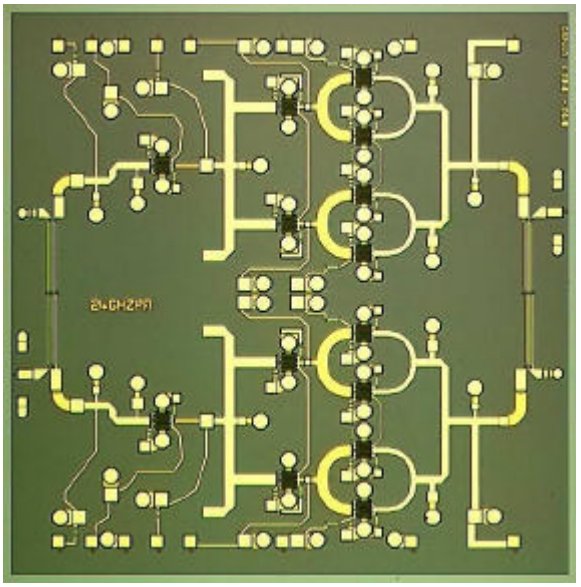
HEMT MMIC 1W POWER AMPLIFIER, 24GHz



Features

- Gain; 16dB typical @ 24GHz
- P-1dB; 30.5dBm typical @ 24GHz
- 0.20 ? m pHEMT technology

Description

The P35-5134-000-200 is a high performance 24GHz Gallium Arsenide power amplifier, capable of output powers in excess of 1 Watt. It is intended for use in point-to-point digital radio and point-to- multipoint communications.

The die is fabricated using Caswell Technology's 0.20um gate length, Power HEMT process (H40P) and is fully protected using Silicon Nitride passivation for excellent performance and reliability.

Electrical Performance

Ambient Temperature 22±3° C, Z_O = 50Ω, Vdd = 5V,Vg1 to Vg3 Set for Typical Drain Current. U.O.S

Parameter	Conditions	Min	Typ	Max	Units
Small Signal Gain	23 – 25GHz	-	16	-	dB
Gain Flatness	23 – 25GHz	-	± 0.5	± 1.0	dB
Input Return Loss	23 – 25GHz	-	15	-	dB
Output Return Loss	23 – 25GHz	-	15	-	dB
P-1dB Output Power	23 – 25GHz				
	Vdd=5V	-	30.5	-	dBm
Drain Voltage Vdd	-	-	5	7	Volts
Gate Voltage Vgg	-	-	-0.3	-	Volts
First Stage Current	Set Vg1	-	180	-	mA
Second Stage Current	Set Vg2	-	360	-	mA
Third Stage Current	Set Vg3	-	720	-	mA
Thermal Resistance	-	-	11	-	°C/W

Notes

1. All parameters measured on wafer

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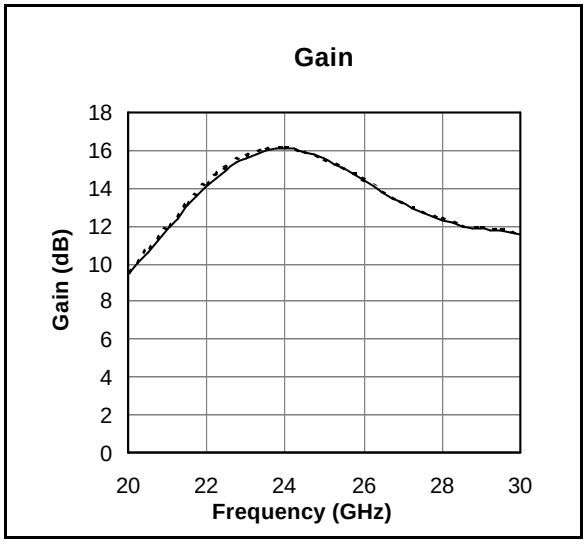
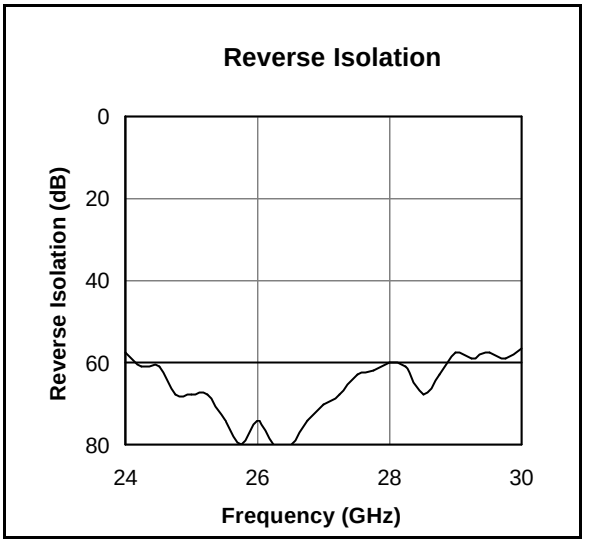
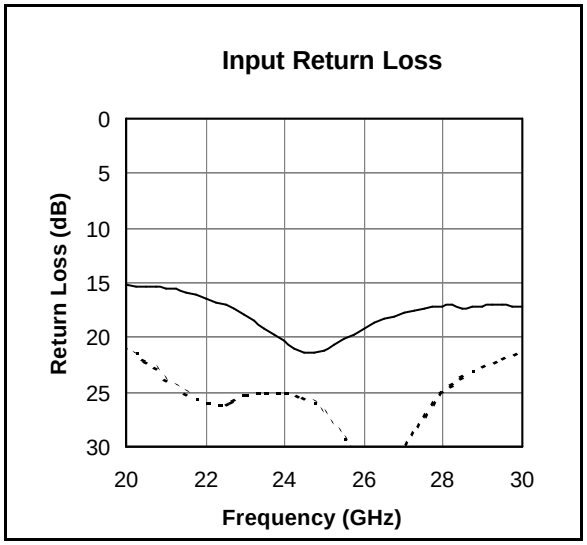
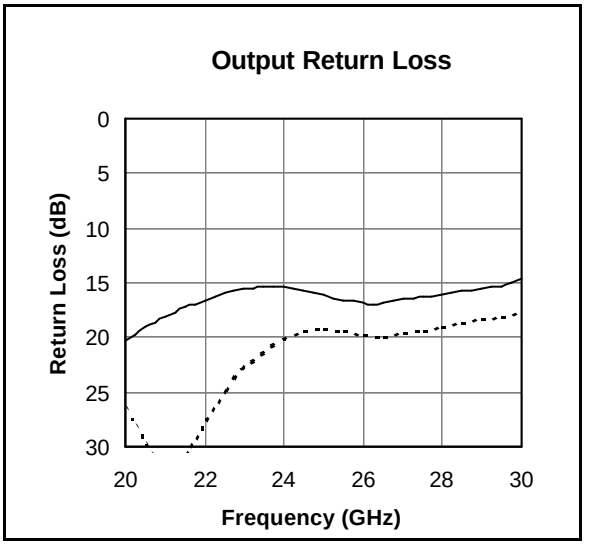
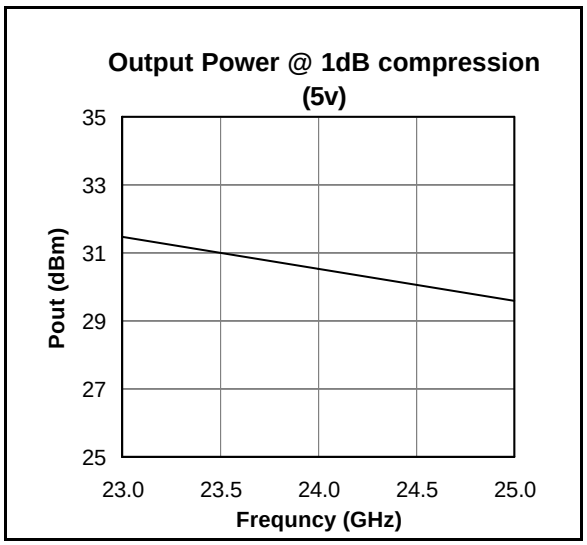
Typical RFOW Performance (----- With Bondwires)

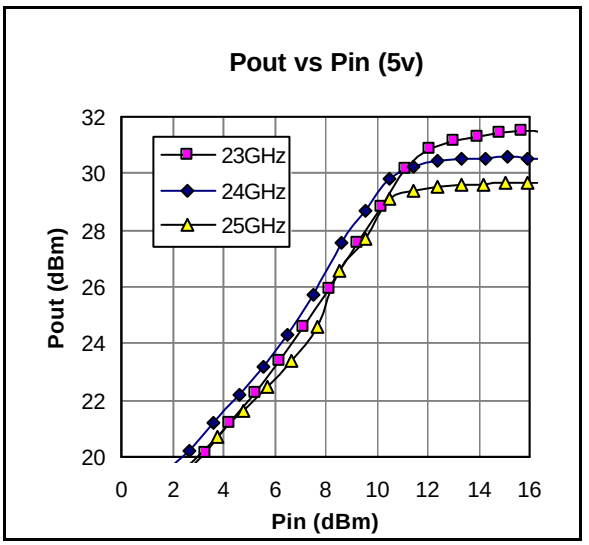












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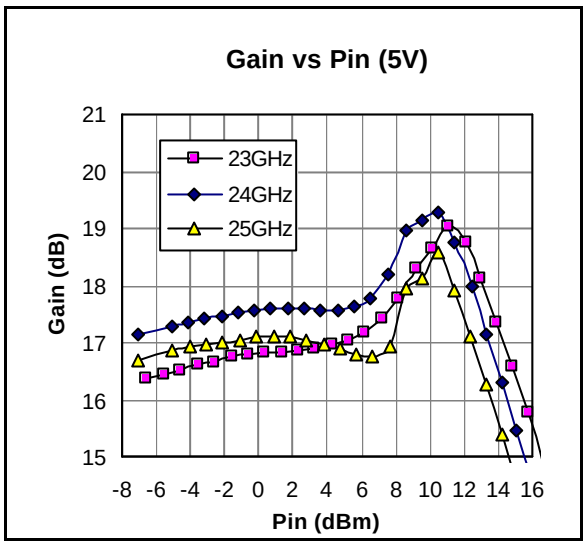
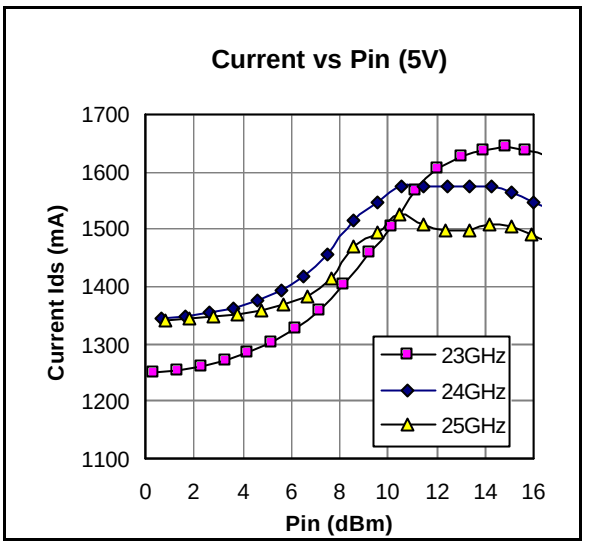
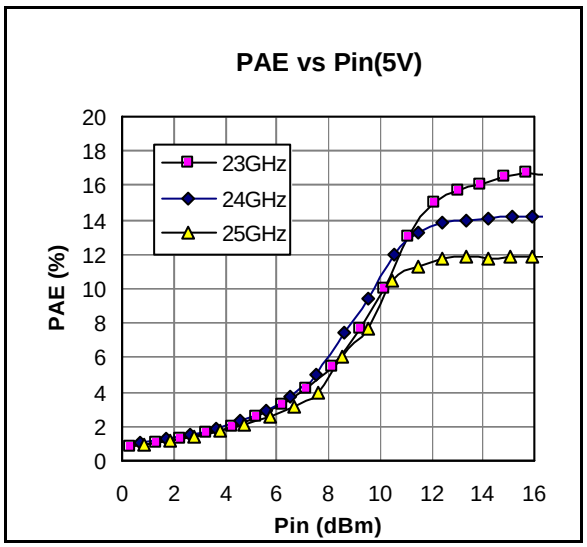
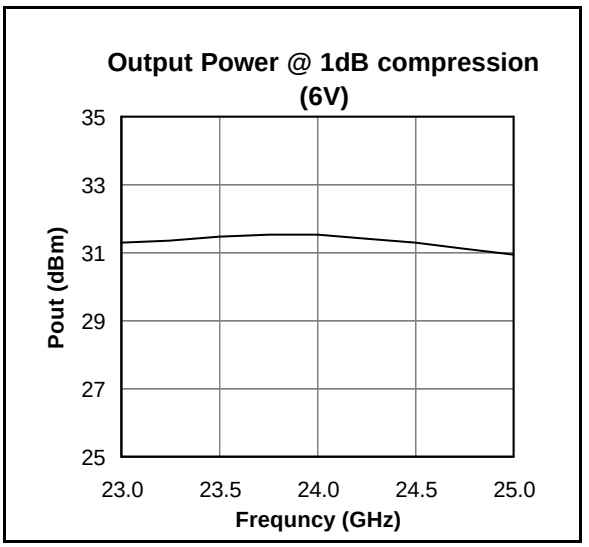
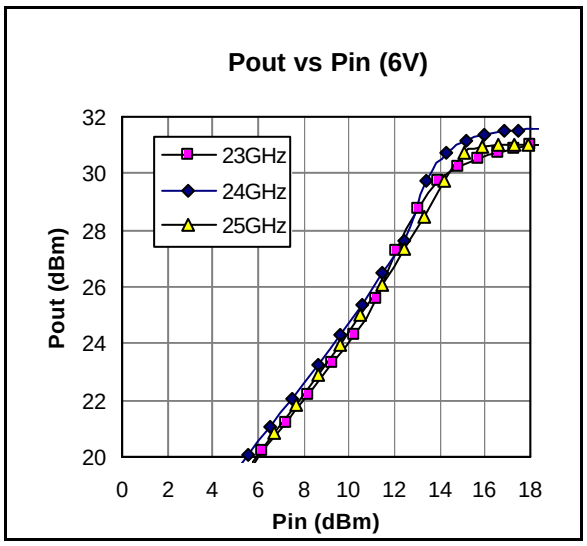
Typical RFOW Performance

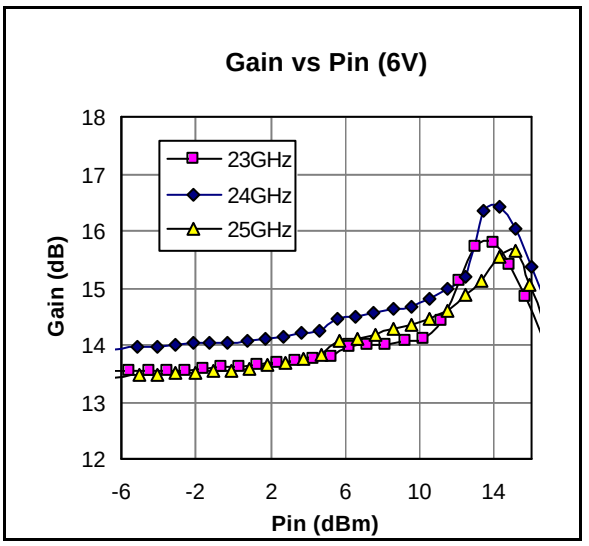












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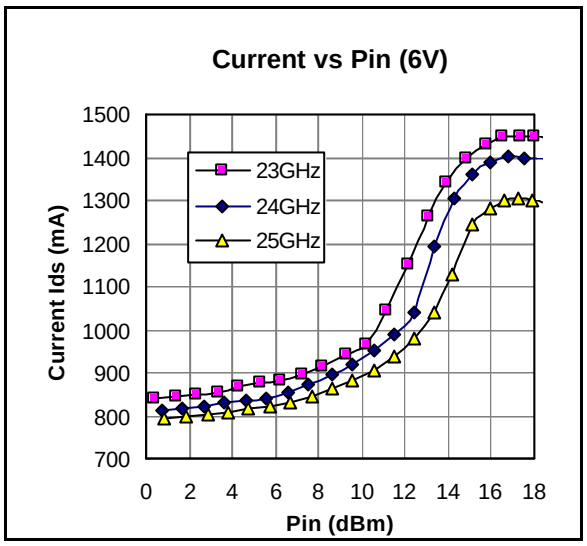
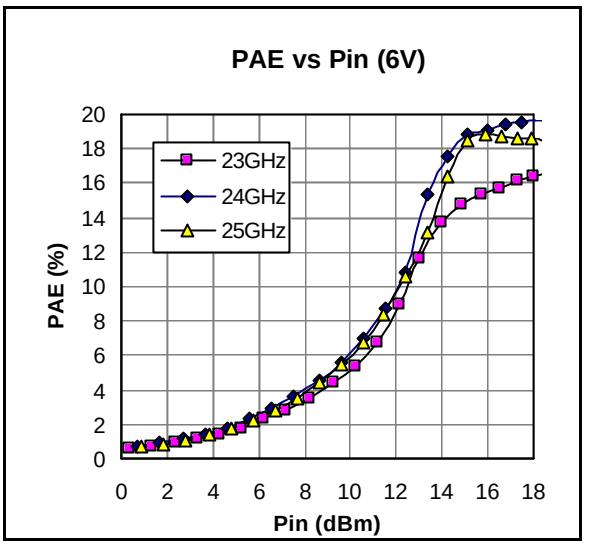
Typical RFOW Performance





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Typical S-parameters (RFOW)

Frequency	S11		S21		S12		S22	
(GHz)	Mag	Angle	Mag	Angle	Mag	Angle	Mag	Angle
20.00	0.17	-60.4	2.95	-51.9	0.002	-27.8	0.10	-57.0
20.25	0.17	-65.6	3.17	-63.4	0.002	-23.6	0.10	-63.0
20.50	0.17	-70.1	3.40	-75.2	0.002	-30.7	0.11	-67.5
20.75	0.17	-74.7	3.65	-87.4	0.002	-52.3	0.12	-71.9
21.00	0.17	-78.9	3.91	-100.0	0.002	-46.9	0.13	-76.0
21.25	0.17	-83.4	4.18	-112.9	0.002	-48.5	0.13	-80.1
21.50	0.16	-87.9	4.46	-126.2	0.001	-54.6	0.14	-83.9
21.75	0.16	-91.7	4.76	-139.9	0.002	-52.6	0.14	-87.3
22.00	0.15	-95.4	5.04	-154.1	0.001	-65.3	0.15	-90.8
22.25	0.15	-98.5	5.33	-168.7	0.001	-68.4	0.15	-94.0
22.50	0.14	-101.7	5.59	176.3	0.002	-89.2	0.16	-97.6
22.75	0.13	-104.9	5.83	161.2	0.001	-96.9	0.16	-101.4
23.00	0.13	-108.1	6.03	145.8	0.001	-98.3	0.17	-104.6
23.25	0.12	-109.7	6.19	130.2	0.001	-90.8	0.17	-107.8
23.50	0.11	-110.4	6.31	114.4	0.002	-106.4	0.17	-111.0
23.75	0.10	-110.3	6.38	98.6	0.002	-125.4	0.17	-114.2
24.00	0.10	-109.8	6.38	82.7	0.001	-144.1	0.17	-117.5
24.25	0.09	-107.3	6.34	66.9	0.001	-173.7	0.17	-120.4
24.50	0.08	-103.2	6.26	51.1	0.001	-173.6	0.16	-122.8
24.75	0.08	-98.4	6.13	35.6	0.000	164.2	0.16	-125.0
25.00	0.09	-93.9	5.97	20.3	0.000	142.7	0.16	-126.2
25.25	0.09	-91.1	5.81	5.3	0.000	156.8	0.15	-126.7
25.50	0.10	-89.7	5.63	-9.4	0.000	125.4	0.15	-126.7
25.75	0.10	-89.5	5.44	-23.9	0.000	-54.4	0.15	-126.4
26.00	0.11	-89.5	5.25	-38.1	0.000	15.7	0.14	-126.0
26.25	0.12	-90.2	5.06	-52.2	0.000	25.8	0.14	-125.2
26.50	0.12	-92.2	4.89	-66.0	0.000	-135.5	0.14	-124.3
26.75	0.12	-93.9	4.72	-79.8	0.000	-60.3	0.15	-124.2
27.00	0.13	-95.6	4.57	-93.5	0.000	-33.7	0.15	-124.5
27.25	0.13	-97.6	4.43	-107.1	0.000	-19.7	0.15	-124.4
27.50	0.13	-100.0	4.31	-120.6	0.001	-54.6	0.15	-124.4
27.75	0.14	-102.1	4.21	-134.1	0.001	-63.1	0.15	-124.6
28.00	0.14	-104.1	4.13	-147.8	0.001	-112.9	0.16	-124.8
28.25	0.14	-106.8	4.06	-161.9	0.001	-156.2	0.16	-125.1
28.50	0.14	-108.7	3.96	-176.1	0.000	178.4	0.16	-125.4
28.75	0.14	-109.0	3.93	170.2	0.001	-122.6	0.16	-125.7
29.00	0.14	-110.5	3.90	155.3	0.001	-144.3	0.17	-125.9
29.25	0.14	-112.0	3.88	140.1	0.001	-179.2	0.17	-125.9
29.50	0.14	-113.3	3.85	124.4	0.001	176.4	0.17	-125.8
29.75	0.14	-114.9	3.82	108.1	0.001	160.1	0.18	-125.4
30.00	0.14	-115.9	3.79	91.3	0.002	146.7	0.18	-125.8

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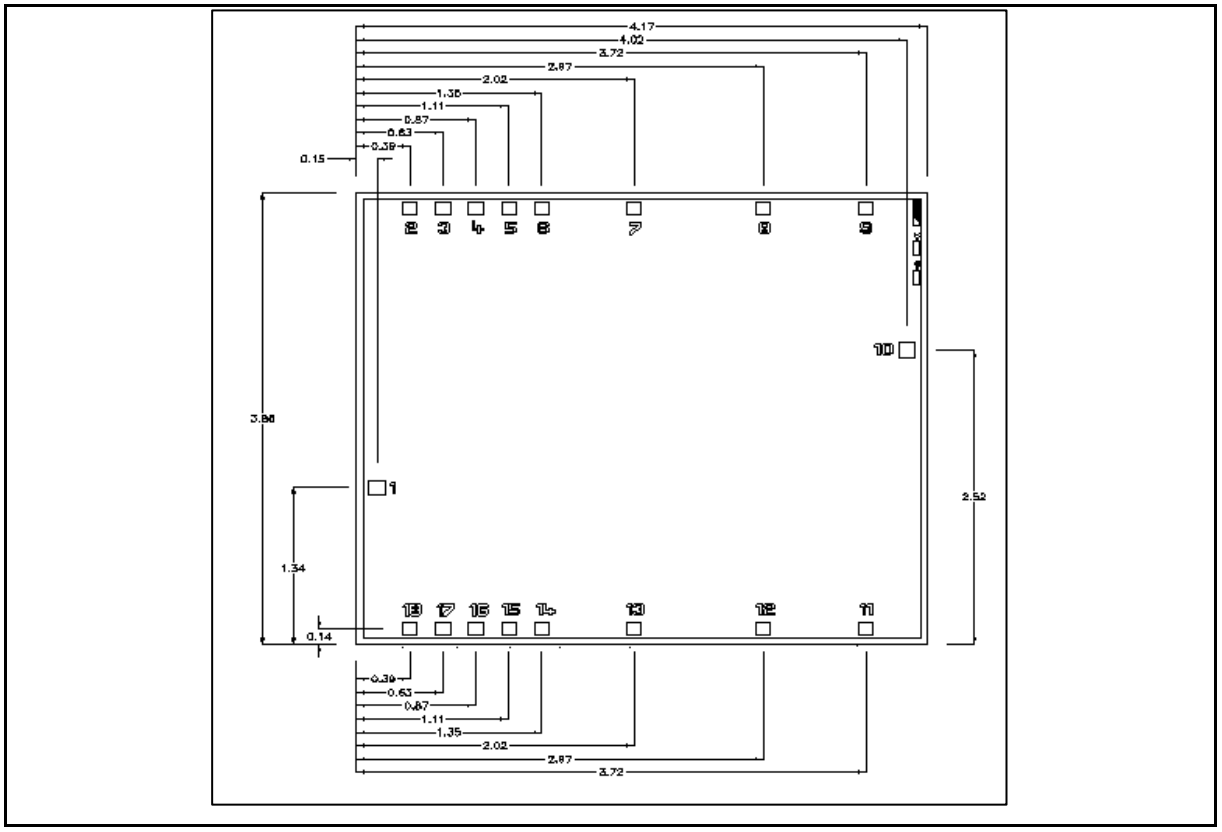
Chip Outline



Die size:	4.17 x 3.86mm
RF bond pads (1 & 10):	120 x 120µm
All other bond pads:	120µm x 120µm
Die Thickness:	100µm

Pad Details

Pad	Function
1	RF Input
2	Vg1
3	Vd1
4	Gnd
5	Vg2
6	Vd2
7	Vg3
8	Gnd
9	Vd3
10	RF Output
11	Vd3
12	Gnd
13	Vg3
14	Vd2
15	Vg1
16	Gnd
17	Vd1
18	Vg1

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Handling and Assembly Information

Gallium Arsenide (GaAs) devices are susceptible to electrostatic and mechanical damage. Dice are supplied in antistatic containers, which should be opened in cleanroom conditions at an appropriately grounded anti-static workstation. Devices need careful handling using correctly designed collets, vacuum pickups or, with care, sharp tweezers.

GaAs Products from Caswell Technology's H40P Foundry process are 100µm thick and have through GaAs vias to enable grounding to the circuit. Windows in the surface passivation above the bond pads are provided to allow wire bonding to the die.

The surface to which the die are to be attached should be cleaned with a proprietary de-greasing cleaner.

Eutectic mounting should be used and entails the use of a gold-tin (AuSn) preform, approximately 0.001″ thick, placed between the die and the attachment surface. The preferred method of mounting is the use of a machine such as a Mullins 8-140 die bonder. This utilises a heated collet and workstation with a facility for applying a scrubbing action to ensure total wetting and avoid the formation of voids. Dry nitrogen gas is directed across the work piece.

The gold-tin eutectic (80% Au 20% Sn) has a melting point of approximately 280°C (Note: Gold Germanium with a higher melting temperature should be avoided, in particular for MMICs). The work station temperature should be 310°C ± 10°C. The collet should be heated, and the die pre-heated to avoid excessive thermal shock. The strength of the bonding formed by this method will result in fracture of the die, rather than the bond under die strength testing.

The P35-5134-000-200 amplifier die has gold bond pads. The recommended wire bonding procedure uses 25µm (0.001") 99.99% pure gold wire with 0.5-2% elongation. Thermo-compression wedge bonding is preferred though thermosonic wire bonding may be used providing the ultrasonic content of the bond is minimised. A work station temperature of 260°C ± 10°C with a wedge tip temperature of 120°C ± 10°C is recommended. The wedge force should be 45 ± 5 grams. Bonds should be made from the bond pads on the die to the package or substrate.

The RF bond pads at the input and output are 120µm x 120µm; all other bond pads are 120µm x 120µm.

The P35-5134-000-200 has been designed to include the inductance of two 0.2mm length, 25µm bondwires at both the input and output, facilitating the integration of the die into a 50Ω environment.

Operating and Biasing of the P35-5134-000-200

The P35-5134-000-200 is a three-stage amplifier. The drain bias for all three stages (Vd1, Vd2 & Vd3) should be set to 5.0 volts. The gate voltages (Vg1, Vg2 & Vg3) are typically set to -0.3V. The separate drain and gate voltage supplies for all three stages can be combined into single supplies (Vdd & Vgg). DC bias supplies should be decoupled to ground using 100pF chip capacitors placed close to the chip with short bondwires to the amplifier bond pad, larger 0.01uF capacitors should be used to decouple the supplies further, as indicated on the bonding diagram.

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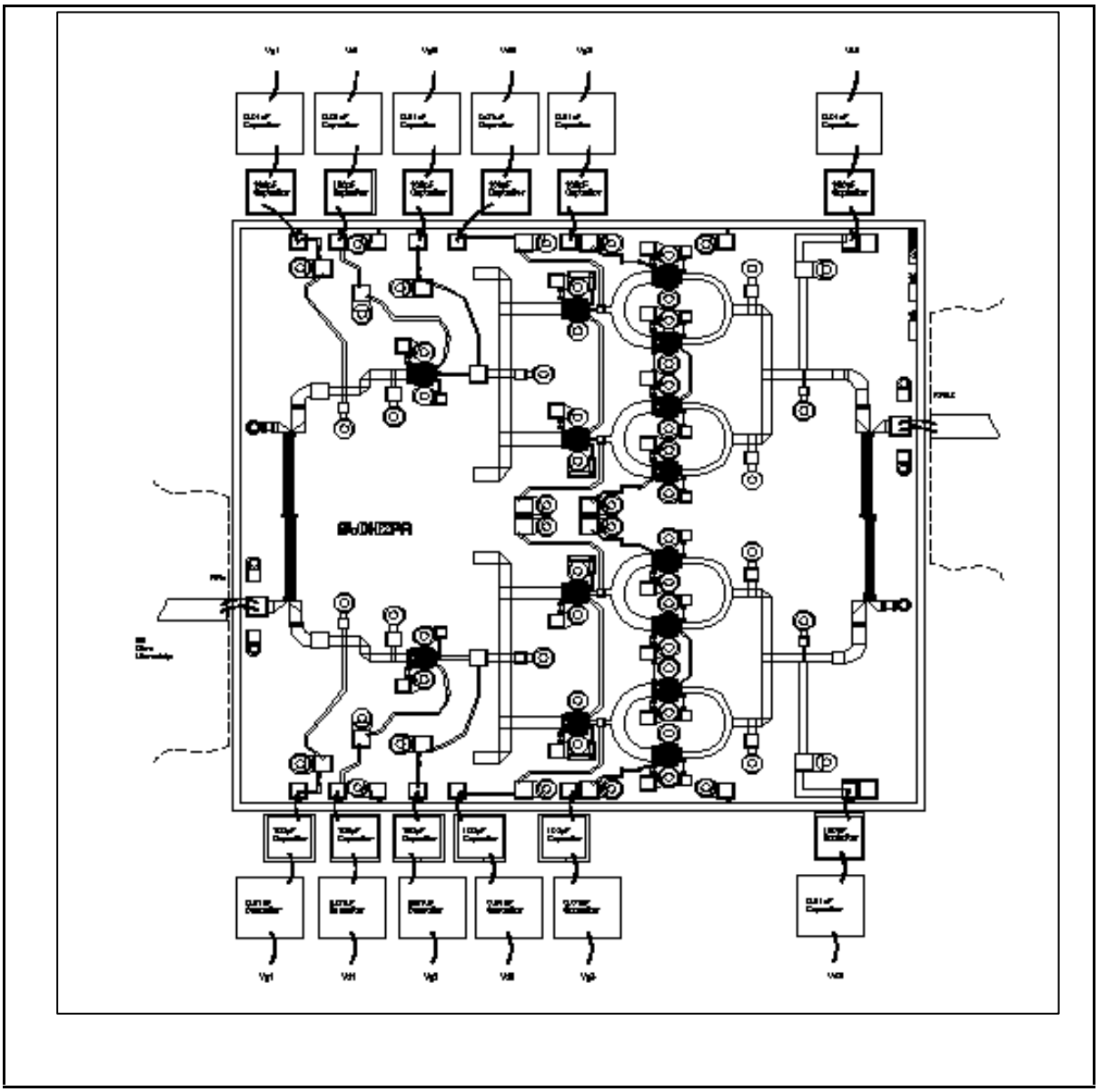
Typical bonding detail



Absolute maximum Ratings

Max Vdd	+7V
Max Vgg	-2V
Max channel temperature	150°C
Storage temperature	-65°C to +150°C

Ordering Information

P35-5134-000-200

This is a pre production specification, sample parts available 1999 Quarter 3.
Prototype samples are devices which have determined to be a potential product but which have not completed all pre-production and production release requirements.
Samples can be supplied for customer evaluation purposes only but are not stocked. Further devices may not be the same.

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