

T-47-13

T-47-17

# 10K ECL Delay Modules

Part No.	Tap Delays (ns)					Output Rise Time
	T <sub>01</sub>	T <sub>02</sub>	T <sub>03</sub>	T <sub>04</sub>	T <sub>05</sub>	
ECLDL025	5	10	15	20	25	4
ECLDL050	10	20	30	40	50	4
ECLDL075	15	30	45	60	75	5
ECLDL100	20	40	60	80	100	6
ECLDL125	25	50	75	100	125	7
ECLDL150	30	60	90	120	150	7
ECLDL200	40	80	120	160	200	8
ECLDL250	50	100	150	200	250	10
ECLDL300	60	120	180	240	300	15
ECLDL350	70	140	210	280	350	15
ECLDL400	80	160	240	320	400	15
ECLDL450	90	180	270	350	450	20
ECLDL500	100	200	300	400	500	20

Delay Tolerance  $\pm 1.5$  ns or 5% whichever is greater.

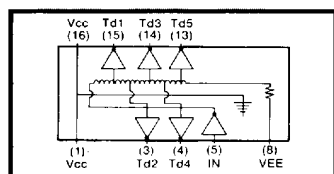
Delay Characteristics @  $V_{ee} = -5.2 \pm .01$  Vdc,  $T_a = 25^\circ\text{C}$ .

Delay time measured @  $-1.3$  V.

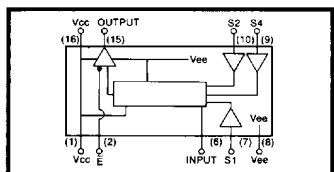
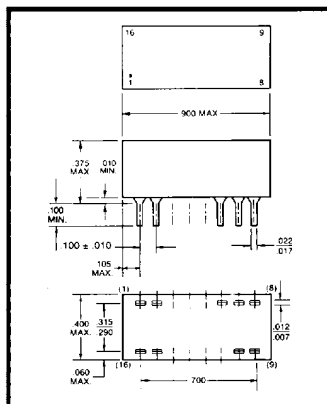
500 linear FPM airflow and output terminated with 50 ohm to  $-2.0$  Vdc.

Rise time measured @ 20 to 80% level of output pulse.

For minimum input pulse width—contact factory.



Dimensions applicable to all modules on this page.



Part No.	Step Delay ns $\pm$ ns	Max. Delay ns $\pm$ ns	Output Rise Time Max. (ns)
ECLPG301	1.0 $\pm$ .4	10.0 $\pm$ 1.5	3.6
ECLPG302	2.0 $\pm$ .6	17.0 $\pm$ 1.5	3.6
ECLPG303	3.0 $\pm$ 1.0	24.0 $\pm$ 1.5	3.6
ECLPG304	4.0 $\pm$ 1.0	31.0 $\pm$ 2.0	3.6
ECLPG305	5.0 $\pm$ 1.5	38.0 $\pm$ 2.0	3.6
ECLPG306	6.0 $\pm$ 1.5	45.0 $\pm$ 2.5	3.6
ECLPG307	7.0 $\pm$ 1.5	52.0 $\pm$ 2.5	3.6
ECLPG308	8.0 $\pm$ 1.5	59.0 $\pm$ 3.0	3.6
ECLPG309	9.0 $\pm$ 1.5	66.0 $\pm$ 3.5	3.6
ECLPG310	10.0 $\pm$ 1.5	73.0 $\pm$ 4.0	3.6
ECLPG315	15.0 $\pm$ 1.5	108.0 $\pm$ 5.0	5.0
ECLPG320	20.0 $\pm$ 1.5	143.0 $\pm$ 7.0	5.0
ECLPG325	25.0 $\pm$ 1.5	178.0 $\pm$ 9.0	5.0
ECLPG330	30.0 $\pm$ 2.0	213.0 $\pm$ 11.0	5.0
ECLPG335	35.0 $\pm$ 2.0	248.0 $\pm$ 12.0	5.0
ECLPG340	40.0 $\pm$ 2.5	283.0 $\pm$ 14.0	5.0
ECLPG345	45.0 $\pm$ 3.0	318.0 $\pm$ 16.0	5.0
ECLPG350	50.0 $\pm$ 3.5	353.0 $\pm$ 17.0	5.0

Delay Characteristics measured at  $V_{ee} = -5.2 \pm .01$  Vdc,  $T_a = 25^\circ\text{C}$ .

Delay time measured @  $-1.3$  V level.

500 linear FPM airflow and output terminated with 50 ohm to  $-2.0$  Vdc.

Rise time measured @ 20 to 80% level of output pulse.

Input is internally terminated, therefore the delay line driver does not require a pull-down resistor. For minimum input pulse width—contact factory.

## 10k ECL Delay Modules

- 10k ECL buffered input and output in 16-pin transfer-molded DIP.
- 5-tap line provides delays from 5 ns to 500 ns.
- Temperature range  $-30$  to  $+85^\circ\text{C}$ .
- Transfer molded—reliable.
- Fanout: 83 ECL loads.
- Military models with temperature range  $-55$  to  $+125^\circ\text{C}$  and ceramic package IC. Add suffix "M" to part number.
- Military models as above, but with ceramic package IC screened to Mil-Std 883C. Add suffix "MX" to part number.
- Military models as "MX" above, but with in-house burn-in and thermal shock, add suffix "MY".

## 10k ECL Programmable Delay Modules

- 10k ECL input and output.
- 3-bit binary gives 7 equal step delays.
- Available in 18 step delays from 1 ns to 50 ns.
- Low inherent delay ( $T_0$ );  $T_0 = 3.0 \pm 1.5$  ns.
- Temperature range  $-30$  to  $+85^\circ\text{C}$ .
- Fanout: 70 ECL loads.
- Military models with temperature range  $-55$  to  $+125^\circ\text{C}$  and ceramic package IC. Add suffix "M" to part number.
- Military models as above, but with ceramic package IC screened to Mil-Std 883C. Add suffix "MX" to part number.
- Military models as "MX" above, but with in-house burn-in and thermal shock, add suffix "MY".

### Notes

Only the pins specified in the schematics are provided with each package.

Pin numbers shown are for reference only and are not necessarily marked on unit.

Lead material is electro tin plated (alloy 42) or solder dipped.

All specifications are subject to change without notice.

**Technitrol®**

1952 East Allegheny Avenue, Philadelphia, PA 19134 USA

215-426-9105 • Fax 215-426-2836