PC924X

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*OPIC Photocoupler for IGBT Drive of Inverter

** Lead forming type (I type) and taping reel type (P type) are also available. (**PC924XI/PC924XP**) *** TÜV (VDE0884) approved type is also available as an option.

■ Features

- 1. Built-in direct drive circuit for IGBT drive (I_{O1P}, I_{O2P}:0.4A)
- 2. High speed response (t_{PLH}, t_{PHL}:MAX. 2.0µs)
- 3. Wide operating supply voltage range (V_{CC}:15 to 30V at Ta=-10 to 60°C)
- 4. High noise resistance type

CM_H:MIN.-1.5kV/µs

CM_I:MIN.1.5kV/us

5. High isolation voltage (V_{iso (rms)}:5.0kV)

■ Applications

1. IGBT drive for inverter control

■ Absolute Maximum Ratings

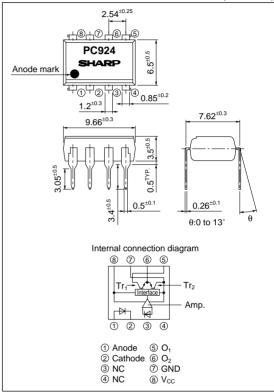
(T_a=T_{opr} unless otherwise specified)

(u op. 1)						
Parameter		Symbol	Rating	Unit		
Input	Forward current	I_{F}	25	mA		
	*1Reverse voltage	V_R	6	V		
ıt	Supply voltage	V _{CC}	35	V		
	O ₁ output current	I _{O1}	0.1	A		
	*2O1 peak output current	I _{O1P}	0.4	A		
Output	O ₂ output current	I_{O2}	0.1	A		
0	*2O2 peak output current	I _{O2P}	0.4	A		
	O ₁ output voltage	V _{O1}	35	V		
	Power dissipation	Po	500	mW		
Total power dissipation		P _{tot}	550	mW		
*3 Isolation voltage		V _{iso (rms)}	5.0	kV		
Operating temperature		Topr	-25 to +80	°C		
Storage temperature		T _{stg}	-55 to +125	°C		
*4Soldering temperature		T _{sol}	260	°C		

^{*1} T_o=25°C

■ Outline Dimensions





 ^{* &}quot;OPIC"(Optical IC) is a trademark of the SHARP Corporation.
 An OPIC consists of a light-detecting element and signal-processing circuit integrated onto a single chip.

^{*2} Pulse width≤0.15µs, Duty ratio:0.01

^{*3 40} to 60% RH, AC for 1minute, Ta=25°C

^{*4} For 10s

■ Electro-optical Characteristics

	■ Electro-optical Characteristics			$(T_a=T_{opr} \text{ unless otherwise specified})$				
		Parameter	Symbol	*5Conditions	MIN.	TYP.	MAX.	Unit
Input		Forward voltage		T _a =25°C, I _F =20mA	_	1.2	1.4	V
				T _a =25°C, I _F =0.2mA	0.6	0.9	_	V
	Reverse current		I_R	T _a =25°C, V _R =4V	-	_	10	μΑ
		Terminal capacitance		T _a =25°C, V=0, f=1kHz	-	30	250	pF
		Operating supply voltage		$T_a = -10 \text{ to } 60^{\circ}\text{C}$	15	_	30	V
Output				_	15	-	24	V
	*6	*6 O ₁ low level output voltage		V _{CC1} =12V, V _{CC2} =-12V	1	0.2	0.4	V
				$I_{O1}=0.1A, I_{F}=10mA$				
	*7	*7 O ₂ high level output voltage V_{O2H} $V_{CC}=V_{O1}=24$ V, $I_{O2}=-0.1$ A, $I_{F}=10$ mA		18	21	-	V	
	*8 O ₂ low level output voltage V_{O2L} V_{CC} =24V, I_{O2} =0.1A, I_F =		$V_{CC}=24V$, $I_{O2}=0.1A$, $I_{F}=0$	ı	1.2	2.0	V	
	*9 O1 leak current		I _{O1L}	$T_a=25^{\circ}C, V_{CC}=V_{O1}=35V, I_F=0$	ı	_	500	μΑ
	*10 O ₂ leak current		I_{O2L}	$T_a=25^{\circ}C, V_{CC}=V_{O2}=35V, I_F=10mA$	ı	_	500	μΑ
	*11	*11 High level supply current		$T_a=25$ °C, $V_{CC}=24$ V, $I_F=10$ mA	-	6	10	mA
				V_{CC} =24V, I_F =10mA	-	_	14	mA
	*11	*11 Low level supply current		$T_a=25^{\circ}C, V_{CC}=24V, I_F=0$	ı	8	13	mA
				$V_{CC}=24V, I_{F}=0$	ı	_	17	mA
	*12	*12 "Low→High" threshold input current		$T_a=25^{\circ}C, V_{CC}=24V$	1.0	4.0	7.0	mA
				V _{CC} =24V	0.6	-	10.0	mA
ics		Isolation resistance	R _{ISO}	T _a =25°C, DC=500V, 40 to 60%RH	5×10 ¹⁰	10^{11}	_	Ω
rist	me	ध ^{*13} "Low→High" propagation delay time			1	1.0	2.0	μs
acte	se ti	*13 "High→Low" propagation delay time	t _{PHL}	$T_a=25^{\circ}C, V_{CC}=24V, I_F=10mA$	-	1.0	2.0	μs
har		*13 Rise time	t _r	$R_{C}=47\Omega$, $C_{G}=3~000pF$	-	0.2	0.5	μs
Fransfer characteristics	Res	*13 Fall time t _f		-	0.2	0.5	μs	
nsf	*14	*14 Instantaneous common mode rejection voltage "Output:High level"		$T_a=25$ °C, $V_{CM}=600V(peak)$	-1.5		_	1-37/
Tra				$I_F=10mA, V_{CC}=24V, \Delta V_{O2H}=2.0V$	-1.5	_		kV/μs
	*14	*14 Instantaneous common mode rejection voltage "Output:Low level"		T _a =25°C, V _{CM} =600V (peak)	1.5			1-37/110
				$I_F=0, V_{CC}=24V, \Delta V_{O2L}=2.0V$	1.5	_		kV/μs

^{*5} When measuring output and transfer characteristics, connect a by-pass capacitor (0.01 μ F or more) between V_{CC} and GND near the device

■ Truth Table

Input	O ₂ Output	Tr.1	Tr.2
ON	High level	ON	OFF
OFF	Low level	OFF	ON

^{*6} Refer to Fig.1

^{*7} Refer to Fig.2 *8 Refer to Fig.3 *9 Refer to Fig.4

^{*10} Refer to Fig.5

^{*11} Refer to Fig.6 *12 I_{FLH} represents forward current when output goes from "Low" to "High", Refer to Fig.7

^{*13} Refer to Fig.8

^{*14} Refer to Fig.9

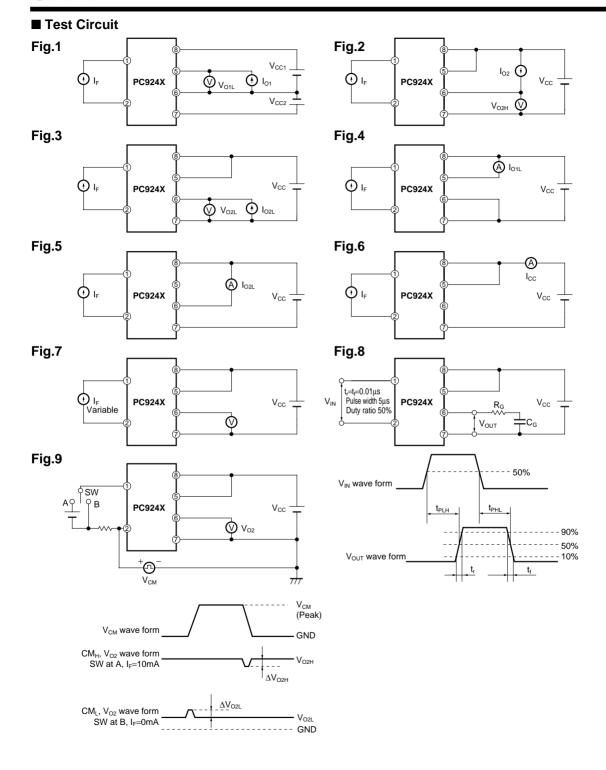


Fig.10 Forward Current vs. Ambient Temperature

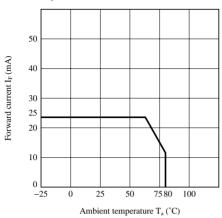


Fig.12 Forward Current vs. Forward Voltage

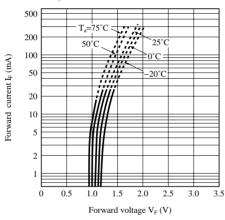


Fig.14 Relative Threshold Input Current vs.
Ambient Temperature

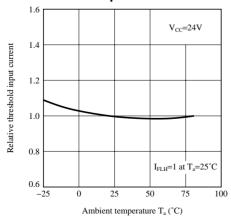


Fig.11 Power Dissipation vs. Ambient Temperature

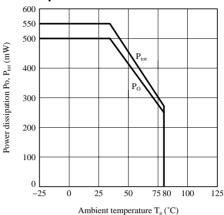


Fig.13 Relative Threshold Input Current vs. Supply Voltage

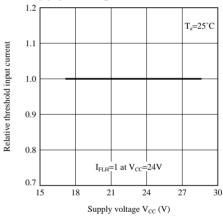


Fig.15 O₁ Low Level Output Voltage vs. O₁ Output Current

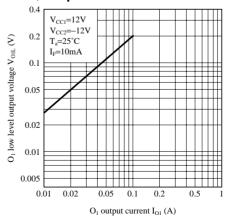


Fig.16 O₁ Low Level Output Voltage vs. Ambient Temperature

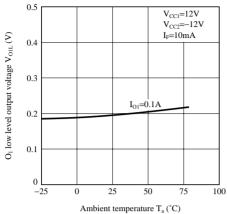


Fig.18 O₂ High Level Output Voltage vs. Ambient Temperature

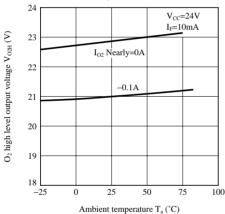


Fig.20 O₂ Low Level Output Voltage vs. Ambient Temperature

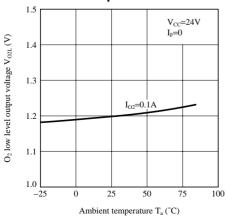


Fig.17 O₂ High Level Output Voltage vs. Supply Voltage

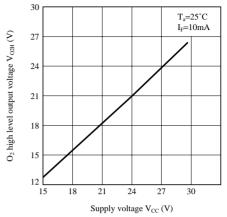


Fig.19 O₂ Low Level Output Voltage vs. O₂ Output Current

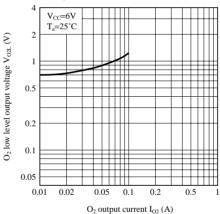


Fig.21 High Level Supply Current vs. Supply Voltage

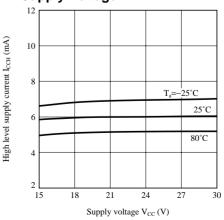


Fig.22 Low Level Supply Current vs. Supply Voltage

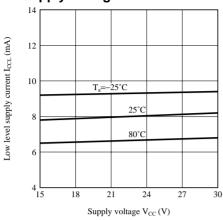


Fig.24 Propagation Delay Time vs. Ambient Temperature

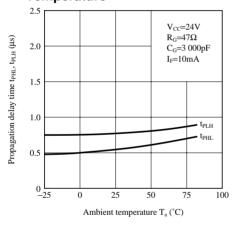
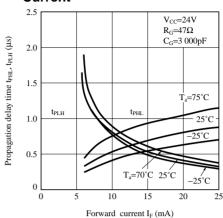
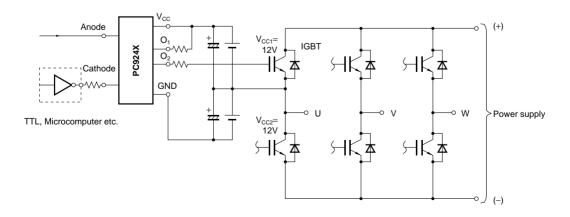


Fig.23 Propagation Delay Time vs. Forward Current



■ Application Circuit (IGBT Drive for Inverter)



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